



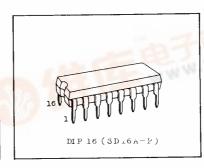
C2MOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC5002BP, TC5022BP BCD TO 7-SEGMENT DECODER/DRIVER

TC5002BP and TC5022BP are decoders to convert BCD code input to the driving signal for 7-segment display element and equipped with NPN transistors as the output buffers enabling direct driving of common cathode type LED.

When BI input is set at "H" level, all the segment outputs are turned "OFF" (not illumination) regardless of other inputs.

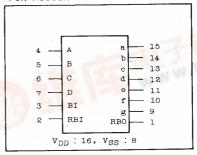
RBI input is to turn the output "OFF" and RBO input is to generate "H" level output only for "O" code input and these are used for leading zero suppress when connected in cascade.



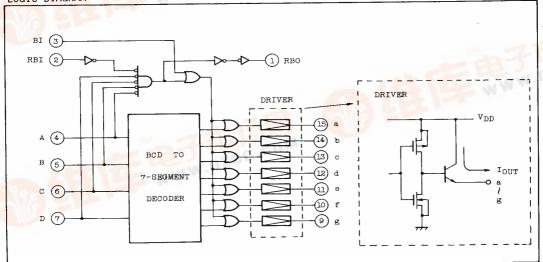
#### ABSOLUTE MAXIMUM RATINGS

CHARACTERISTIC	SYMBOL	RATING	UNIT
DC Supply Voltage	$v_{\mathrm{DD}}$	Vss-0.5~Vss+20	V
Input Voltage	VIN	$V_{SS}-0.5 \sim V_{DD}+0.5$	v
Output Voltage	VOUT	$V_{SS}-0.5 \sim V_{DD}+0.5$	V
DC Input Current	IIN	±10	mA
Power Dissipation	PD	300	mW
Storage Temperature Range	Tstg	-65~ 150	°c
Lead Temp./Time	T <sub>sol</sub>	260°C · 10sec	

PIN ASSIGNMENT

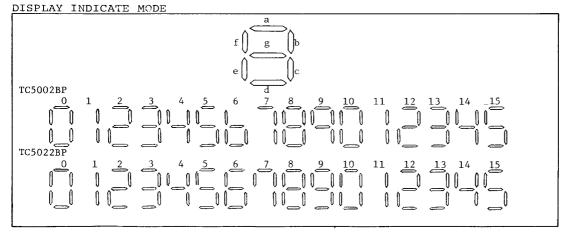


#### LOGIC DIAGRAM



#### TRUTH TABLE

(TC5	(TC5002BP)													
		INP	UT			OUTPUT								
BI	RBI	A	В	С	D	а	Ъ	С	d	e	f	g	RBO	NOTE
Н	*	*	*	*	*	L	L	L	L	L	L	L	☆	
L	Н	L	L	L	L	L	L	L	L	L	L	L_	Н	
L	L	L	L	L	L	Н	H	H	Н	H	H	L	L	
L	*	Н	L	L	L	L	Н	H	L	L	L	L	L	
L	*	L	Н	L	L	Н	Н	L	Н	Н	L	Н	L	
L	*	Н	Н	L	L	Н	Н	H	Н	L	L	Н	L	
L	*	L	L	Н	L	L	H	H	L	L	Н	Н	L	
L	*	Н	L	Н	L	Н	L	Н	H	L	H	Н	L	
L	*	L	Н	Н	L	L	L	Н	Н	Н	Н	Н	L	1
L	*	Н	Н	Н	L	Н	Н	H	L	L	L	L	L	2
L	*	L	·L	L	Н	Н	Н	Н	Н	Н	Н	Н	L	
L	*	Н	L	L	Н	Н	Н	H	L	L	Н	Н	L	3
L	*	L	H	L	Н	Н	Н	Н	H	Н	Н	L	L	
L	*	Н	Н	L	Н	L	Н	H	L	L	L	L	L	
L	*	L	L	H	Н	Н	Н	L	H	Н	L	Н	L	
L	*	Н	L	Н	Н	Н	Н	Н	Н	L	L	H	L	
L	*	L	Н	Н	Н	L	Н	Н	L	L	Н	H	L	
L	*	Н	Н	Н	Н	Н	L	Н	Н	L	Н	Н	L	



### RECOMMENDED OPERATING CONDITIONS (VSS= 0V)

1.20012					
CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Supply Voltage	V <sub>DD</sub>	3	-	18	v
Input Voltage	VIN	0	_	VDD	V
Operating Temp.	Topr	-40	-	85	°C

ELECTRICAL CHARACTERISTICS (VS	'SS=0V)
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		TEST	$v_{\mathrm{DD}}$	-40	)°C		25°C		85	5°C	UNIT
CHARACTERISTIC	SYMBOL	CONDITIONS	(V)		MAX.	MIN.	TYP.	MAX.	MIN.	MAX.	ONLI
High Level Output Voltage (RBO)	v <sub>OH</sub>	I <sub>OUT</sub>   < 1μΑ   V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5 10 15	4.95 9.95 14.95	- - -		5.00 10.00 15.00		4.95 9.95 14.95		v
Low Level Output Voltage (RBO)	VOL	I <sub>OUT</sub>   < 1µA   V <sub>IN</sub> =VSS, V <sub>DD</sub>	5 10 15	1 1 1	0.05 0.05 0.05	- - -	0.00	0.05	- - -	0.05 0.05 0.05	
High Level Output Voltage (a - g)	v <sub>OH</sub>	ایرا> I <sub>OUT</sub> ا V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5 10 15	4.0 9.0 14.0	- - -	4.0 9.0 14.0	4.5 9.5 14.5	- - -	4.0 9.0 14.0	- -	V
High Level Output Current (RBO)	I <sub>OH</sub>	V <sub>OH</sub> =4.6V V <sub>OH</sub> =9.5V V <sub>OH</sub> =13.5V V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5 10 15	-0.2 -0.5 -1.4	<del>-</del> -	-0.16 -0.4 -1.2		-	-0.12 -0.3 -1.0	-	mA
Low Level Output Current (RBO)	<sup>I</sup> OL	V <sub>OL</sub> =0.4V V <sub>OL</sub> =0.5V V <sub>OL</sub> =1.5V V <sub>IN</sub> =V <sub>SS</sub> , V <sub>DD</sub>	5 10 15	0.52 1.3 3.6	- -	0.44 1.1 3.0		-	0.36 0.9 2.4	- - -	
High Level Output Current (a - g)	ІОН	VOH=3.5V VOH=8.5V VOH=13.5V VIN=VSS, VDD	5 10 15	-20 -25 -30	- -	-20 -25 -30		- - -	-15 -20 -25	-	mA
High Level Input Voltage	V <sub>IH</sub> **	V <sub>OUT</sub> =0.5V,4.0V V <sub>OUT</sub> =1.0V,9.0V V <sub>OUT</sub> =1.5V,13.5V 	5 10 15	3.5 7.0 11.0	-	3.5 7.0 11.0	2.75 5.5 8.25	-	3.5 7.0 11.0	-	v
Low Level Input Voltage	VIL **	VOUT=0.5V,4.0V VOUT=1.0V,9.0V VOUT=1.5V,13.5V + LOUT   < LAA	5 10 15		1.5 3.0 4.0	- - -	2.25 4.5 6.75	3.0	-	1.5 3.0 4.0	
Disable Current	IDL	V <sub>OL</sub> =0V	18	_	-3.0	L	-10-4		ļ	-30	μA
Input "H" Level Current "L" Level		V <sub>IH</sub> =18V V <sub>IL</sub> =0V	18 18	-	0.3 -0.3	-	10-5 -10-5	-0.3	_	1.0	μA
Quiescent Current Consumption	I <sub>DD</sub>	VIN=VSS, VDD	5 10 15	- - -	20 40 80		0.005 0.010 0.015	40	- -	150 300 600	μА

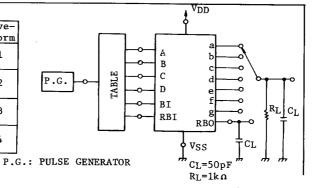
<sup>\*</sup> All valid input combinations. Outputs open.

<sup>\*\*</sup> Required pull down register RL = 20 k $\Omega$  (a  $\sim$  g outputs).

SWITCHING	CHARACTERISTICS	(Ta-259C	Vec-ou	CT _ EO _ E)
DMTICUTING	CHARACTERISTICS	Ta=Z5°C.	vss=0v.	~L=50 pF)

CHARACTERISTIC	SYMBOL	TEST		MIN.	TYP.	MAX.	UNIT	
	JIIDOI	CONDITIONS	$V_{DD}(V)$	min.	111.	rian.	J ONLI	
Output Rise Time (SEGMENT OUT)	tTLH	R <sub>L</sub> =1 kΩ	5 10 15	- - -	100 50 40	200 100 80		
Output Rise Time (RBO)	t <sub>TLH</sub>		5 10 15	- - -	130 65 50	400 200 160	ns	
Output Fall Time (RBO)	t <sub>THL</sub>		5 10 15	- - -	100 50 40	200 100 80		
(LOW-HIGH) Propagation Delay Time (A,B,C,D-SEGMENT OUT)	tpLH	R <sub>L</sub> =1 kΩ	5 10 15	- - -	500 150 120	1000 400 300	ns	
(HIGH-LOW) Propagation Delay Time (A,B,C,D-SEGMENT OUT)	tpHL	RL=1 kΩ	5 10 15	- - -	1000 450 320	2000 1000 700		
(LOW-HIGH) Propagation Delay Time (A,B,C,D - RBO)	tpLH		5 10 15	- - -	1000 370 250	2000 1000 750	ns	
(HIGH-LOW) Propagation Delay Time (A,B,C,D - RBO)	t <sub>P</sub> HL		5 10 15	- 4	500 200 140	1000 500 300	115	
(LOW-HIGH) Propagation Delay Time (RBI - RBO)	tpLH		5 10 15	-	800 270 190	1600 700 500		
(HIGH-LOW) Propagation Delay Time (RBI - RBO)	t <sub>p</sub> HL		5 10 15	- - -	180 70 50	700 350 250	ns	
Propagation Delay Time (BI - SEGMENT OUT)	t <sub>p</sub> LH t <sub>p</sub> HL	RL=1 kΩ	5 10 15	- - -	500 200 150	1500 600 500	ns	
Input Capacity	CIN			-	5	7.5	pF	

SWITCHING TIME TEST CIRCUIT TABLE (tpLH, tpHL Test Codition) Wave-P.G. "H" "L" OUTPUT TEST form A,B,C,D -SEGMENT OUT Other 1 Inputs A,B,C,D -Other Α RBI RBO 2 RBO Inputs Other RBI - RBO RBI RBO 3 Inputs Other 4 SEGMENT OUT Inputs



### SWITCHING TIME TEST WAVEFORMS

