

**TOSHIBA****TB62705CPG/CFG/CFNG**

TOSHIBA Bi-CMOS INTEGRATED CIRCUIT SILICON MONOLITHIC

**TB62705CPG, TB62705CFG, TB62705CFNG****8-BIT SHIFT REGISTER, LATCHES & CONSTANT-CURRENT DRIVERS**

The TB62705CPG / CFG / CFNG are specifically designed for LED and LED DISPLAY constant-current drivers.

These constant-current output circuits can support the set-up of an external resistor ( $I_{OUT} = 5\text{~}90\text{mA}$ ).

This IC is a monolithic integrated circuit designed to be used together with Bi-CMOS process.

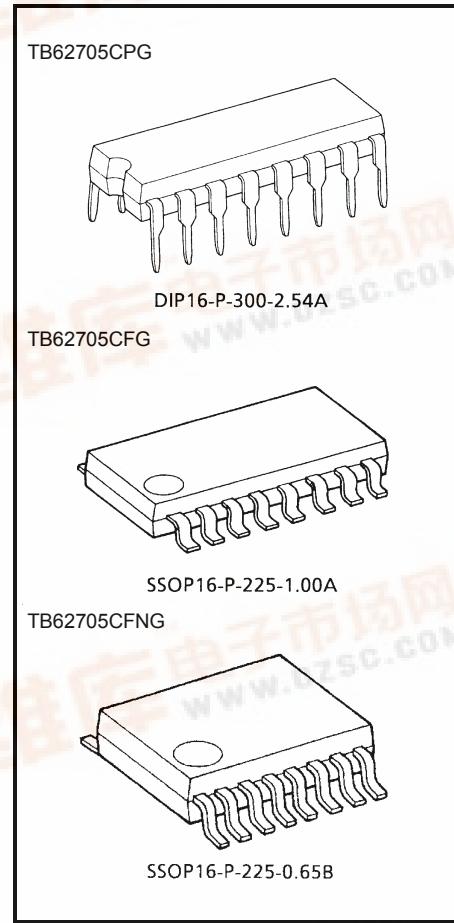
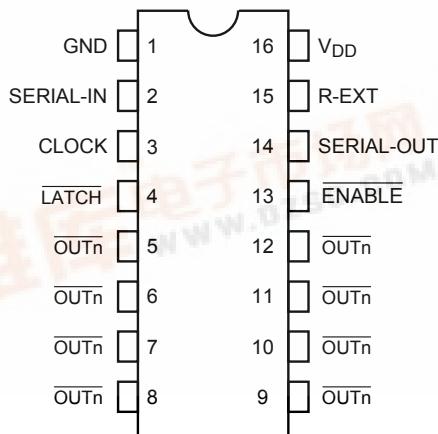
The devices consist of an 8-bit shift register, latch, AND-GATE and constant-current drivers.

This devices are a product for the Pb free(Sn-Ag).

**FEATURES**

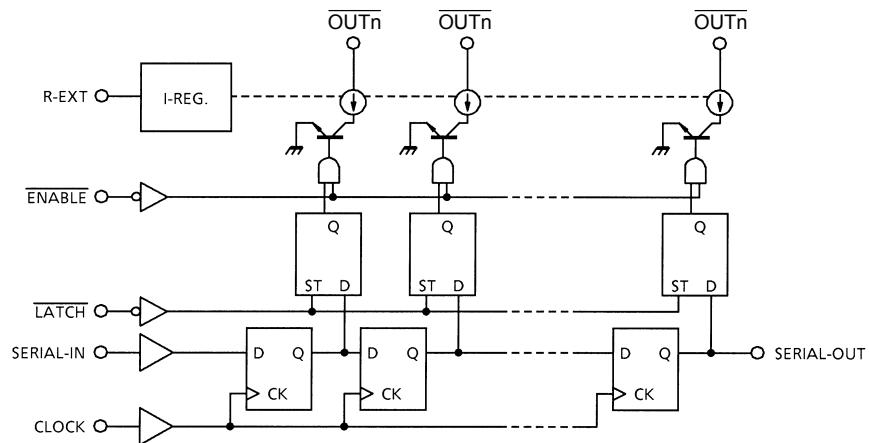
- Constant-current Output : current with one resistor for 5 to 90mA.
- Maximum Clock Frequency :  $f_{CLK} = 15$  (MHz)  
(Cascade Connect Operate,  $T_{OPR} = 25^\circ\text{C}$ )
- 5V C-MOS Compatible Input
- Package : DIP16-P-300-2.54A (TB62705CPG)  
SSOP16-P-225-1.00A (TB62705CFG)  
SSOP16-P-225-0.65B (TB62705CFNG)
- Constant Output Current Matching:

OUTPUT-GND VOLTAGE	CURRENT MATCHING	OUTPUT CURRENT
$\geq 0.4$ V	$\pm 6.0\%$	5~40 mA
$\geq 0.7$ V	$\pm 6.0\%$	5~90 mA

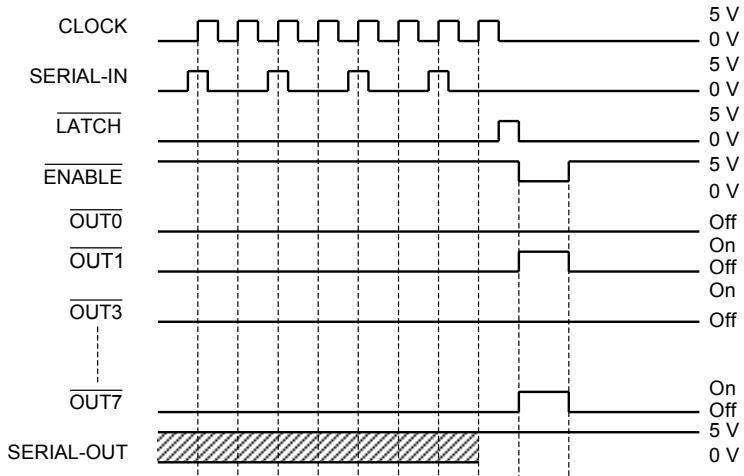
**PIN CONNECTION (Top view)**

Weight  
DIP16-P-300-2.54A : 1.11 g (typ.)  
SSOP16-P-225-1.00A : 0.14 g (typ.)  
SSOP16-P-225-0.65B : 0.07 g (typ.)

## BLOCK DIAGRAM



## TIMING DIAGRAM



Note: Latches are level-sensitive, not rising edge-sensitive, and are not synchronized with the CLOCK signal. The data will pass through the latch circuit if the latch input is set at "H" level, and will be retained if the input is set at "L".

## PIN DESCRIPTION

PIN No.	PIN NAME	FUNCTION
1	GND	GND terminal for control logic
2	SERIAL-IN	Input pin for shift register serial data
3	CLOCK	Clock input terminal for data shift to up-edge.
4	<u>LATCH</u>	Data strobe input terminal. Latches pass <u>LATCH</u> data with "H" level input and retain data with "L" level input.
5~12	<u>OUTn</u>	Output terminals
13	<u>ENABLE</u>	Input terminal for output enable. All outputs ( <u>OUTn</u> ) go off with <u>ENABLE</u> data input at "H" level and go on with data input at "L" level.
14	SERIAL-OUT	Output terminal for serial data for the next SERIAL-IN terminal.
15	R-EXT	Input terminal for connecting a resistor to regulate all output currents.
16	V <sub>DD</sub>	5-V supply pin of the IC

## TRUTH TABLE

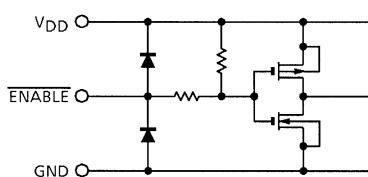
CLOCK	<u>LATCH</u>	<u>ENABLE</u>	SERIAL-IN	<u>OUTn</u>	SERIAL-OUT
UP	H	L	D <sub>n</sub>	D <sub>n</sub> … D <sub>n-5</sub> … D <sub>n-7</sub>	D <sub>n-7</sub>
UP	L	L	D <sub>n+1</sub>	No change	D <sub>n-6</sub>
UP	H	L	D <sub>n+2</sub>	D <sub>n+2</sub> … D <sub>n-3</sub> … D <sub>n-5</sub>	D <sub>n-5</sub>
DOWN	X	L	D <sub>n+3</sub>	D <sub>n+2</sub> … D <sub>n-3</sub> … D <sub>n-5</sub>	D <sub>n-5</sub>
DOWN	X	H	D <sub>n+3</sub>	Off	D <sub>n-5</sub>

Note:  $\overline{OUT_n}$  = on if  $D_n = H$  level, and  $\overline{OUT_n}$  = off if  $D_n = L$  level.

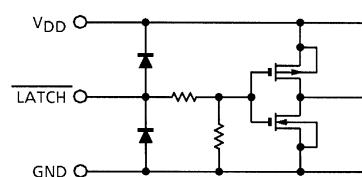
An external resistor is connected with R-EXT and GND. Be sure to administer the correct power supply voltage.

## INPUT/OUTPUT EQUIVALENT CIRCUITS

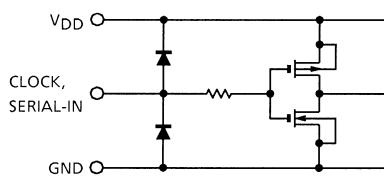
## 1. ENABLE terminal



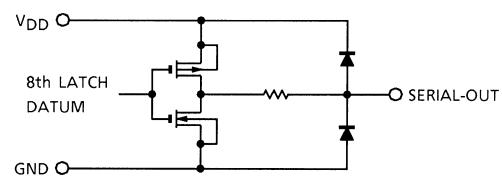
## 2. LATCH terminal



### 3. CLOCK, SERIAL-IN terminal



#### 4. SERIAL-OUT terminal



## MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V <sub>DD</sub>	0~7.0	V
Input Voltage	V <sub>IN</sub>	-0.4~V <sub>DD</sub> + 0.4	V
Output Current	I <sub>OUT</sub>	90	mA
Output Voltage	V <sub>CE</sub>	-0.5~17.0	V
Clock Frequency	f <sub>CK</sub>	15	MHz
GND Terminal Current	I <sub>GND</sub>	720	mA
Power Dissipation	P <sub>D</sub>	1.47 (CPG-type : FREE AIR, Ta = 25°C) 0.78 (CFG / CFNG-type : ON PCB, Ta = 25°C)	W
Thermal Resistance	R <sub>th</sub> (j-a)	85 (CPG-type : FREE AIR, Ta = 25°C) 160 (CFG / CFNG-type : ON PCB, Ta = 25°C)	°C / W
Operating Temperature	T <sub>opr</sub>	-40~85	°C
Storage Temperature	T <sub>stg</sub>	-55~150	°C

Note: CPG type: For an ambient temperature above 25°C, the derating is 11.8 mW/°C.

CFG and CFNG type: For an ambient temperature above 25°C, the derating is 6.3 mW/°C.

## RECOMMENDED OPERATING CONDITION (Ta = -40~85°C unless otherwise stated)

CHARACTERISTIC	SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Supply Voltage	V <sub>DD</sub>	—	4.5	5.0	5.5	V
Output Voltage	V <sub>OUT</sub>	—	—	—	15.0	V
Output Current	I <sub>O</sub>	OUT <sub>n</sub> , DC 1 circuit	5	—	88	mA
	I <sub>OH</sub>	SERIAL-OUT	—	—	1.0	
	I <sub>OL</sub>	SERIAL-OUT	—	—	-1.0	
Input Voltage	V <sub>IH</sub>	—	0.7 V <sub>DD</sub>	—	V <sub>DD</sub> +0.3	V
	V <sub>IL</sub>	—	-0.3	—	0.3 V <sub>DD</sub>	
LATCH Pulse Width	t <sub>w</sub> LAT	V <sub>DD</sub> = 4.5~5.5 V	100	—	—	ns
CLOCK Pulse Width	t <sub>w</sub> CLK		50	—	—	ns
ENABLE Pulse Width	t <sub>w</sub> EN		4500	—	—	ns
Set-up Time for DATA	t <sub>setup</sub> (D)		60	—	—	ns
Hold Time for DATA	t <sub>hold</sub> (D)		20	—	—	ns
Set-up Time for LATCH	t <sub>setup</sub> (L)		100	—	—	ns
Hold Time for LATCH	t <sub>hold</sub> (L)		60	—	—	ns
Clock Frequency	f <sub>CK</sub>	Cascade operation	10.0	—	—	MHz
Power Dissipation	P <sub>D</sub>	Ta = 85°C (CPG-type FREE AIR)	—	—	0.82	W
		Ta = 85°C (CFG / CFNG-type ON PCB)	—	—	0.40	

ELECTRICAL CHARACTERISTICS (V<sub>DD</sub> = 5.0 V, Ta = 25°C unless otherwise stated)

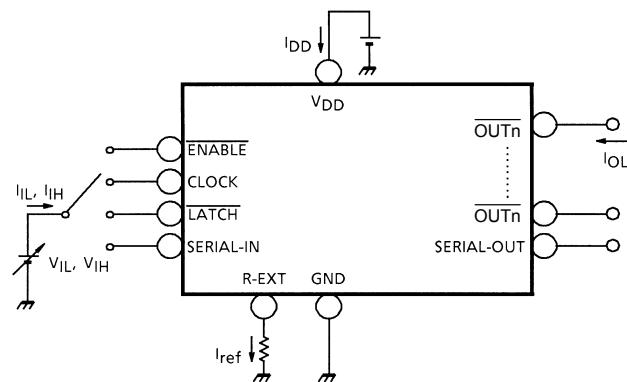
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	CONDITION		MIN	TYP.	MAX	UNIT
Input Voltage	"H" Level	V <sub>IH</sub>	—	—		0.7 V <sub>DD</sub>	—	V <sub>DD</sub>	V
	"L" Level	V <sub>IL</sub>	—	—		GND	—	0.3 V <sub>DD</sub>	
Output Leakage Current		I <sub>OH</sub>	—	V <sub>OH</sub> = 15.0 V		—	—	10	μA
Output Voltage	S-OUT	V <sub>OL</sub>	—	I <sub>OL</sub> = 1.0 mA		—	—	0.4	V
		V <sub>OH</sub>	—	I <sub>OH</sub> = -1.0 mA		4.6	—	—	
Output Current 1	I <sub>OL1</sub>	—	V <sub>CE</sub> = 0.7 V	R <sub>EXT</sub> = 470 Ω (Include skew)	34.1	40.0	45.9	mA	
		—	V <sub>CE</sub> = 0.4 V		33.7	39.5	45.3		
	Current Skew	ΔI <sub>OL1</sub>	—	I <sub>O</sub> = 40 mA, V <sub>CE</sub> = 0.4 V	R <sub>EXT</sub> = 470 Ω	—	±1.5	±6.0	%
Output Current 2	I <sub>OL3</sub>	—	V <sub>CE</sub> = 1.0 V	R <sub>EXT</sub> = 250 Ω (Include skew)	64.2	75.5	86.8	mA	
		—	V <sub>CE</sub> = 0.7 V		63.8	75.0	86.2		
	Current Skew	ΔI <sub>OL2</sub>	—	I <sub>O</sub> = 75 mA, V <sub>CE</sub> = 0.7 V	R <sub>EXT</sub> = 250 Ω	—	±1.5	±6.0	%
Supply Voltage Regulation		% / V <sub>DD</sub>	—	R <sub>EXT</sub> = 470 Ω, Ta = -40~85°C		—	1.5	5.0	% / V
Pull-Up Resistor		R <sub>IN</sub> (up)	—	—		150	300	600	kΩ
Pull-Down Resistor		R <sub>IN</sub> (down)	—	—		100	200	400	kΩ
Supply Current	"OFF"	I <sub>DD</sub> (off) 1	—	R <sub>EXT</sub> = OPEN, OUT0 ~ 7 = off		—	0.6	1.2	mA
		I <sub>DD</sub> (off) 2	—	R <sub>EXT</sub> = 470 Ω, OUT0 ~ 7 = off		3.5	5.8	8.0	
		I <sub>DD</sub> (off) 3	—	R <sub>EXT</sub> = 250 Ω, OUT0 ~ 7 = off		6.5	10.7	15.0	
	"ON"	I <sub>DD</sub> (on) 1	—	R <sub>EXT</sub> = 470 Ω, OUT0 ~ 7 = on		7.0	12.0	18.0	
		I <sub>DD</sub> (on) 2	—	R <sub>EXT</sub> = 250 Ω, OUT0 ~ 7 = on		10.0	22.0	32.0	

## SWITCHING CHARACTERISTICS (Ta = 25°C unless otherwise stated)

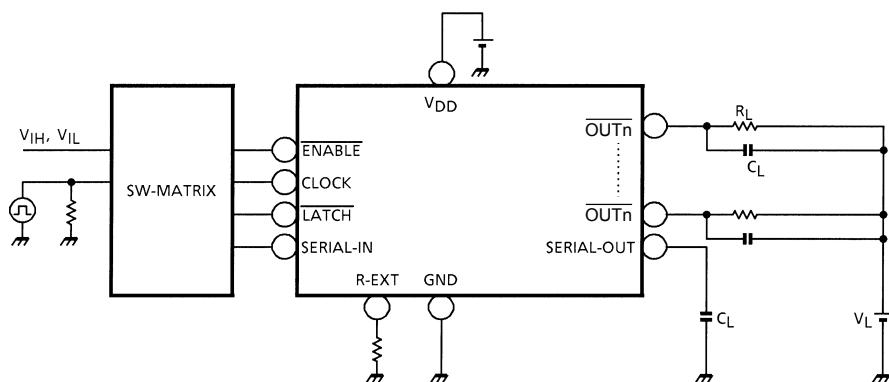
CHARACTERISTIC		SYMBOL	TEST CIR-CUIT	CONDITION	MIN	TYP.	MAX	UNIT	
Propagation Delay Time ("L" to "H")	SIN - OUTn	tpLH	—	$V_{DD} = 5.0 \text{ V}$ $V_{CE} = 0.4 \text{ V}$ $V_{IH} = V_{DD}$ $V_{IL} = \text{GND}$ $R_{EXT} = 470 \Omega$ $I_{OUT} = 40 \text{ mA}$ $V_L = 3.0 \text{ V}$ $R_L = 65 \Omega$ $C_L = 10.5 \text{ pF}$	—	1200	1500	ns	
	LATCH - OUTn				—	1200	1500		
	ENABLE - OUTn				—	1200	1500		
	CLK-SOUT				—	30	70		
Propagation Delay Time ("H" to "L")	SIN - OUTn	tpHL	—	$V_{DD} = 5.0 \text{ V}$ $V_{CE} = 0.4 \text{ V}$ $V_{IH} = V_{DD}$ $V_{IL} = \text{GND}$ $R_{EXT} = 470 \Omega$ $I_{OUT} = 40 \text{ mA}$ $V_L = 3.0 \text{ V}$ $R_L = 65 \Omega$ $C_L = 10.5 \text{ pF}$	—	700	1000	ns	
	LATCH - OUTn				—	700	1000		
	ENABLE - OUTn				—	700	1000		
	CLK-SOUT				—	30	70		
Pulse Width	CK	t <sub>w</sub> CLK	—	$V_{DD} = 5.0 \text{ V}$ $V_{CE} = 0.4 \text{ V}$ $V_{IH} = V_{DD}$ $V_{IL} = \text{GND}$ $R_{EXT} = 470 \Omega$ $I_{OUT} = 40 \text{ mA}$ $V_L = 3.0 \text{ V}$ $R_L = 65 \Omega$ $C_L = 10.5 \text{ pF}$	—	20	30	ns	
	LATCH	t <sub>w</sub> LAT	—		—	10	25		
Set-up Time for LATCH	L-H	t <sub>setup</sub>	—		—	25	50	ns	
	H-L				—	25	50		
Hold Time for LATCH	L-H	t <sub>hold</sub>	—		—	0	30	ns	
	H-L				—	0	30		
Maximum CLOCK Rise Time		t <sub>r</sub>	—		—	—	10	μs	
Maximum CLOCK Fall Time		t <sub>f</sub>	—		—	—	10	μs	
Output Rise Time		t <sub>or</sub>	—		300	600	1000	ns	
Output Fall Time		t <sub>of</sub>	—		150	300	600	ns	

## TEST CIRCUIT

## DC characteristics

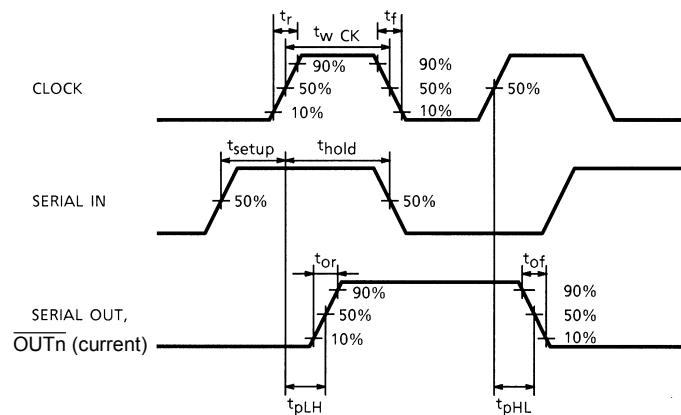
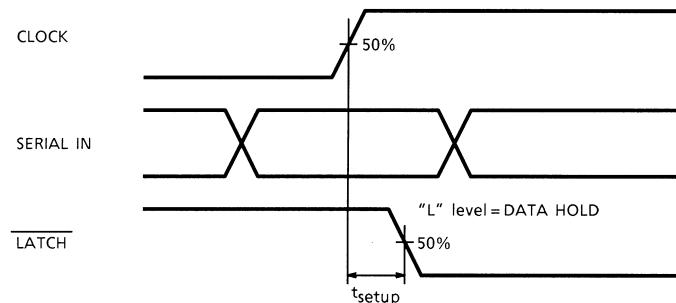
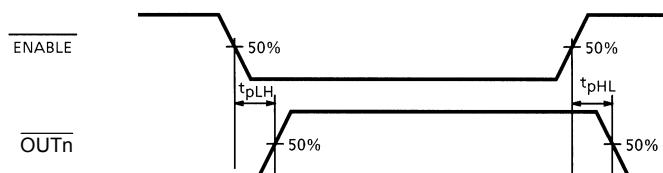


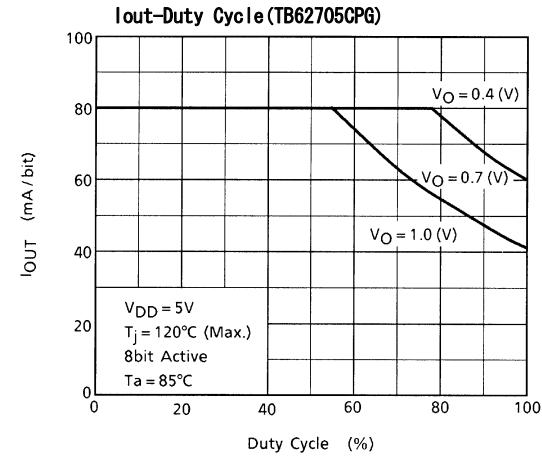
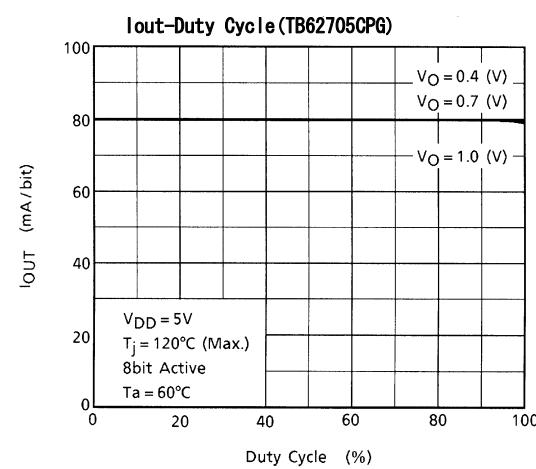
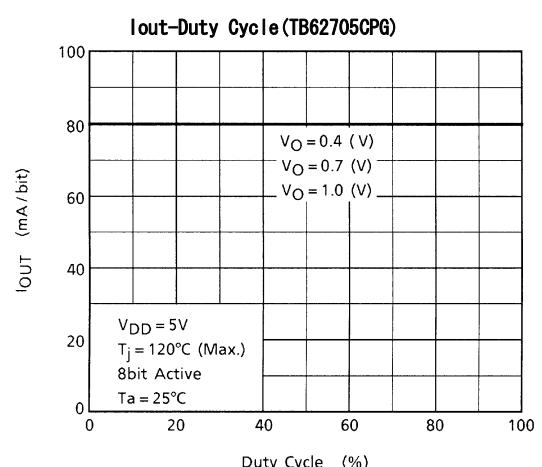
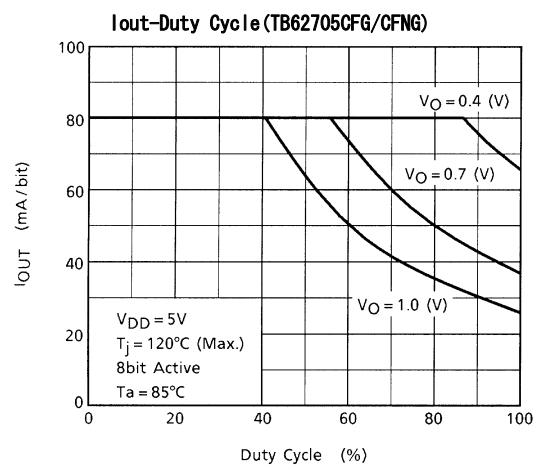
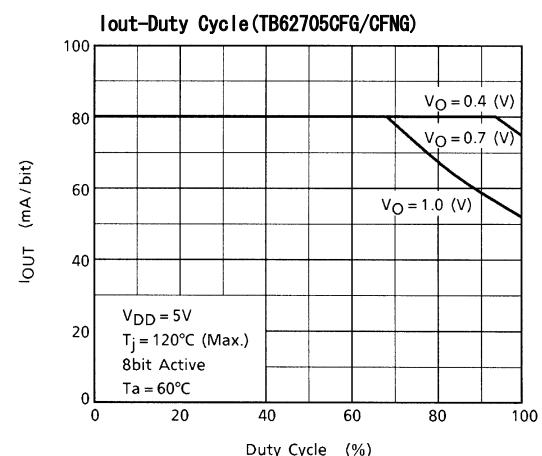
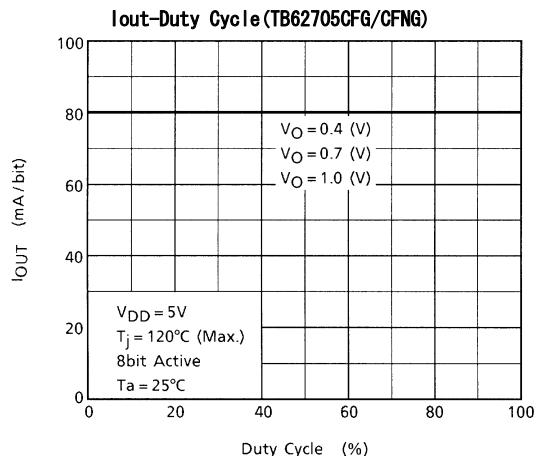
## AC characteristics

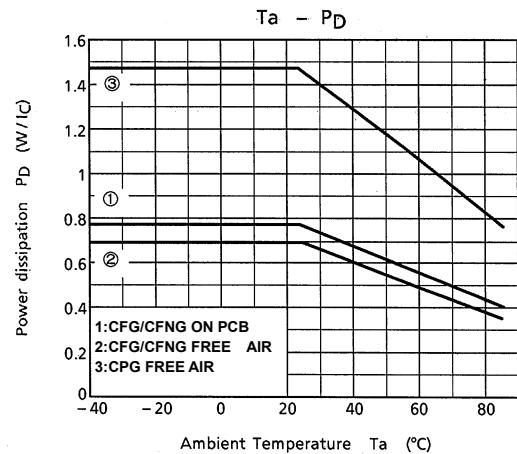
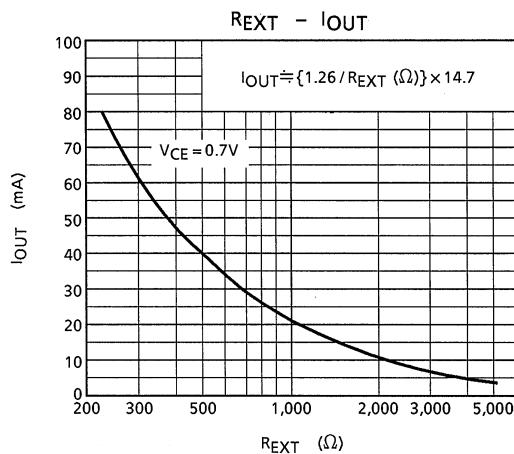


## Precaution on Use

Utmost care is necessary in the design of the output line, VCC (VDD) and GND line since the IC may be damaged due to short-circuits between outputs, air contamination faults, or faults caused by improper grounding.

**TIMING WAVEFORM****1. CLOCK-SERIAL OUT,  $\overline{\text{OUTn}}$** **2. CLOCK-LATCH****3. ENABLE- $\overline{\text{OUTn}}$** 



**LED DRIVER TB6270X SERIES APPLICATION NOTE**

[1] Output current ( $I_{OUT}$ )

$I_{OUT}$  is set by the external resistor ( $R-EXT$ ), as shown in Fig. 1.

## [2] Total supply voltage (VLED)

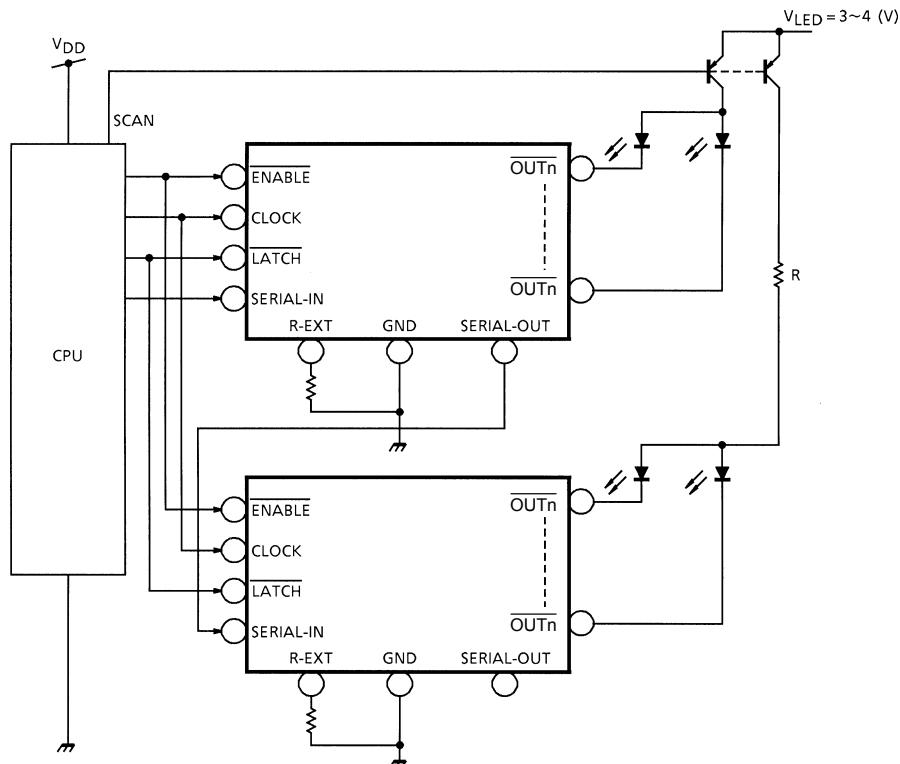
This device can operate on 0.4~0.7 V ( $V_O$ ).

When a higher voltage is input to the device, the excess voltage is consumed inside the device, which leads to power dissipation. To minimize power dissipation and loss, we recommend that the total supply voltage be set as follows:

$$V_{LED} (\text{total supply voltage}) = V_{CE} (\text{Tr Vsat}) + V_f (\text{LED forward voltage}) + V_O (\text{IC supply voltage}).$$

When the total supply is too high in the light of the power dissipation of this device, an additional resistor ( $R$ ) can be used to decrease the supply voltage ( $V_O$ ).

## PATTERN LAYOUT



## [3] Pattern layout

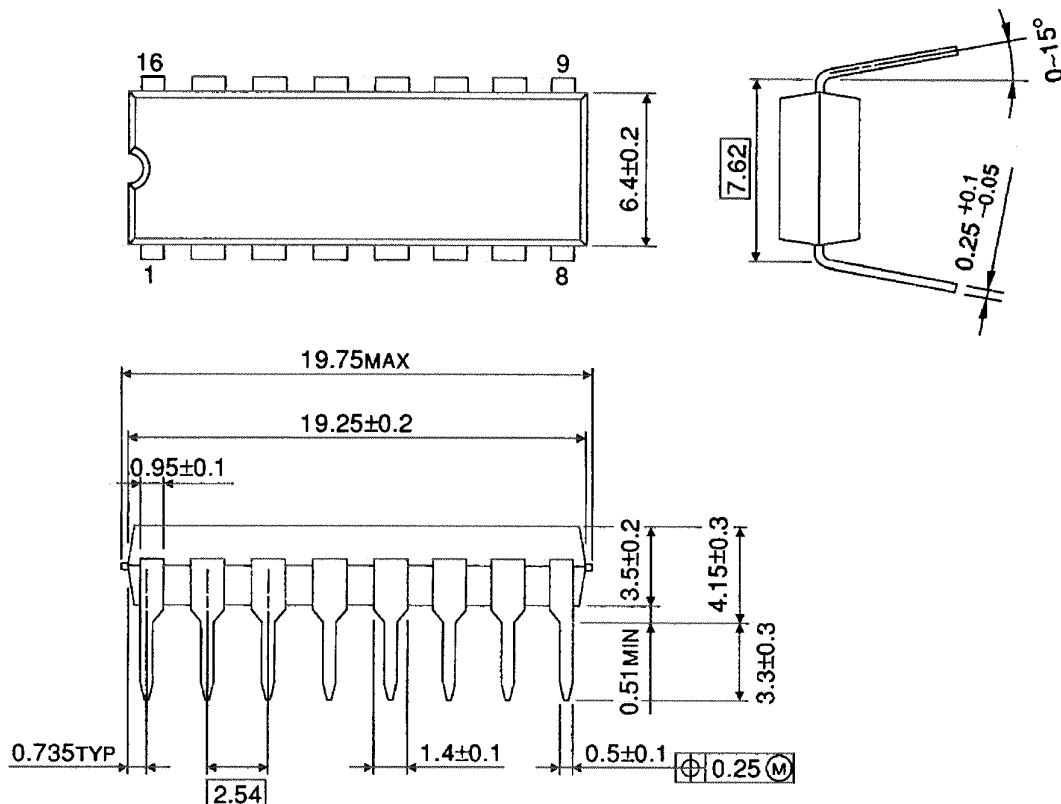
This device has only one ground pin, i.e., the combined signal ground pin and power ground pin.

If the ground pattern layout contains a large amount of inductance and impedance, and the voltage between the ground and LATCH or CLOCK terminals exceeds 2.5 V due to switching noise, the device may not operate correctly. Be sure to pay attention to pattern layout to minimize inductance.

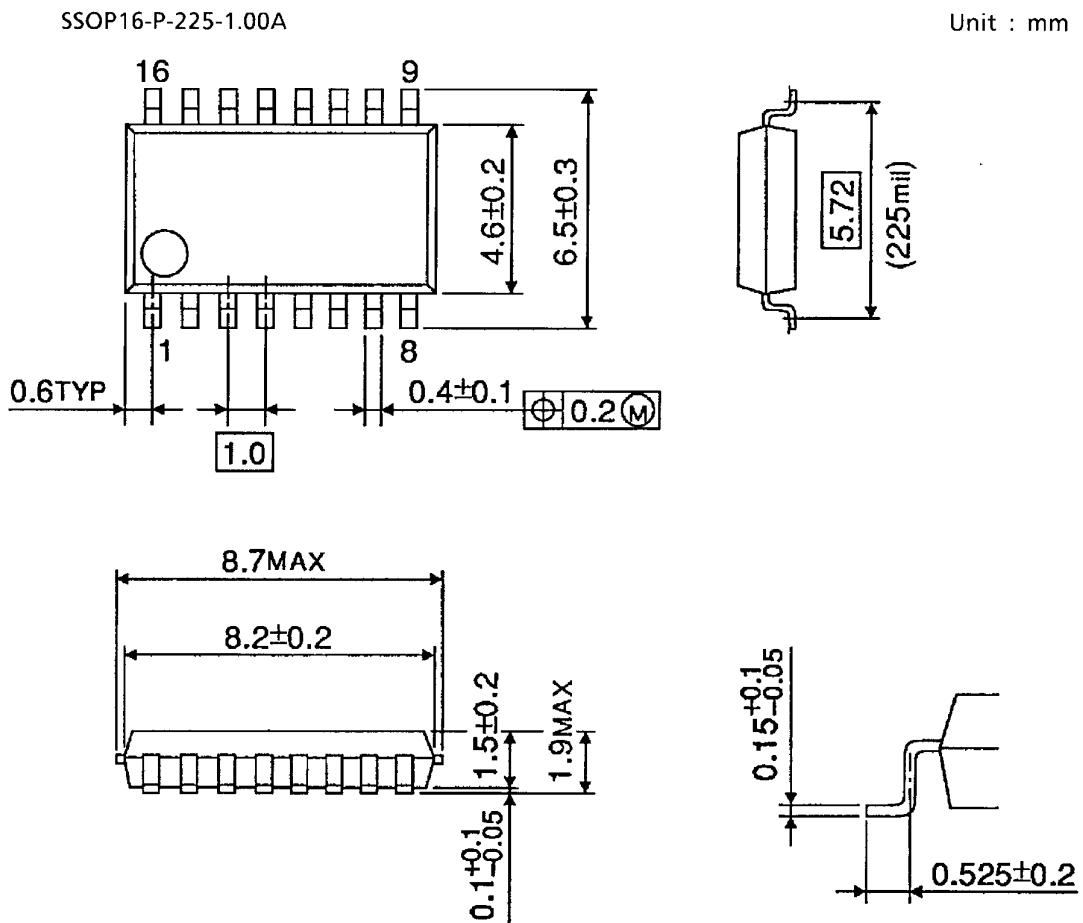
**TOSHIBA****TB62705CPG/CFG/CFNG****PACKAGE DIMENSIONS**

DIP16-P-300-2.54A

Unit : mm



Weight: 1.11 g (Typ.)

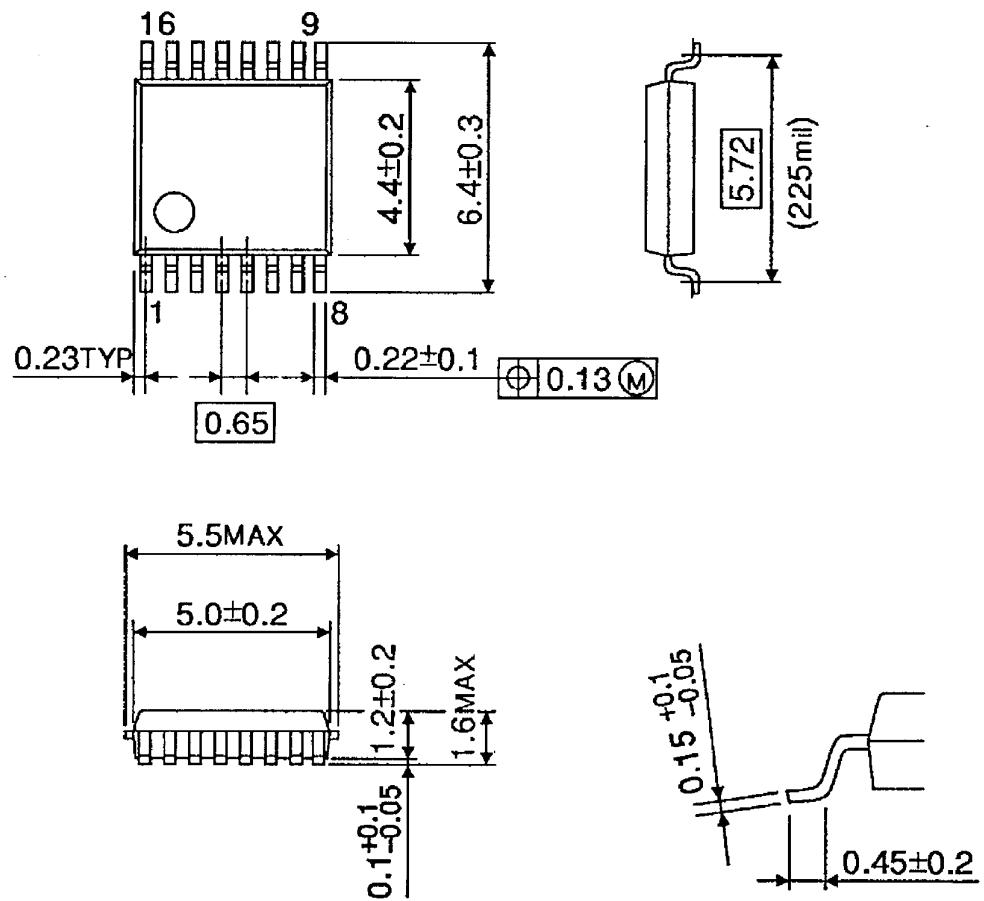
**PACKAGE DIMENSIONS**

Weight: 0.14 g (Typ.)

**PACKAGE DIMENSIONS**

SSOP16-P-225-0.65B

Unit : mm



Weight: 0.07 g (Typ.)

About solderability, following conditions were confirmed

- Solderability
  - (1) Use of Sn-63Pb solder Bath
    - solder bath temperature = 230°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux
  - (2) Use of Sn-3.0Ag-0.5Cu solder Bath
    - solder bath temperature = 245°C
    - dipping time = 5 seconds
    - the number of times = once
    - use of R-type flux

## **RESTRICTIONS ON PRODUCT USE**

030619EBA

- The information contained herein is subject to change without notice.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA for any infringements of patents or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of TOSHIBA or others.
- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property.  
In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The products described in this document are subject to the foreign exchange and foreign trade laws.
- TOSHIBA products should not be embedded to the downstream products which are prohibited to be produced and sold, under any law and regulations.