



SamHop Microelectronics Corp.

Jun.6 2005

STS 2621

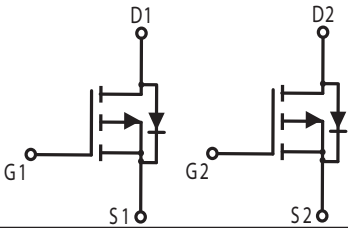
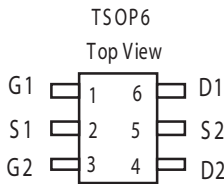
Dual P-Channel Enhancement Mode Field Effect Transistor

PRODUCT SUMMARY

VDSS	ID	RDS(ON) (mΩ) MAX
-20V	-2A	130 @ VGS = -4.5V
		190 @ VGS = -2.5V

FEATURES

- Super high dense cell design for low RDS(ON).
- Rugged and reliable.
- SOT-26 Package.



ABSOLUTE MAXIMUM RATINGS (TA=25 °C unless otherwise noted)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	VDS	-20	V
Gate-Source Voltage	VGS	±10	V
Drain Current-Continuous ^a @ TC=25°C -Pulsed ^b	ID	-2	A
	IDM	-7	A
Drain-Source Diode Forward Current ^a	IS	-1.25	A
Maximum Power Dissipation ^a	PD	1	W
Operating Junction and Storage Temperature Range	TJ, TSTG	-55 to 150	°C

THERMAL CHARACTERISTICS

Thermal Resistance, Junction-to-Ambient ^a	R θJA	125	°C/W
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ELECTRICAL CHARACTERISTICS (TA =25°C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
OFF CHARACTERISTICS						
Drain-Source Breakdown Voltage	BV _{DSS}	V _{GS} = 0V, I _D = -250uA	-20			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -16V, V _{GS} = 0V			1	uA
Gate-Body Leakage	I _{GSS}	V _{GS} = ± 10V, V _{DS} = 0V			±100	nA
ON CHARACTERISTICS ^b						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250uA	-0.5	-0.8	-1.5	V
Drain-Source On-State Resistance	R _{DS(ON)}	V _{GS} = -4.5V, I _D = -2.0A		115	130	m-ohm
		V _{GS} = -2.5V, I _D = -1.0A		175	190	m-ohm
On-State Drain Current	I _{D(ON)}	V _{DS} = -5V, V _{GS} = -4.5V	-5			A
Forward Transconductance	g _{FS}	V _{DS} = -5V, I _D = -2A		6		S
DYNAMIC CHARACTERISTICS ^c						
Input Capacitance	C _{ISS}	V _{DS} = -20V, V _{GS} = 0V f = 1.0MHz		295		pF
Output Capacitance	C _{OSS}			63		pF
Reverse Transfer Capacitance	C _{RSS}			52		pF
SWITCHING CHARACTERISTICS ^c						
Turn-On Delay Time	t _{D(ON)}	V _{DD} = -10V, I _D = -1A, V _{GS} = -4.5V, R _{GEN} = 6 ohm		11.5		ns
Rise Time	t _r			15.6		ns
Turn-Off Delay Time	t _{D(OFF)}			83.1		ns
Fall Time	t _f			43.6		ns
Total Gate Charge	Q _g	V _{DS} = -10V, I _D = -2A, V _{GS} = -4.5V		3.5		nC
Gate-Source Charge	Q _{gs}			0.9		nC
Gate-Drain Charge	Q _{gd}			1.1		nC

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ELECTRICAL CHARACTERISTICS (T_A=25 °C unless otherwise noted)

Parameter	Symbol	Condition	Min	Typ ^c	Max	Unit
DRAIN-SOURCE DIODE CHARACTERISTICS ^b						
Diode Forward Voltage	V _{SD}	V _{GS} = 0V, I _s = -1.25A		-0.85	-1.2	V

Notes

- a.Surface Mounted on FR4 Board, t_l≤10sec.
- b.Pulse Test:Pulse Width ≤ 300us, Duty Cycle ≤ 2%.
- c.Guaranteed by design, not subject to production testing.

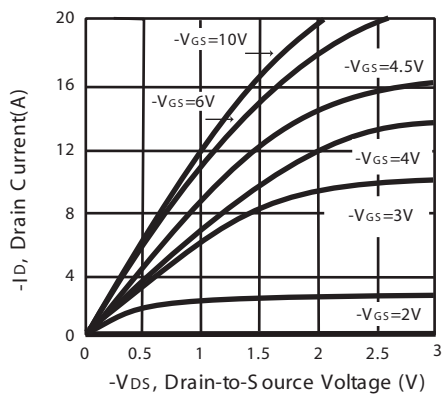


Figure 1. Output Characteristics

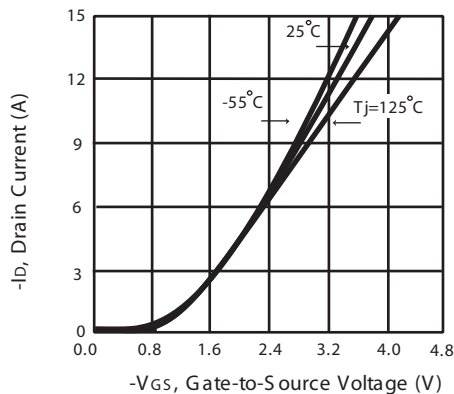


Figure 2. Transfer Characteristics

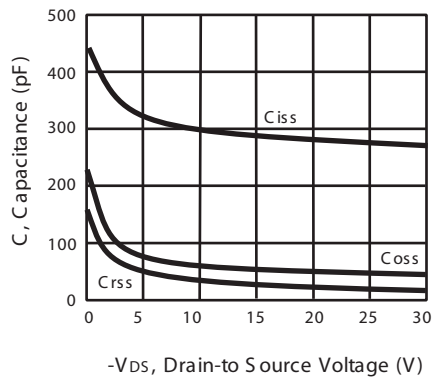


Figure 3. Capacitance

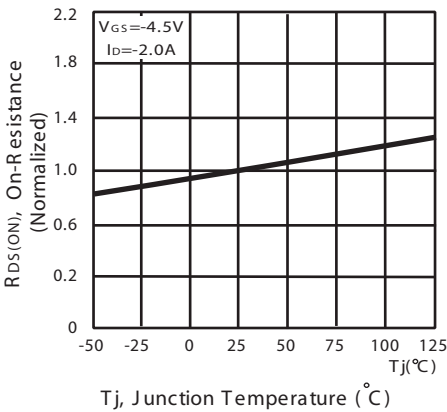
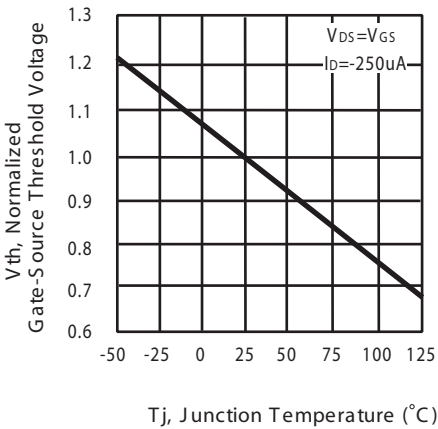


Figure 4. On-Resistance Variation with Temperature

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with Temperature

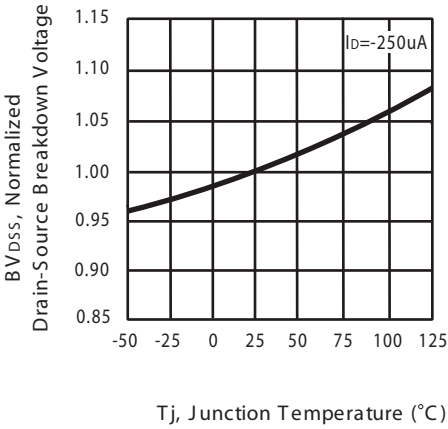


Figure 6. Breakdown Voltage Variation with Temperature

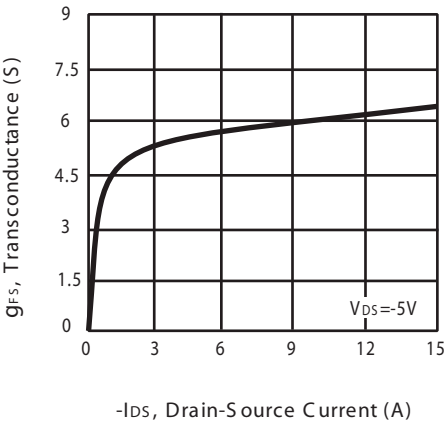


Figure 7. Transconductance Variation with Drain Current

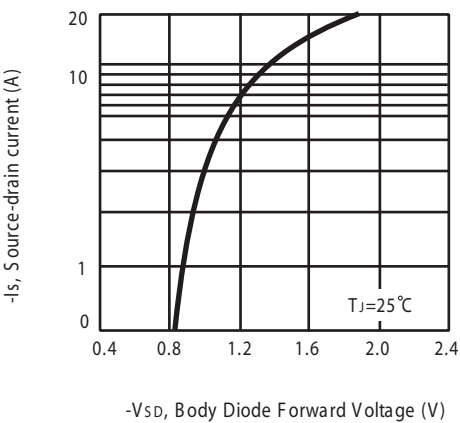


Figure 8. Body Diode Forward Voltage Variation with Source Current

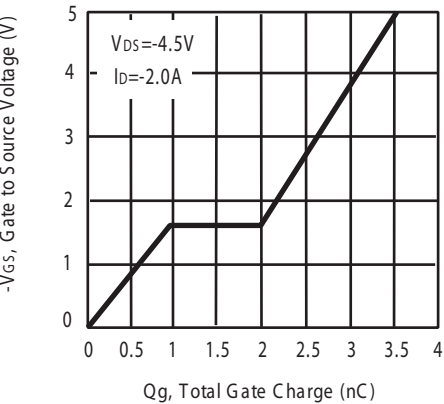


Figure 9. Gate Charge

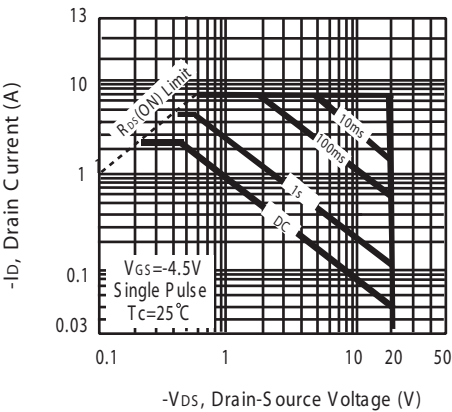


Figure 10. Maximum Safe Operating Area

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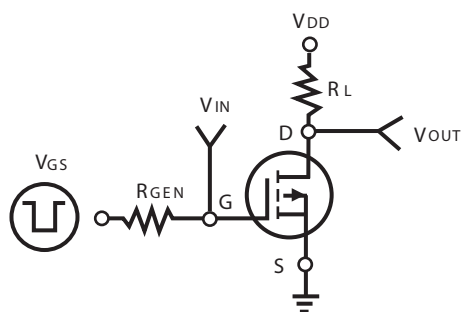


Figure 11. S switching Test Circuit

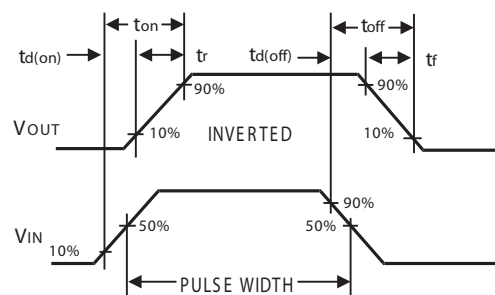


Figure 12. Switching Waveforms

