

## Advanced Power MOSFET

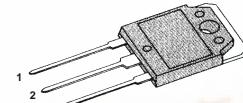
## SSH6N70A

## FEATURES

- Avalanche Rugged Technology
- Rugged Gate Oxide Technology
- Lower Input Capacitance
- Improved Gate Charge
- Extended Safe Operating Area
- Lower Leakage Current : 25  $\mu$ A (Max.) @  $V_{DS} = 700V$
- Low  $R_{DS(ON)}$  : 1.552  $\Omega$  (Typ.)

 $BV_{DSS} = 700 V$  $R_{DS(on)} = 1.8 \Omega$  $I_D = 6 A$ 

TO-3P



1.Gate 2. Drain 3. Source

## Absolute Maximum Ratings

Symbol	Characteristic	Value	Units
$V_{DSS}$	Drain-to-Source Voltage	700	V
$I_D$	Continuous Drain Current ( $T_C=25^\circ C$ )	6	A
	Continuous Drain Current ( $T_C=100^\circ C$ )	3.8	
$I_{DM}$	Drain Current-Pulsed	24	A
$V_{GS}$	Gate-to-Source Voltage	$\pm 30$	V
$E_{AS}$	Single Pulsed Avalanche Energy	582	mJ
$I_{AR}$	Avalanche Current	6	A
$E_{AR}$	Repetitive Avalanche Energy	14	mJ
$dv/dt$	Peak Diode Recovery $dv/dt$	2.5	V/ns
$P_D$	Total Power Dissipation ( $T_C=25^\circ C$ )	140	W
	Linear Derating Factor	1.12	W/ $^\circ C$
$T_J, T_{STG}$	Operating Junction and Storage Temperature Range	- 55 to +150	$^\circ C$
	Maximum Lead Temp. for Soldering Purposes, 1/8" from case for 5-seconds	300	

## Thermal Resistance

Symbol	Characteristic	Typ.	Max.	Units
$R_{\theta JC}$	Junction-to-Case	--	0.89	$^\circ C/W$
$R_{\theta CS}$	Case-to-Sink	0.24	--	
$R_{\theta JA}$	Junction-to-Ambient	--	40	

Rev. B

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# SSH6N70A

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## Electrical Characteristics ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	700	--	--	V	$\text{V}_{\text{GS}}=0\text{V}, \text{I}_D=250\mu\text{A}$
$\Delta\text{BV}/\Delta T_J$	Breakdown Voltage Temp. Coeff.	--	0.79	--	V/ $^\circ\text{C}$	$\text{I}_D=250\mu\text{A}$ See Fig 7
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	--	4.0	V	$\text{V}_{\text{DS}}=5\text{V}, \text{I}_D=250\mu\text{A}$
$\text{I}_{\text{GSS}}$	Gate-Source Leakage , Forward	--	--	100	nA	$\text{V}_{\text{GS}}=30\text{V}$
	Gate-Source Leakage , Reverse	--	--	-100		$\text{V}_{\text{GS}}=-30\text{V}$
$\text{I}_{\text{DSS}}$	Drain-to-Source Leakage Current	--	--	25	$\mu\text{A}$	$\text{V}_{\text{DS}}=700\text{V}$
		--	--	250		$\text{V}_{\text{DS}}=560\text{V}, \text{T}_C=125^\circ\text{C}$
$\text{R}_{\text{DS}(\text{on})}$	Static Drain-Source On-State Resistance	--	--	1.8	$\Omega$	$\text{V}_{\text{GS}}=10\text{V}, \text{I}_D=3\text{A}$ ④*
$\text{g}_{\text{fs}}$	Forward Transconductance	--	4.12	--	$\Omega$	$\text{V}_{\text{DS}}=50\text{V}, \text{I}_D=3\text{A}$ ④
$\text{C}_{\text{iss}}$	Input Capacitance	--	920	1200	pF	$\text{V}_{\text{GS}}=0\text{V}, \text{V}_{\text{DS}}=25\text{V}, f=1\text{MHz}$ See Fig 5
$\text{C}_{\text{oss}}$	Output Capacitance	--	100	115		
$\text{C}_{\text{rss}}$	Reverse Transfer Capacitance	--	45	55		
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	--	18	45	ns	$\text{V}_{\text{DD}}=350\text{V}, \text{I}_D=6\text{A},$ $\text{R}_G=11.5\Omega$ See Fig 13 ④⑤
$t_r$	Rise Time	--	23	55		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	--	76	160		
$t_f$	Fall Time	--	26	60		
$\text{Q}_g$	Total Gate Charge	--	51	67	nC	$\text{V}_{\text{DS}}=560\text{V}, \text{V}_{\text{GS}}=10\text{V},$ $\text{I}_D=6\text{A}$ See Fig 6 & Fig 12④⑤
$\text{Q}_{\text{gs}}$	Gate-Source Charge	--	8.3	--		
$\text{Q}_{\text{gd}}$	Gate-Drain("Miller") Charge	--	23.1	--		

## Source-Drain Diode Ratings and Characteristics

Symbol	Characteristic	Min.	Typ.	Max.	Units	Test Condition
$\text{I}_S$	Continuous Source Current	--	--	6	A	Integral reverse pn-diode in the MOSFET
$\text{I}_{\text{SM}}$	Pulsed-Source Current ①	--	--	24		
$\text{V}_{\text{SD}}$	Diode Forward Voltage ④	--	--	1.4	V	$\text{T}_J=25^\circ\text{C}, \text{I}_S=6\text{A}, \text{V}_{\text{GS}}=0\text{V}$
$\text{t}_{\text{rr}}$	Reverse Recovery Time	--	440	--	ns	$\text{T}_J=25^\circ\text{C}, \text{I}_F=6\text{A}$ $d\text{I}_F/dt=100\text{A}/\mu\text{s}$ ④
$\text{Q}_{\text{rr}}$	Reverse Recovery Charge	--	4.05	--		

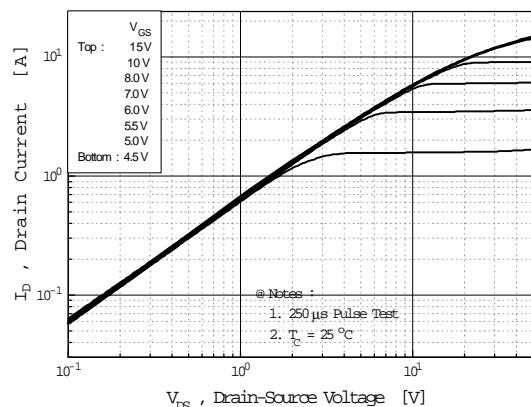
### Notes :

- ① Repetitive Rating : Pulse Width Limited by Maximum Junction Temperature
- ②  $L=30\text{mH}, \text{I}_{\text{AS}}=6\text{A}, \text{V}_{\text{DD}}=50\text{V}, \text{R}_G=27\Omega$ , Starting  $\text{T}_J=25^\circ\text{C}$
- ③  $\text{I}_{\text{SD}} \leq 6\text{A}, d\text{I}/dt \leq 140\text{A}/\mu\text{s}, \text{V}_{\text{DD}} \leq \text{BV}_{\text{DSS}}$ , Starting  $\text{T}_J=25^\circ\text{C}$
- ④ Pulse Test : Pulse Width =  $250\mu\text{s}$ , Duty Cycle  $\leq 2\%$
- ⑤ Essentially Independent of Operating Temperature

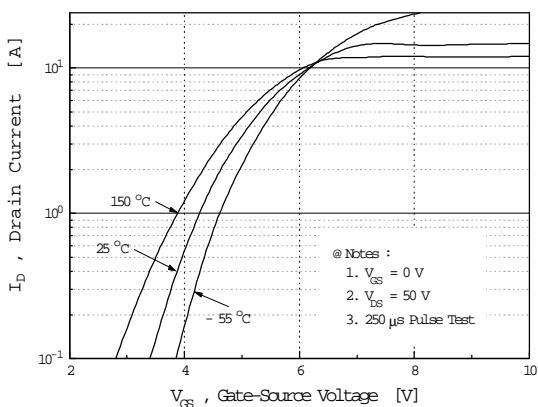
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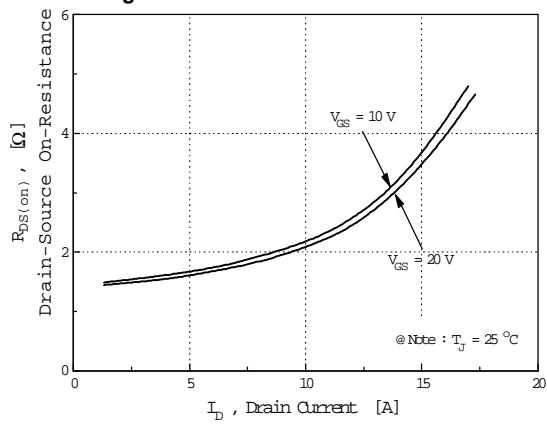
**Fig 1. Output Characteristics**



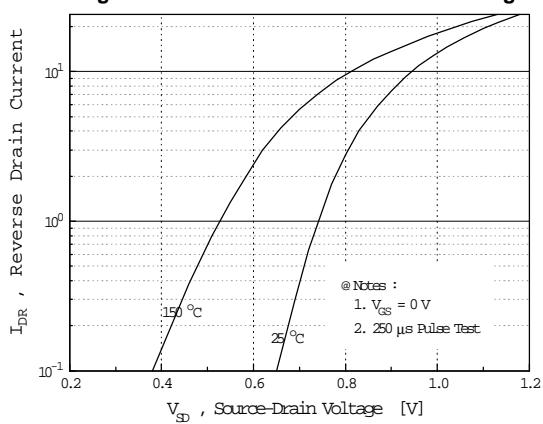
**Fig 2. Transfer Characteristics**



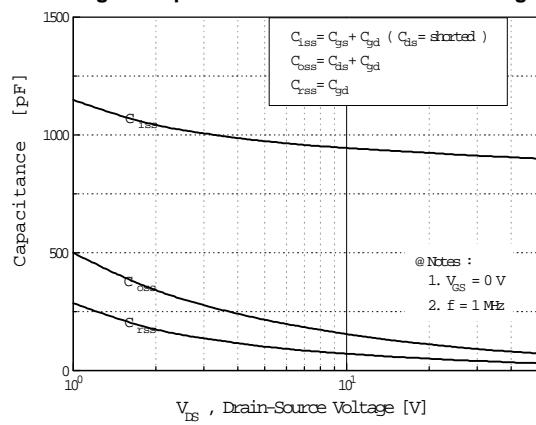
**Fig 3. On-Resistance vs. Drain Current**



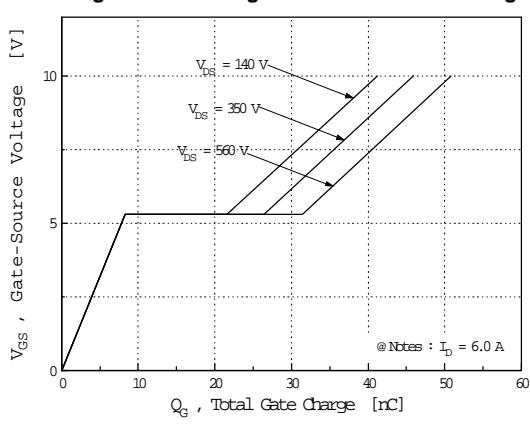
**Fig 4. Source-Drain Diode Forward Voltage**



**Fig 5. Capacitance vs. Drain-Source Voltage**

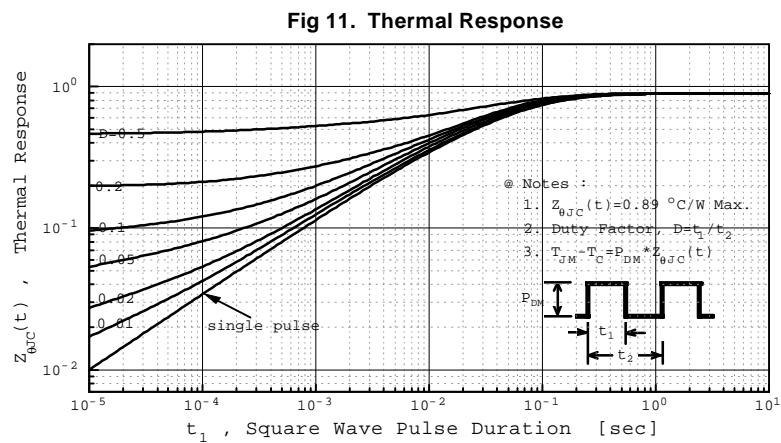
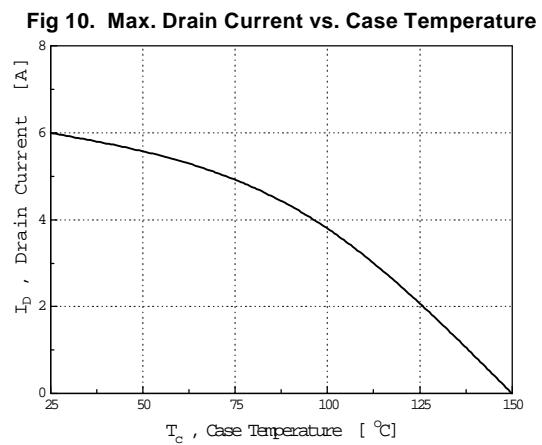
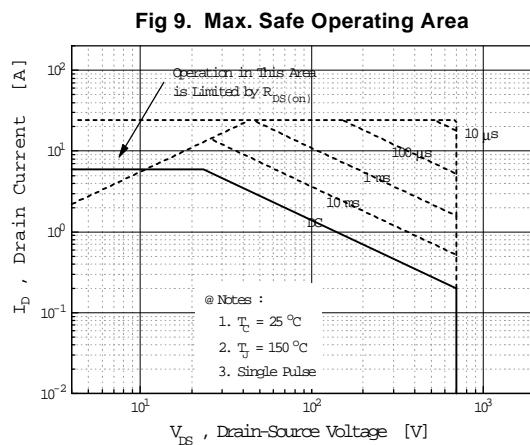
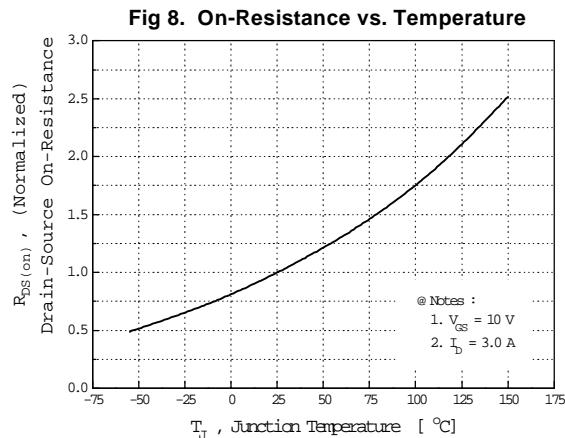
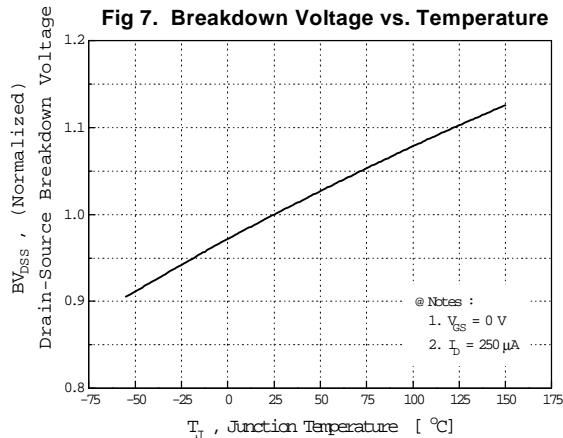


**Fig 6. Gate Charge vs. Gate-Source Voltage**



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Fig 12. Gate Charge Test Circuit & Waveform

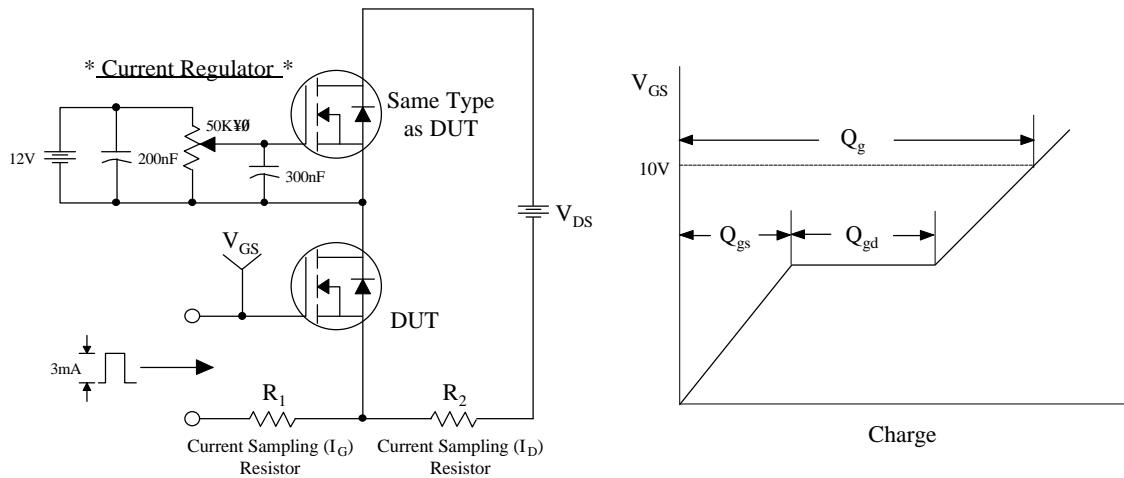


Fig 13. Resistive Switching Test Circuit & Waveforms

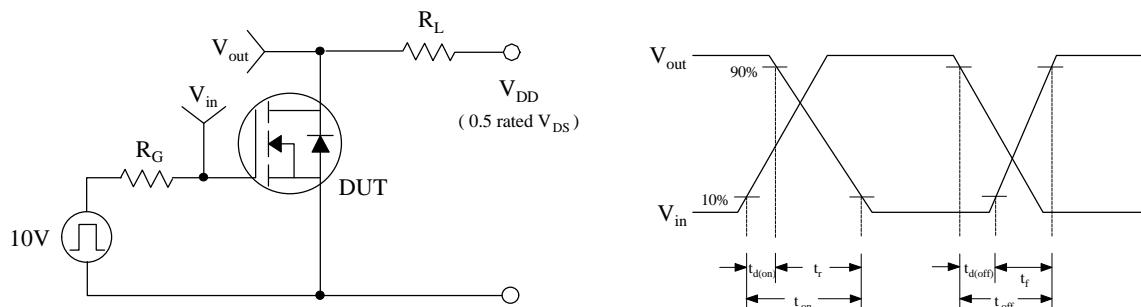
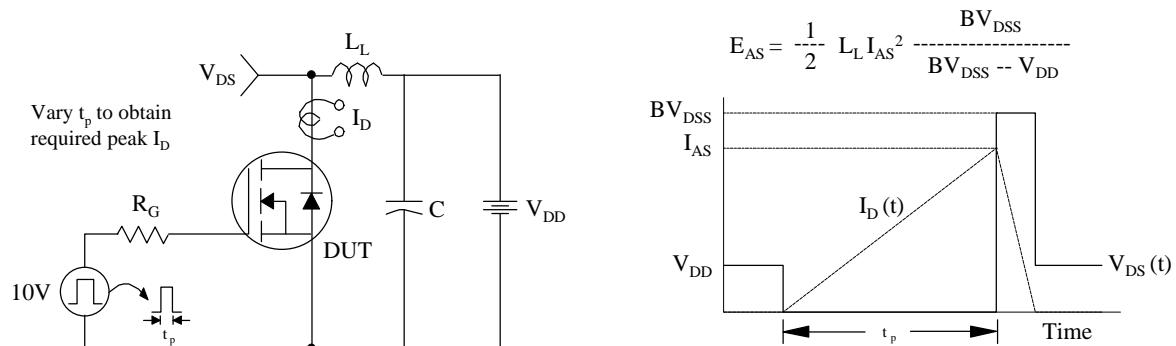


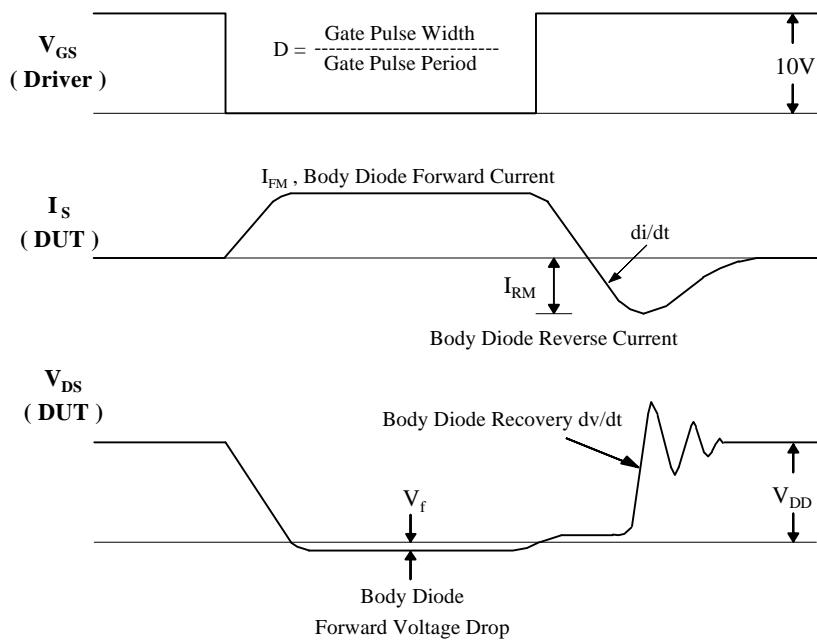
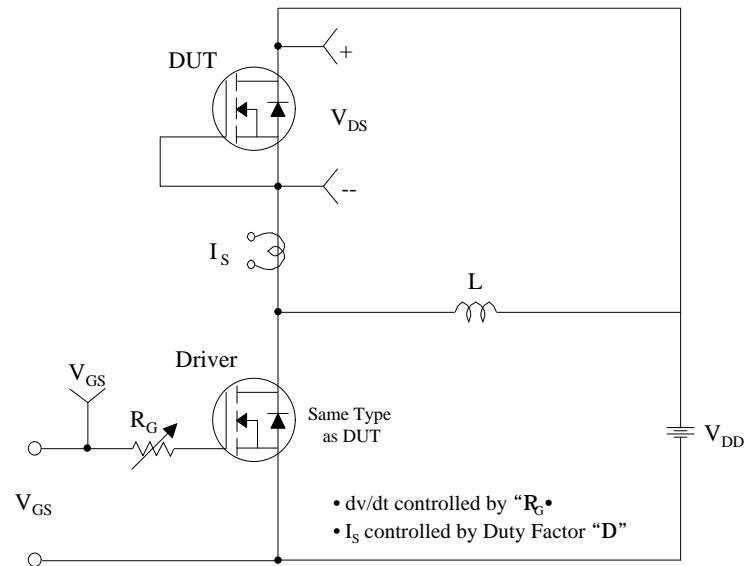
Fig 14. Unclamped Inductive Switching Test Circuit & Waveforms



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Fig 15. Peak Diode Recovery dv/dt Test Circuit & Waveforms



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