－Designed for WLAN IF Applications
－Low Insertion Loss
－ $9.1 \times 7.1$ mm Version of SF1059A－1
－Unbalanced Input and Output
－Complies with Directive 2002／95／EC（RoHS）

## Absolute Maximum Ratings

| Rating | Value | Units |
| :--- | :---: | :---: |
| Maximum Incident Power in Passband | +10 | dBm |
| Max．DC voltage between any 2 terminals | 30 | VDC |
| Storage Temperature Range | -40 to +85 | ${ }^{\circ} \mathrm{C}$ |
| Suitable for lead－free soldering－Max Soldering Profile | $260^{\circ} \mathrm{C}$ for 30 s |  |



## Electrical Characteristics

| Characteristic | Sym | Notes | Min | Typ | Max | $\frac{\text { Units }}{\mathrm{MHz}}$ |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Nominal Center Frequency | $\mathrm{f}_{\mathrm{C}}$ | 1 | 350.00 |  |  |  |
| Passband Insertion Loss at fc | IL |  |  | 8 | 10.0 | dB |
| 3 dB Passband | $\mathrm{BW}_{3}$ | 1， 2 | $\pm 400$ | $\pm 600$ |  | kHz |
| Amplitude Variation over fc $\pm 250 \mathrm{kHz}$ |  |  |  | 0.5 | 1.0 | $\mathrm{dB}_{\text {P－P }}$ |
| Group Delay Variation over fc $\pm 400 \mathrm{kHz}$ | GDV |  |  | 200 | 250 | $n S_{\text {P－P }}$ |
| Rejection fc－8．0 to fc－2．0 and fc＋2．0 to +8.0 MHz |  | 1，2， 3 | 35 | 40 |  |  |
| fc－50 to fc－8．0 and fc +8.0 to fc +50 MHz |  |  | 40 | 45 |  | dB |
| Ultimate |  |  |  | 50 |  |  |
| Operating Temperature Range | $\mathrm{T}_{\text {A }}$ | 1 | －20 |  | ＋70 | ${ }^{\circ} \mathrm{C}$ |


| Impedance Matching to $50 \Omega$ unbalanced | External L－C |
| :--- | :---: |
| Case Style | SM9171－10 $9.1 \times 7.1 \mathrm{~mm}$ Nominal Footprint |
| Lid Symbolization（XX＝ 2 character date code） | RFM SF1059A XX |

Electrical Connections

| Connection |  | Terminals |
| :--- | :--- | :---: |
| Port 1 | Input or Return | 5 |
|  | Return or Input | 6 |
| Port 2 | Output or Return | 10 |
|  | Return or Output | 1 |
| Ground |  | All others |
| Single Ended Operation | Return is ground |  |
| Differential Operation | Return is hot |  |

## Notes：

1．Unless noted otherwise，all specitication apply over the operating temperature range with filter soldered to the specified demonstration board with impedanced matching to $50 \Omega$ network analyzer．
2．Unless noted otherwise，all frequency specitications are referenced to the nominal center frequency，fc．
3．Rejection is measured as attenuation below the minimum IL point in the passband．Rejection in final user application is dependent on PCB layout and external impedance matching design．See Application Note No． 42 for details．
4．＂LRIP＂or＂L＂after the part number indicates＂low rate initial production＂and＂ENG＂or＂E＂indicates＂engineering prototypes．＂
5．The design，manufacturing process，and specifications of this filter are subject to change．
6．Either Port 1 or Port 2 may be used for either input or output in the design．However，impedances and impedance matching may vary between Port 1 and Port 2，so that the filter must always be installed in one direction per the circuit design．
US and international patents may apply．
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## SAW Filter

## SM9171-10 Case

## 10-Terminal Ceramic Surface-Mount Case <br> $9.1 \times 7.1$ mm Nominal Footprint



| Case Dimensions |  |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Dimension | $\mathbf{m m}$ |  |  | Inches |  |  |  |
|  | Min | Nom | Max | Min | Nom | Max |  |
| A | 8.86 | 9.09 | 9.40 | 0.349 | 0.358 | 0.370 |  |
| B | 6.88 | 7.11 | 7.40 | 0.271 | 0.280 | 0.291 |  |
| C |  | 1.91 | 2.00 |  | 0.075 | 0.079 |  |
| D |  | 0.99 |  |  | 0.039 |  |  |
| E |  | 0.79 |  |  | 0.031 |  |  |
| H |  | 1.0 |  |  | 0.039 |  |  |
| P |  | 2.54 |  |  | 0.100 |  |  |


| Materials |  |
| :--- | :--- |
| Solder Pad <br> Termination | Au plating 30-60 ulnches (76.2-152 uM) over 80- <br> 200 ulnches (203-508 uM) Ni. |
| Lid | Fe-Ni-Co Alloy Electroless Nickel Plate (8-11\% <br> Phosphorus) 100-200 ulnches Thick |
| Body | $\mathrm{Al}_{2} \mathrm{O}_{3}$ Ceramic |
| Pb Free |  |


| Electrical Connections |  |  |
| :--- | :--- | :---: |
| Connection |  | Terminals |
| Port 1 | Input or Return | 5 |
|  | Return or Input | 6 |
| Port 2 | Output or Return | 10 |
|  | Return or Output | 1 |
| Ground |  |  |
| Single Ended Operation |  | Return is ground |
| Differential Operation |  | Return is hot |



| REV | ECN | DESCRIPTION | DATE |
| :---: | :---: | :---: | :---: |
| A | 12256 | INITIAL RELEASE |  |
|  |  |  | $06 / 18 / 04$ |
|  |  |  |  |


| DRAWN BY/DATE: <br> D. GLAVIN | $06 / 18 / 04$ |
| :--- | :--- |
| CHECKED/APPROVED BY: <br> J. GRANT | $06 / 18 / 04$ |


#### Abstract

SF1059A Demo Board Plots

16 Jun 2004 08:03:04 

ㅁ |  | 0.000000 NHz |  |  |
| :---: | :--- | :--- | :--- |
| BW: | 1.233460 MHz | $\Delta$ | REF |
| cent: 349.871138 MHz |  |  |  | 

REF 0 dB 

든 $\begin{array}{ll}\underset{\sim}{〔} & \grave{0}\end{array}$ HId


SF1059A

16 Jun 2004 08:13:06
2:-09.087ns


SPAN 1.000000 MHz
SF1059A-013
CH1 $\mathrm{S}_{11}$ (UFS





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| :---: | :---: | :---: | :---: | :---: | :---: |

