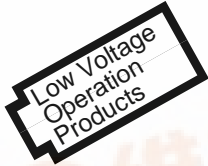


SCI7630M Series

Switching Regulator



- **Step-up Switching Regulator**
(1.5V → 2.0, 2.2, 2.35, 2.4, 2.7, 2.8, 3.0, 3.1, 3.5, 3.7, 4.2, 5.0V)
- **Low Operating Voltage (Min 0.9V)**
- **Voltage Detecting function, Battery Back-up function (SCI7631M)**

DESCRIPTION

The SCI7630M Series offers a variety of high-efficiency and low current dissipation switching regulators produced by the silicon gate CMOS process. (An external inductor is used to implement the boosting function.) Each SCI7630M basically consists of a reference voltage supply, comparator, and inductor driving transistors. Its output voltage is fixed internally.

The SCI7630M Series operates with low current dissipation and low voltage. Its optional features include voltage detecting function, battery back-up function, and stand-by function. It is thus suited for controlling the power supply of portable or small size equipment that requires high operating efficiency.

The SCI7630M series has four major variation. The SCI7631M and the SCI7638M operate by the clock signal generated by an on-chip CR oscillation circuit. The SCI7633M operate by the clock signal generated by a crystal oscillation circuit. And another feature of the SCI7638M is its output voltage which has temperature characteristics matching the properties of an LCD, making it suitable as the power supply of a product using an LCD.

FEATURES

- Low operating voltage Min. 0.9V
- Low current dissipation Typ. 8μA (SCI7631M)
- Low stand-by current Typ. 3μA
- High accurate voltage detection 1.05V±0.05V
- Be able to power off
- Battery back-up function (SCI7631M)
- Built-in CR oscillation circuit (SCI7631M, SCI7638M)
- Power on clear (SCI7631M/C, SCI7638M)
- Response compensation inputs (SCI7638M)
- Temperature gradient of output voltage for LCD (SCI7638M)
- Package SCI7630M: SOP3-8pin (plastic)



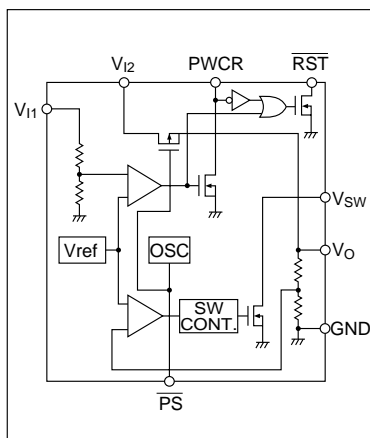
SCI7630M Series

LINE UP

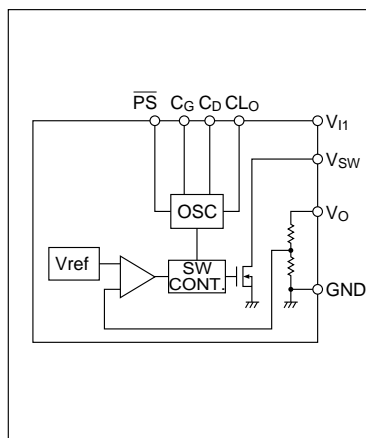
Type No.	Input voltage	Output voltage	Oscillation for booster	Voltage detecting function	Power-on clear	Battery back-up	Ripple noise suppression input	Output voltage temp gradient	Package
SCI7631M _{LA}	1.5V (0.9Vmin.)	2.4V	internal CR oscillation	available	available	available	—	—	SOP3-8pin
SCI7631M _{BA}		3.0V							
SCI7631M _{KA}		3.5V							
SCI7631M _{AA}		5.0V							
SCI7633M _{BA}	1.5V (0.9Vmin.)	3.0V	crystal oscillation	—	—	—	—	—	SOP3-8pin
SCI7638M _{HA}	1.5V (0.9Vmin.)	2.2V	internal CR oscillation	available	available	—	available	-4.5mV/°C	SOP3-8pin
SCI7638M _{LA}		2.4V						-4.0mV/°C	

BLOCK DIAGRAM

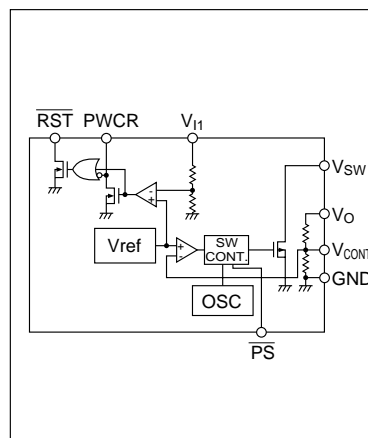
SCI7631M



SCI7633M

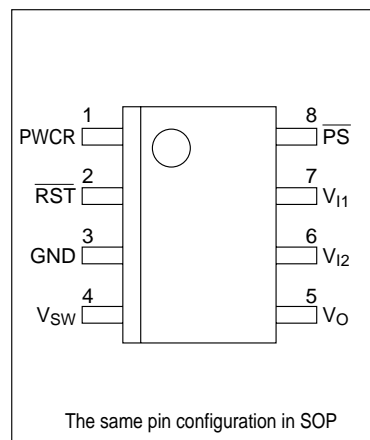


SCI7638M

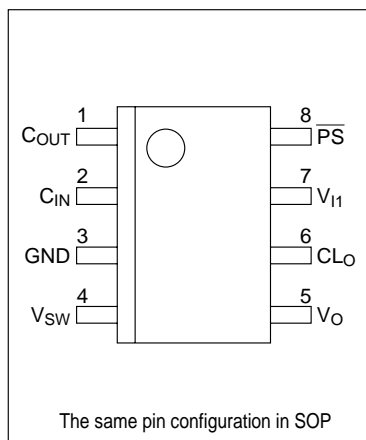


PIN CONFIGURATION

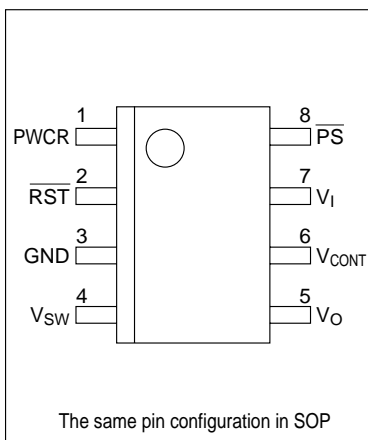
SCI7631M



SCI7633M



SCI7638M



SCI7630M Series

PIN DESCRIPTION

Pin Name	Function	Pin Name	Function
V _{I1}	Power supply for booster (positive)	$\overline{\text{RST}}$	Reset signal output (voltage detection)
V _{I2}	Power Supply for back-up (positive)	C _{OUT}	Crystal oscillator terminal (Drain)
V _O	Output voltage	C _{IN}	Crystal oscillator terminal (Gate)
V _{SW}	Terminal for connection of inductor for booster	CL _O	Clock pulse output
$\overline{\text{PS}}$	Power save control *1	GND	Power supply (GND)
PWCR	Power on clear *2	V _{CONT}	Comparator input terminal *3

*1. Stand-by mode and back-up function (back-up function is available to SCI7631M).

The device can be set into stand-by mode by connecting the $\overline{\text{PS}}$ pin to GND level. In the stand-by mode, the on-chip back-up switch turns on and the input voltage connected to the V_{I2} pin is output to the V_O pin with the boosting operation at rest. The $\overline{\text{PS}}$ pin is internally pulled up, and must be held open when its control is unnecessary.

(Connecting the voltage detection output to the $\overline{\text{PS}}$ pin validates the back-up function.)

*2. Power on clear function (SCI7631M, SCI7638M) (voltage detection function)

See the SCI7631M example of application. For a system with an external resistor R₁ and capacitor C₁ connected to the PWCR pin and a pull-up resistor to the $\overline{\text{RST}}$ pin, a reset pulse whose width can flexibly be selected by R₁ and C₁ is obtained after acquisition of normal output by boosting operation. This enables the system (for example, a microcomputer), connected to the $\overline{\text{RST}}$ pin, to be correctly reset.

With the PWCR pin connected to the V_O pin, the $\overline{\text{RST}}$ pin stays open when V_{I1} exceeds the detection voltage, and develops a GND level output when V_{I1} is smaller than the detection voltage.

*3. The ripple voltage generated on the boost output voltage can be reduced by connecting response compensation capacitor between the comparator input terminals V_{CONT} and the V_O. (SCI7638M)

ABSOLUTE MAXIMUM RATINGS

(GND=0V, Ta=25°C)

Rating	Symbol	Value	Unit
input voltage	V _{I1}	7	V
Output current	I _O	100	mA
Output voltage	V _O	7	V
Power dissipation	P _d	200 (SOP3-8pin)	mW
Operating temperature	T _{opr}	-30 to 85	°C
Storage temperature	T _{stg}	-65 to 150	°C
Soldering temperature and time	T _{sol}	260°C, 10s (at lead)	—

Note: When SOP 3-8pin is soldered in the solder-reflow process, be sure to maintain the reflow furnace temperature at the curve shown in "Fig. 3-5 Temperature profile for standard SMD package (QFP, SOP, PLCC and etc.)" of DATA BOOK. And SOP3-8pin can not be exposed to high temperature of the solder dipping.

SCI7630M Series

ELECTRICAL CHARACTERISTICS

● SCI7631MAA

(GND=0V, Ta=25°C)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating voltage	V _{I1} V _{I2}	V _O >V _{I2}	0.9 0.9	—	2.0 2.0	V
Output voltage	V _O	V _{I1} =1.5V	4.80	5.00	5.20	V
Detection voltage*	V _{DET}		1.00	1.05	1.10	V
Hysteresis difference of* detection voltage	ΔV _{DET}		—	5	—	%
Operating supply current	I _{DDO}	SCI7631MAA V _{I1} =1.5V	—	10	50	μA
Stand-by supply current	I _{DDS}	V _{I1} =1.5V	—	3	10	μA
Switching transistor on resistance	R _{SWON}	V _{I1} =1.5V V _O =5.0V V _{SW} =0.2V	—	5	10	Ω
Switching transistor leakage current	I _{SWO}	V _{I1} =1.5V V _O =1.5V V _{SW} =7.0V	—	—	0.5	μA
Back-up switch on resistance*	R _{BSON}	V _{I1} =1.0V V _{I2} ≥3.0V I _O =1.0mA	—	50	100	Ω
Back-up switch leakage current*	I _{BSO}	V _{I1} =1.0V V _O =5.0V V _{I2} =3.0V	—	—	0.1	μA
Output current (R _{ST} pin, V _{DETOUT} pin)	I _{OL}	V _{I1} =0.9V V _{DS} =0.2V	0.05	0.15	—	mA
Pull-up current(PS pin)	I _{IH}	V _{I1} =1.5V	—	—	0.5	μA
Booster clock frequency	f _{CLK}	SCI7631M/C _{AA} V _{I1} =1.5V	30	40	50	kHz

SCI7630M Series

● SCI7631M_{BA}, SCI7633M_{BA}

(GND=0V, T_a=25°C)

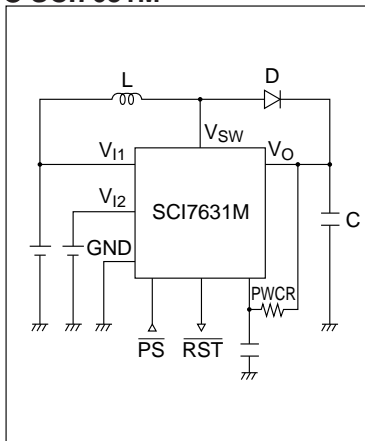
Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Operating voltage	V _{I1} V _{I2}	V _O >V _{I2}	0.9 0.9	—	2.0 2.0	V
Output voltage	V _O	V _{I1} =1.5V	2.90	3.00	3.10	V
Detection voltage *	V _{DET}		1.00	1.05	1.10	V
Hysteresis difference of detection voltage *	ΔV _{DET}		—	5	—	%
Operating supply current	I _{DDO}	SCI7631M/C _{BA} V _{I1} =1.5V	—	8	40	μA
		SCI7633M/C _{BA} V _{I1} =1.5V	—	5	30	
Stand-by supply current	I _{DDS}	V _{I1} =1.5V	—	3	10	μA
Switching transistor on resistance	R _{SWON}	V _{I1} =1.5V V _O =3.0V V _{SW} =0.2V	—	6	12	Ω
Switching transistor leakage current	I _{SWQ}	V _{I1} =1.5V V _O =1.5V V _{SW} =7.0V	—	—	0.5	μA
Back-up switch on resistance*	R _{BSON}	V _{I1} =1.0V V _{I2} =2.0V I _O =1.0mA	—	70	160	Ω
Back-up switch leakage current*	I _{BSQ}	V _{I1} =1.0V V _O =3.0V V _{I2} =2.0V	—	—	0.1	μA
Output current (RST pin, V _{DETOUT} pin)	I _{OL}	V _{I1} =0.9V V _{DS} =0.2V	0.05	0.15	—	mA
Pull-up current (PS pin)	I _{IH}	V _{I1} =1.5V	—	—	0.5	μA
Booster clock frequency	f _{CLK}	SCI7631M/C _{BA} V _{I1} =1.5V	30	40	50	kHz

* The asterisked characteristic is not applicable to the SCI7633M/C_{BA}

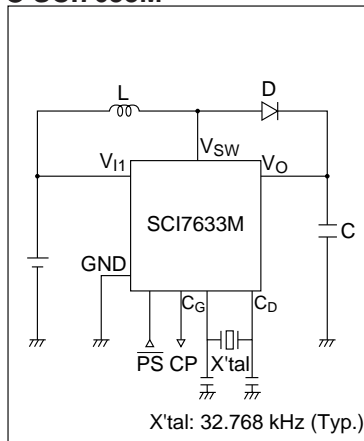
* * For another characteristics, please inquire IC sales section.

■ BASIC EXTERNAL CONNECTION

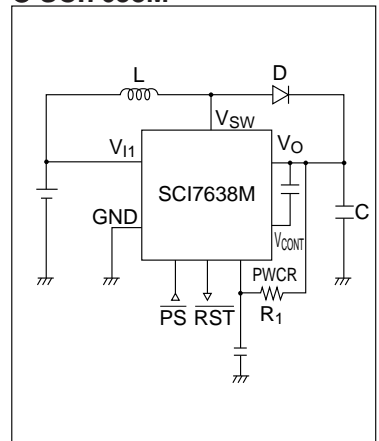
● SCI7631M



● SCI7633M



● SCI7638M



* 100 μH ≤ L ≤ 1mH, C ≥ 10 μF, D: Schottky diode

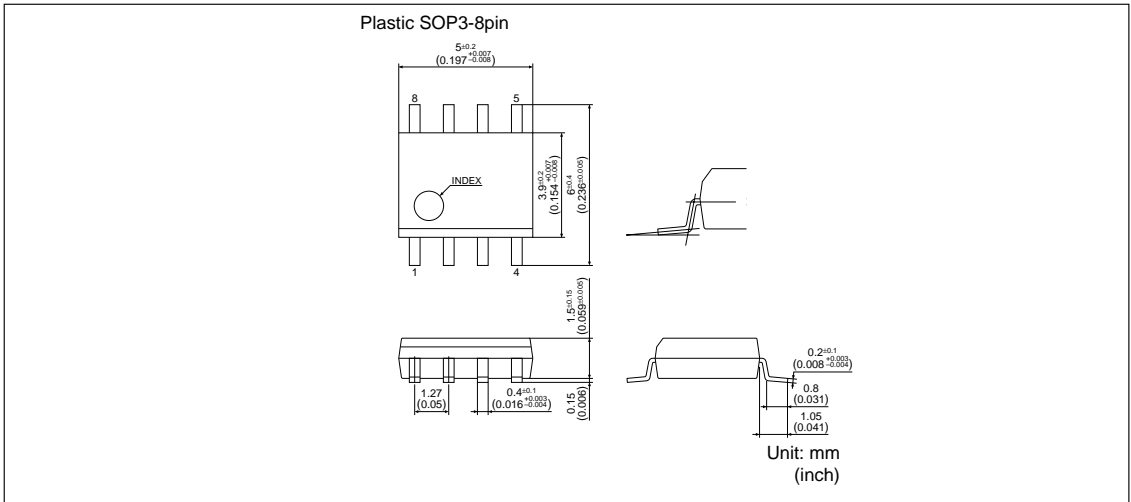
SCI7630M Series

The boosting performance depends on the external parts (such as the inductor) and switching frequency.

- Examples with SCI7631M_{AA}
 - L=220 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=4mA, P_{eff}=70% (leadless inductor used)
 - L=220 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=6mA, P_{eff}=75% (drum type inductor used)
 - L=300 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=7mA, P_{eff}=80% (toroidal inductor used)
- Examples with SCI7631M_{BA} and SCI7633M_{BA}
 - L=220 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=8mA, P_{eff}=70% (leadless inductor used)
 - L=220 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=9mA, P_{eff}=75% (drum type inductor used)
 - L=300 μ H, V_{I1}=1.5V, f_{CLK}=32kHz, I_O=10mA, P_{eff}=80% (toroidal inductor used)

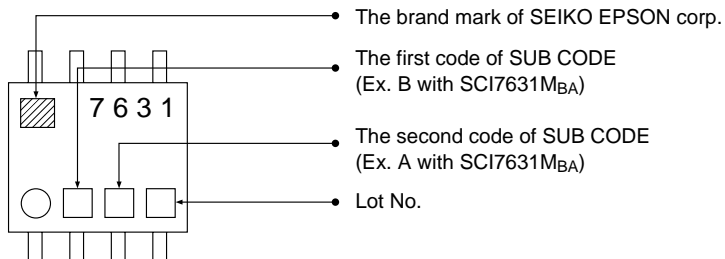
PACKAGE DIMENSIONS

● SCI763xM



MARKING

A subcode is printed on SCI763xM below, because its package is very small.



NOTICE:

No part of this material may be reproduced or duplicated in any form or by any means without the written permission of Seiko Epson. Seiko Epson reserves the right to make changes to this material without notice. Seiko Epson does not assume any liability of any kind arising out of any inaccuracies contained in this material or due to its application or use in any product or circuit and, further, there is no representation that this material is applicable to products requiring high level reliability, such as, medical products. Moreover, no license to any intellectual property rights is granted by implication or otherwise, and there is no representation or warranty that anything made in accordance with this material will be free from any patent or copyright infringement of a third party. This material or portions thereof may contain technology or the subject relating to strategic products under the control of the Foreign Exchange and Foreign Trade Law of Japan and may require an export license from the Ministry of International Trade and Industry or other approval from another government agency.

© Seiko Epson Corporation 2000 All right reserved.

All other product names mentioned herein are trademarks and/or registered trademarks of their respective companies.

SEIKO EPSON CORPORATION

ELECTRONIC DEVICES MARKETING DIVISION

IC Marketing & Engineering Group

ED International Marketing Department I (Europe & U.S.A.)

421-8, Hino, Hino-shi, Tokyo 191-8501, JAPAN

Phone : +81-(0)42-587-5812 FAX : +81-(0)42-587-5564

ED International Marketing Department II (Asia)

421-8, Hino, Hino-shi, Tokyo 191-8501, JAPAN

Phone : +81-(0)42-587-5814 FAX : +81-(0)42-587-5110

■ EPSON Electronic Devices Website

<http://www.epson.co.jp/device/>

