

SBR05U20LP

0.5A SBR[®] Surface Mount Sup<mark>er Barrier Rectifier</mark>

Features

- Ultra Low Forward Voltage Drop
- Superior Reverse Avalanche Capability
- Patented Super Barrier Rectifier Technology
- Soft, Fast Switching Capability
- 150°C Operating Junction Temperature
- Lead Free Finish, RoHS Compliant (Note 1)
- "Green" Molding Compound (No Br, Sb)

Mechanical Data

- Case: DFN1006-2
- Case Material: Molded Plastic, "Green" Molding compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: Cathode Dot
- Terminals: Finish NiPdAu annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 3Ordering Information: See Page 3
- Weight: 0.001 grams

Maximum Ratings @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage	V _{RRM}		
Working Peak Reverse Voltage	V_{RWM}	V _{RWM} 20	
DC Blocking Voltage	V _{RM}	17.	m .
RMS Reverse Voltage	V _{R(RMS)}	14	V
Average Rectified Output Current (See Figure 1)	Io	500	mA
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I _{FSM}	5 WW.Bra	А
Operating and Storage Temperature Range	T _J , T _{STG}	-65 to +150	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic Charac	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 2)	$V_{(BR)R}$	20	-	-	V	Ι _R = 50 μΑ
Forward Voltage Drop	V _F	-	0.34 0.25 0.39 0.31 0.47	0.38 0.28 0.43 0.34 0.50	V W W	$I_F = 0.1A$, $T_J = 25^{\circ}C$ $I_F = 0.1A$, $T_J = 150^{\circ}C$ $I_F = 0.2A$, $T_J = 25^{\circ}C$ $I_F = 0.2A$, $T_J = 150^{\circ}C$ $I_F = 0.5A$, $T_J = 25^{\circ}C$
Leakage Current (Note 2)	I _R	C.COM	0.43 6 1.5	0.46 50 5	μA mA	$I_F = 0.5A, T_J = 150$ °C $V_R = 20V, T_J = 25$ °C $V_R = 20V, T_J = 150$ °C

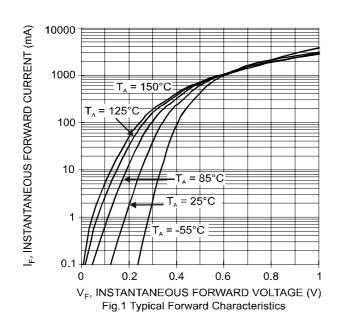
Notes:

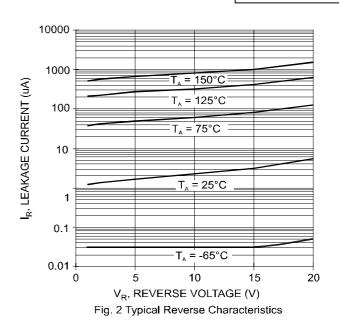
- 1. RoHS revision 13.2.2003. High temperature solder exemption applied, see EU Directive Annex Note 7.
- 2. Short duration pulse test used to minimize self-heating effect.



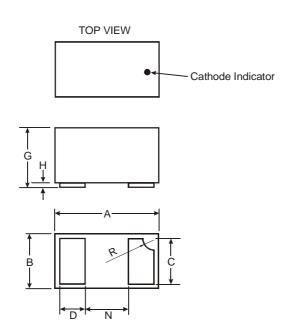


SBR05U20LP





Package Outline Drawing



	DFN1006-2					
Dim	Min	Max	Тур			
Α	0.95	1.075	1.00			
В	0.55	0.675	0.60			
С	0.45	0.55	0.50			
D	0.20	0.30	0.25			
G	0.47	0.53	0.50			
Н	0	0.05	0.03			
N	_	_	0.40			
R	0.05	0.15	0.10			
All Dimensions in mm						



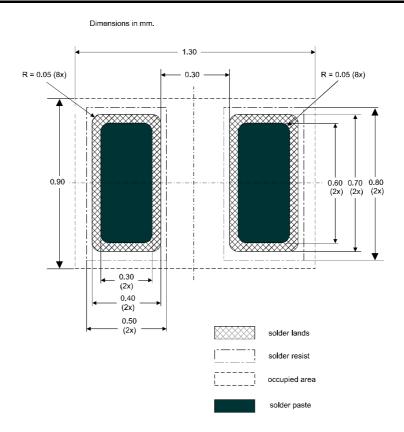
SBR05U20LP

Marking, Polarity, Weight & Ordering Information

Δ.	Case Style (DFN1006-2)		Marking	Weight
SBR05U20L	Top View	Back View	• <u>5</u> 2	0.001g (approx.)

Ordering Information	Date Code
SBR05U20LP-7 3000/Tape & Reel	52 = Product Type Marking Code Dot Denotes Cathode Side

Suggested Pad Layout



IMPORTANT NOTICE

Diodes Incorporated and its subsidiaries reserve the right to make modifications, enhancements, improvements, corrections or other changes without further notice to any product herein. Diodes Incorporated does not assume any liability arising out of the application or use of any product described herein; neither does it convey any license under its patent rights, nor the rights of others. The user of products in such applications shall assume all risks of such use and will agree to hold Diodes Incorporated and all the companies whose products are represented on our website, harmless against all damages.

LIFE SUPPORT

Diodes Incorporated products are not authorized for use as critical components in life support devices or systems without the expressed written approval of the President of Diodes Incorporated.