# **SBM340**

# **3A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER POWERMITE 3**

atures		"n7TP	100	Mr.
Guard Ring Die Construction for Transient Protection				
Low Power Loss, High Efficiency				
Low Forward Voltage Drop		PO	WERMIT	-
For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Applications		— G Dim	4.03	4.09
Lead Free Finish/RoHS Compliant (Note 2)			6.40	6.6
		Î с	.889	NOM
chanical Data		JH D	1.83	NOM
		E	1.10	1.1
Case: POWERMITE 3	1 2	G	.178	NOM
Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0		HANNING HA	5.01	5.1
Moisture sensitivity: Level 1 per J-STD-020C		J	4.37	4.4
Terminals: Solderable per MIL-STD-202, Method 208		` к	.178	NOM
Lead Free Plating (Matte Tin Finish). (3)		L	.71	.77
Polarity: See Diagram	PIN 1 0	M	.36	.46
Marking: Type Number	PIN 2 O PIN 3, I HEAT S	BOTTOMSIDE SINK P	1.73	1.8
Weight: 0.072 grams (approximate)	Note: Pins 1 & 2 must be electr connected at the printed	,	mensions	in mi

# Maximum Ratings @ T<sub>A</sub> = 25 C unless otherwise specified

ingle phase, half wave, 60Hz, resistive or inductive load. for capacitive load, derate current by 20%.			
Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RBM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Rectified Output Current (See also Figure 5)	Io	3	А
Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave Superimposed on Rated Load @ $T_C = 100 C$	I <sub>FSM</sub>	50	A
Typical Thermal Resistance Junction to Soldering Point	R JS	3.4	C/W
Operating Temperature Range	Tj	-55 to +125	С
Storage Temperature Range	T <sub>STG</sub>	-55 to +150	°C

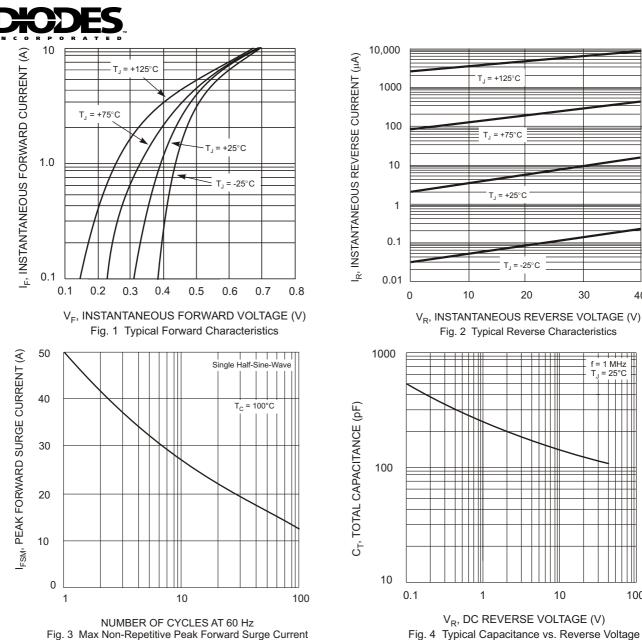
#### @ T<sub>A</sub> = 25 C unless otherwise specified **Electrical Characteristics**

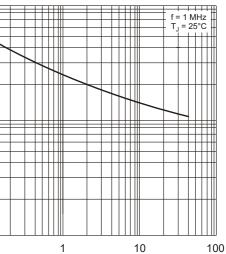
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 1)	V <sub>(BR)R</sub>	40			V	$I_{R} = 0.5 mA$
Forward Voltage	VFM		0.46 0.40 0.57 0.54	0.50 0.44 0.61 0.58	V	$\begin{array}{l} I_F = 3A, \ T_j = \ 25 \ C \\ I_F = 3A, \ T_j = 125 \ C \\ I_F = 6A, \ T_j = \ 25 \ C \\ I_F = 6A, \ T_j = 125 \ C \end{array}$
Reverse Current (Note 1)	I <sub>RM</sub>		15	500 20	A mA	$\begin{array}{l} T_{j} = \ 25 \ C, \ V_{R} = 40V \\ T_{j} = 100 \ C, \ V_{R} = 40V \end{array}$
Total Capacitance	CT		180		pF	f = 1.0MHz, V <sub>R</sub> = 4.0V DC

PDE Short duration test pulse used to minimize self-heating effect. 2 BoHS revision 13.2.2003. High Temperature Solder Exemption Applied, see EU Directive Annex Note 7.

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Notes





T<sub>J</sub> = +125°C

T<sub>J</sub> = +75°C

T<sub>J</sub> = +25°C

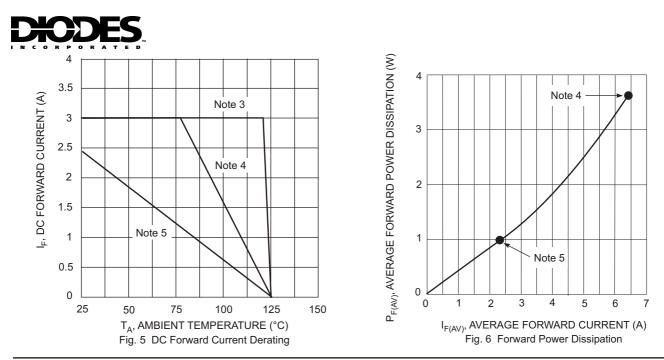
T<sub>J</sub> = -25°C

20

30

40

 $V_R^{}$ , DC REVERSE VOLTAGE (V) Fig. 4 Typical Capacitance vs. Reverse Voltage



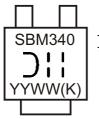
### Ordering Information (Note 6)

Device	Packaging	Shipping
SBM340-13-F	POWERMITE 3	5000/Tape & Reel

Notes: 3.  $T_A = T_{SOLDERING POINT}$ , R  $_{JS} = 3.4$  C/W, R  $_{SA} = 0$  C/W.

- Device mounted on GETEK substrate, 2"x2", 2 oz. copper, double-sided, cathode pad dimensions 0.75" x 1.0", anode pad dimensions 0.25" x 1.0". R JA in range of 20-40°C/W.
- Device mounted on FR-4 substrate, 2"x2", 2 oz. copper, single-sided, pad layout as per Diodes Inc. suggested pad layout document AP02001 which can be found on our website at http://www.diodes.com/datasheets/ap02001.pdf. R JA in range of 95-115°C/W.
- 6. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

### **Marking Information**



SBM340 = Product type marking code ) | = Manufacturers' code marking YYWW = Date code marking YY = Last digit of year ex: 02 for 2002 WW = Week code 01 to 52 (K) = Factory Designator

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