

# RJK5015DPK

# Silicon N Channel MOS FET High Speed Power Switching

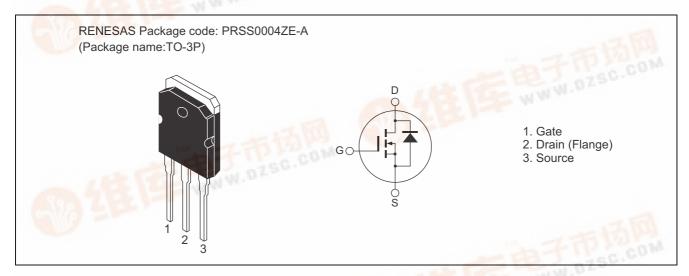
REJ03G1360-0200 Rev.2.00 Apr 18, 2007

WWW.DZSG

### **Features**

- Low on-resistance
- Low leakage current
- High speed switching

### **Outline**



### **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	500	V
Gate to source voltage	$V_{GSS}$	±30	V
Drain current	I <sub>D</sub>	25	A
Drain peak current	I <sub>D (pulse)</sub> Note1	75	A
Body-drain diode reverse drain current	I <sub>DR</sub>	25	A
Body-drain diode reverse drain peak current	I <sub>DR</sub> (pulse)	75	A
Avalanche current	I <sub>AP</sub> Note3	7	Α
Avalanche energy	E <sub>AR</sub> Note3	2.7	mJ
Channel dissipation	Pch Note2	150	W
Channel to case thermal impedance	θch-c	0.833	°C/W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

- 2. Value at Tc = 25°C
- 3. STch =  $25^{\circ}$ C, Tch  $\leq 150^{\circ}$ C



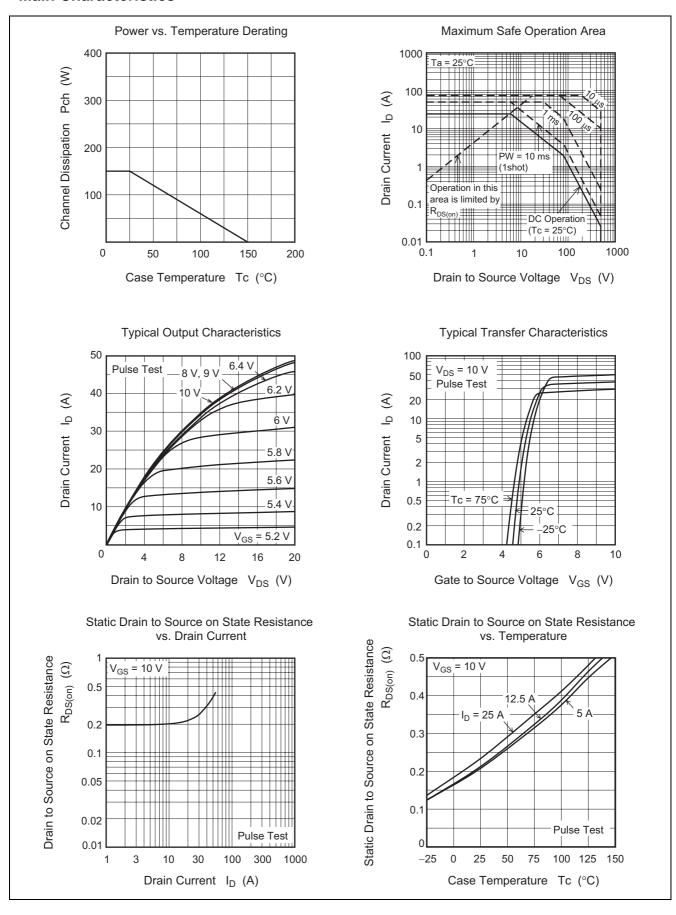
### **Electrical Characteristics**

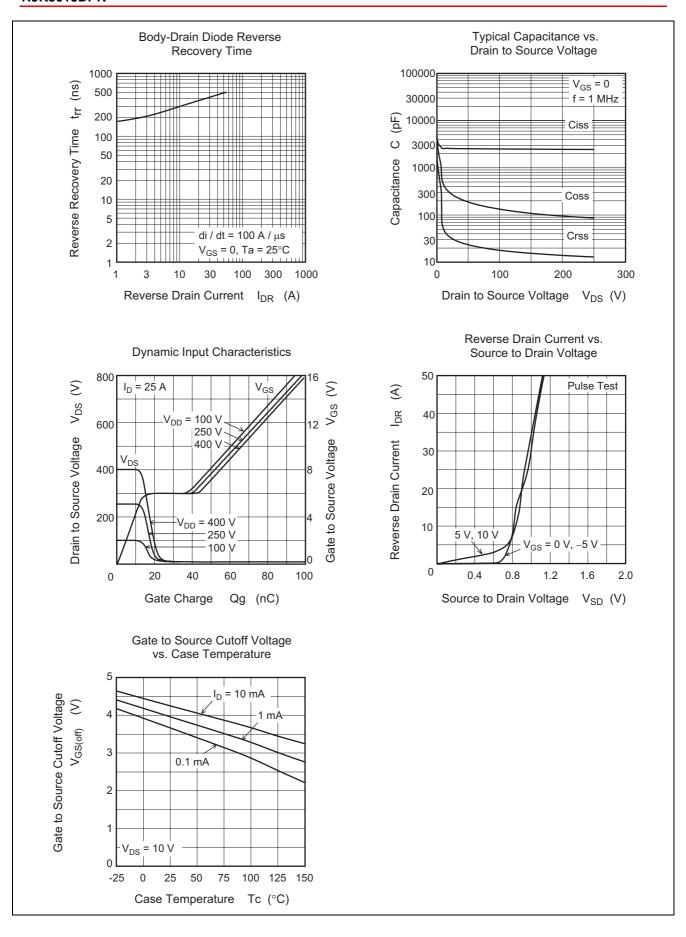
 $(Ta = 25^{\circ}C)$ 

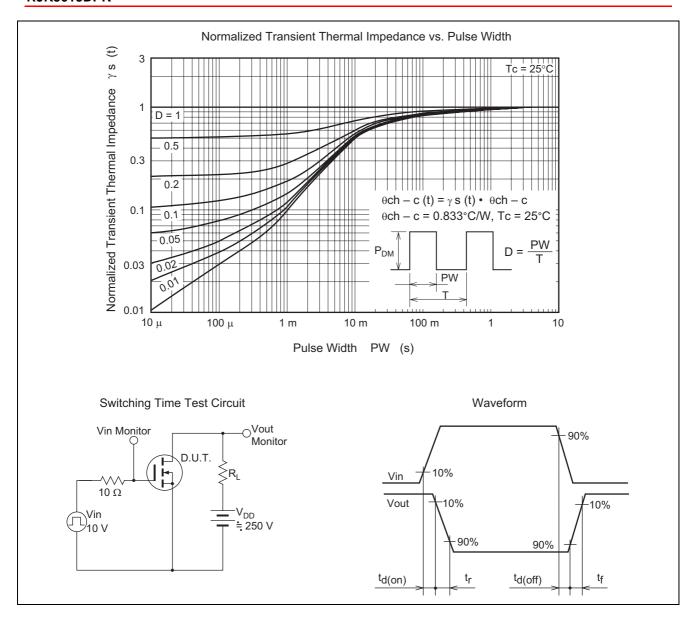
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	500	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>	_	_	1	μΑ	$V_{DS} = 500 \text{ V}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±0.1	μΑ	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	3.0	_	4.5	V	$V_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	0.21	0.24	Ω	$I_D = 12.5 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$
resistance						
Input capacitance	Ciss	_	2600	_	pF	V <sub>DS</sub> = 25 V
Output capacitance	Coss	_	270	_	pF	V <sub>GS</sub> = 0 f = 1 MHz
Reverse transfer capacitance	Crss	_	32	_	pF	
Turn-on delay time	t <sub>d(on)</sub>	_	42	_	ns	I <sub>D</sub> = 12.5 A V <sub>GS</sub> = 10 V
Rise time	t <sub>r</sub>	_	59	_	ns	
Turn-off delay time	t <sub>d(off)</sub>	_	103	_	ns	$R_L = 20 \Omega$
Fall time	t <sub>f</sub>	_	45	_	ns	$Rg = 10 \Omega$
Total gate charge	Qg	_	66	_	nC	V <sub>DD</sub> = 400 V
Gate to source charge	Qgs	_	14	_	nC	V <sub>GS</sub> = 10 V
Gate to drain charge	Qgd	_	29	_	nC	I <sub>D</sub> = 25 A
Body-drain diode forward voltage	$V_{DF}$	_	0.96	1.60	V	$I_F = 25 \text{ A}, V_{GS} = 0^{\text{Note4}}$
Body-drain diode reverse recovery time	trr	_	380	_	ns	I <sub>F</sub> = 25 A, V <sub>GS</sub> = 0
						$di_F/dt = 100 A/\mu s$

Notes: 4. Pulse test

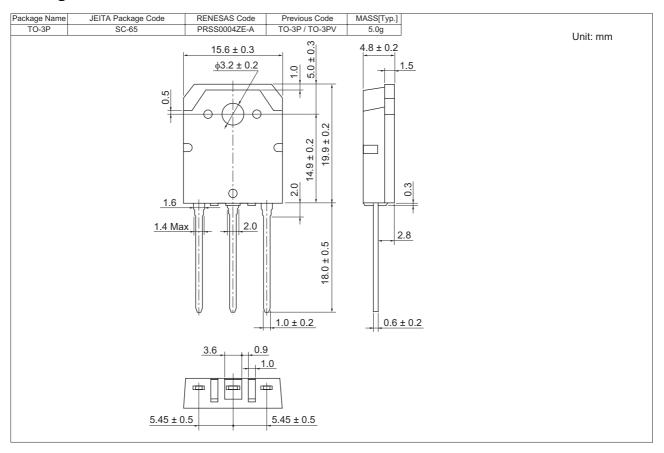
### **Main Characteristics**







## **Package Dimensions**



# **Ordering Information**

Part No.	Quantity	Shipping Container
RJK5015DPK-00-T0	360 pcs	Box (Tube)

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