CDMA/FM RECEIVE AGC AMPLIFIER

Typical Applications

- CDMA/FM Cellular Systems
- Supports Dual-Mode AMPS/CDMA
- Supports Dual-Mode TACS/CDMA
- General Purpose Linear IF Amplifier

WWW.BZSG.

- Commercial and Consumer Systems
- Portable Battery Powered Equipment

Product Description

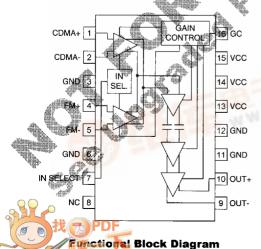
The RF9907 is a complete AGC amplifier designed for the receive section of dual-mode CDMA/FM cellular applications. It is designed to amplify IF signals while providing over 90dB of gain control range. Noise Figure, IP3, and other specifications are designed to be compatible with the IS-95 Interim Standard for CDMA cellular communications. This circuit is designed as part of the RFMD CDMA Chip Set, consisting of a Transmit IF AGC Amp, a Transmit Upconverter, a Receive LNA/Mixer, and this Receive IF AGC Amp. The IC is manufactured on an advanced Gallium Arsenide Heterojunction Bipolar Transistor (HBT) process, and is packaged in a standard miniature 16-lead plastic SSOP package.

.010

Package Style: SSOP-16

Optimum Technology Matching™ Applied

- ☐ Si BJT Si Bi-CMOS
- GaAs HBT
- ☐ GaAs MESFET



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Features

- Supports Dual Mode Operation
- -45dB to +45dB Gain Control Range
- Single 3.6V Power Supply
- Digitally Selectable Inputs
- >-5dBm Input IP₃
- 12MHz to 285MHz Operation

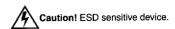
Ordering Information

RF9907 RF9907 PCBA CDMA/FM Receive AGC Amplifier Fully Assembled Evaluation Board

RF Micro Devices, Inc. 7625 Thorndike Boad Greensboro, NC 27409, USA

Tel (910) 664 1233 Fax (910) 664 0454 http://www.rfmd.com 10

Parameter	Rating	Unit
Supply Voltage	-0.5 to +7.0	V _{DC}
Control Voltage	-0.5 to +5.0	V _{DC}
Input RF Power	+10	dBm
Operating Ambient Temperature	-40 to +85	~℃
Storage Temperature	-40 to +150	~C



RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does

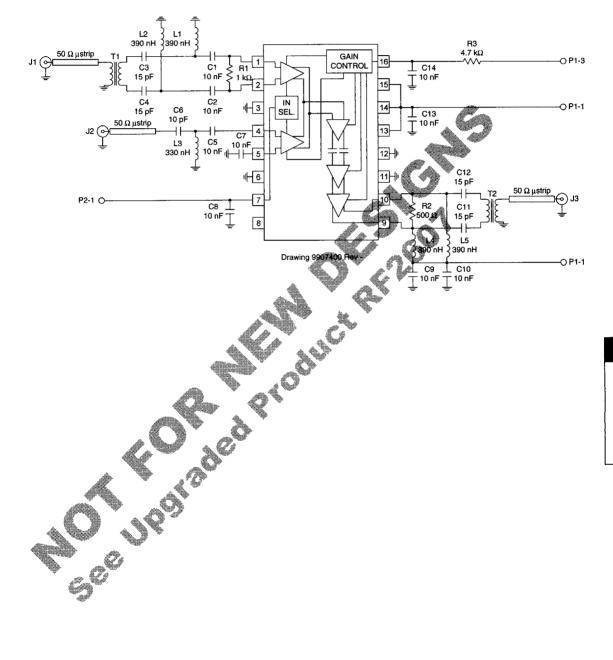
Davamatar		Specification		Unit	Condition		
Parameter	Min. Typ.		Max.	Offic			
Overall					T=25°C, 85MHz, $V_{\rm CC}$ =3.6V, $Z_{\rm S}$ =500Ω, $Z_{\rm L}$ =500Ω 500Ω External CDMA Input Terminating Resistor, 500Ω External Output Terminating Resistor (Effective $Z_{\rm S}$ =333Ω, Effective $Z_{\rm L}$ =250Ω) (See Application Example)		
Frequency Range		12 to 285		MH≇	Lample)		
CDMA Maximum Gain	+45	+55		₫B	V _{GC} =2.9V		
CDMA Minimum Gain	140	-50	-45	de	V _{GC} =0.2¥		
FM Maximum Gain	+45	+55	٠,	d₿	V _{9C} =2.9V		
FM Minimum Gain	743	-50	-45	dΒ	V _{GC} =0.2V		
	30	-30 47	60	dB/V	Measured in 0.5V increments		
Gain Slope Gain Control Voltage Range	30	0 to 3	00	V.==	Source impedance of 4.7kΩ		
		30		V DC	Source impodence of the task		
Gain Control Input Impedance		30		K28			
Noise Figure		5.		dB	At maximum gain and 85MHz		
Input IP3	-44	-38	4	dBm	At +40 gain, referenced to 500 Ω		
Stability (Max VSWR)	10:1			>	Spurious<-70 dBm		
IF Input							
Input Impedance		1		kΩ	CDMA, differential		
Input Impedance	.40	850		Ω	FM, single-ended		
CDMA to FM Isolation		30		dB			
Power Supply							
Voltage		3.6± 5%		v			
Current Consumption		13	17	mA	Minimum gain		
Current Consumption		14	17	mA	Maximum gain		

Pin	Function	Description	Interface Schematic
1	CDMA+	CDMA Balanced Input Pin. This pin is internally DC biased and should be DC blocked if connected to a device with a DC level other than $V_{\rm CC}$	
		present. A DC to connection to V _{CC} is acceptable. For single-ended	
		input operation, one pin is used as an input and the other CDMA input	
		is AC coupled to ground. The balanced input impedance is 1 k Ω , while the single-ended input impedance is 500 Ω	
2	CDMA-	Same as pin 1, except complementary input.	
3	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.	
4	FM+	FM Balanced Input Pin. This pin is internally DC biased and should be	
		DC blocked if connected to a device with DC present. For single-ended input operation, one pin is used as an input and the other FM input is	
		AC coupled to ground. The balanced input impedance is $1.7k\Omega$, while	\
		the single-ended input impedance is 850Ω .	
5	FM-	Same as pin 4, except complementary input.	
6	GND	Same as pin 3.	
7	IN SELECT	Selects which IF input (CDMA or FM) is used. This is a digitally con-	
		trolled input. A logic "high" selects the CDMA input amplifier. A logic "low" selects the FM input amplifier. The threshold voltage is approxi-	
		mately 1.3V.	
8	NC	No Connection pin. This pin is internally biased and should not be connected to any external circuitry, including ground or N_{CC} .	
9	OUT-	Balanced Output pin. This is an open-collector output, designed to	
		operate into a 250 Ω balanced load. The load sets the operating imped-	
		ance, but an external choke or matching inductor to V_{CC} must also be supplied in order to correctly bias tals output. This bias inductor is typi-	
		cally incorporated in the matching network between the output and next	
		stage. Because this pin is based to V _{CC} , a DO blocking capacitor must	
		be used if the next stage's input has a DC path to ground.	
10	OUT+	Same as pin 9, except complementary output.	
11	GND	Same as pin 3	
12	GND	Same as pin 3.	
13	vcc	Supply Voltage pin. External typassing is required. The trace length between the pin and the bypass capacitors should be minimized. The	
		ground side of the bypass capacitors should connect immediately to	
		ground plane.	
14	VCC 🎤	Same as pin 13	
15	VCC	Same as pig 13.	
16	GC	Analog can adjustment for all amplifiers. Valid control ranges are from 0V to 3.0V. Maximum gain is selected with 3.0V. Minimum gain is	
		selected with 0V. These voltages are only valid for a 4.7 k Ω DC source	
		impedance.	
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	4000		

R2 sets the balanced output impedance to 500 Ω. L1 and C2 serves as as series DC block. In addition, the values of \$1 strong C2 serves as a series DC block. In addition, the values of \$1 strong C2 serves as a series DC block. In addition, the values of \$1 strong C2 serves as a series DC block. In addition, the values of \$1 strong C2 serves as a series DC block. In addition, the values of L1 and C1 are chosen to form a parallel resonant tank circuit at the IF when the load impedance is 500 Ω.

Measurement

Evaluation Board Schematic





PACKAGE DRAWINGS

Package Drawing		Dimonoiou	Measurement (mm)		Measurement (inches)	
		Dimension	Typical	Tolerance	Typical	Tolerance
QLCC-16		Α	6.35	±0.20	0.250	±0.008
- A	- к	В	6.35	±0.20	0.250	±0.008
	ļ_Fi	c	1.27		0.050	
	=	D	Radius 0.20		Radius 0.008	d.
E B		E	3.81		0.150	ĺ
	L- <u>-</u>	F	0.64	±0.20	0.025	±0.008
	.	G	0.64		0.025	
5 101	[н	2.49		0.098	
		J	2.49		0.098	
F G		к	1.78	±0.13	0.070	±0.005
		L	0.51	±0.05	0.020	±0.002
SOP-8		Α	3.90	±0.05	0.154	±0.002
A	- - J	В	0.43		0.017	
B	r al	С	1.27		0.050	
		D	6.00	±0.10	0.236	±0.004
<u> </u>	<u> </u>	E	4.90	±0.20	0.193	±0.008
D		F	0.50	±0.05	0.020	±0.002
5	→	G	0.20		0.008	
		Н	1.37	±0.05	0.054	±0.002
		J	0.10 Min		0.004 Min	
\F		θ	5°		5°	
SOP-14			2.00			
A	- - J	A B	3.90	±0.09	0.154	±0.004
B B		C	0.41 1.27	±0.05	0.016	±0.002
		D	6.00	.0.00	0.050	
€ □ □ □ □	<u> </u>	E	8.65	±0.20	0.236	±0.008
		F	0.84	±0.09 ±0.43	0.340	±0.003
		G	0.84	±0.43 ±0.03	0.033 0.009	±0.017
		н	1.61	±0.03 ±0.09	0.009	+0.001, -0.002
D ——	→ → н	J	0.18	+0.07, -0.08	0.003	+0.004, -0.003 ±0.003
6 °		в	0° to 8°	. 0.07, 0.00	0.007 0° to 8°	10.003
F		-			0 10 6	
· · · · · · · · · · · · · · · · · · ·					ĺ	

		Measurer	nent (mm)	Measurem	urement (inches)	
Package Drawing	Dimension	Typical	Tolerance	Typical	Tolerance	
SOP-16	А	3.90	±0.09	0.154	±0.004	
A - - J	В	0.41	±0.05	0.016	±0.002	
T III III III III III III III III III I	c	1.27		0.050		
	D	6.00	±0.20	0.236	±0.008	
	E	9.89	±0.09	0.390	+0.003, -0.004	
	F	0.84	±0.43	0.033	±0.017	
	G	0.22	±0.03	0.009	+0.001, -0.002	
	н	1.41	±0.09	0.063	+0.004, -0.003	
	ا ن	0.18	+0.07, -0.08	0.007	±0.003	
	θ	0° to 8°		0° to 8°		
9° - F G						
SOP-20	Α	7.50	±0.10	0.295	±0.004	
J — A — — — — — — — — — — — — — — — — —	В	0.41	±0.05	0.016	±0.002	
B	С	1.27	i	0.050		
	D	10.31	±0.20	0.406	±0.008	
	E	12.75	±0.10	0.502	±0.004	
	F	0.76	±0.25	0.030	±0.010	
	G	0.27	±0.05	0.011	±0.002	
	н	2.34		0.092		
	J	0.20	±0.10	0.008	±0.004	
	θ	0° to 8°		0° to 8°		
e						
PSOP-8	Α	3.91	+0.08, -0.10	0.154	+0.003, -0.004	
- A - B	В	0.41	±0.08	0.016	±0.003	
	С	1.27		0.050		
	D	6.02	±0.18	0.237	±0.007	
	E	4.90	+0.08, -0.10	0.193	+0.003, -0.004	
	F	0.64	+0.25, -0.23	0.025	+0.010, -0.009	
р р н н н	G	0.23	+0.03, -0.05	0.009	+0.001, -0.002	
EXPOSED HEATSINK	н	1.47	±0.08	0.058	±0.003	
e° unit	J	0.18	±0.08	0.007	±0.003	
K	К	2.92	±0.20	0.115	±0.008	
G III	L	2.00	±0.20	0.079	±0.008	
	θ	0° to 8°		0° to 8°		

	l =	Measurement (mm)		Measurement (inches)	
Package Drawing	Dimension	Typical	Tolerance	Typical	Tolerance
SOP-8-C	Α	7.87	±0.13	0.310	±0.005
— A — — H —	В	4.22	Square	0.166	Square
	С	0.38	±0.05	0.015	±0.002
l C	D	4.57	Max	0.180	Max
	E	1.27		0.050	
	F	0.38	±0.05	0.015	±0.002
D —	G	0.13	±0.03	0.005	±0.001
	н	1.45	Max	0.057	Max
0°	J	0 to 0.10		0 to 0.004	
	θ	0° to 4°		0° to 4°	
→ F G J					
SOP-16 Batwing	Α	3.91	±0.10	0.154	±0.004
- A	В	0.43	±0.08	0.017	±0.003
B	С	1.27		0.050	
	D	6.02	±0.18	0.237	±0.007
	E	9.88	±0.08	0.389	±0.003
	F	0.64	+0.25, -0.23	0.025	+0.010, -0.009
	G	0.23	±0.03	0.009	±0.001
- C	н	1.45	+0.05, -0.08	0.057	+0.002, -0.003
	J	0.17	+0.06, -0.08	0.007	+0.002, -0.003
	К	1.69	+0.06, -0.07	0.067	+0.002, -0.003
	θ	0° to 8°		0° to 8°	
do					
		0.04	.0.10	0.151	±0.004
SOP-16 QBW1	A	3.91	±0.10	0.154 0.017	±0.004
B	В	0.43 1.27	±0.08	0.017	10.003
	i D	6.02	±0.18	0.030	±0.007
	K E	9.88	±0.18	0.389	±0.003
	'	0.64	+0.25, -0.23	0.025	+0.010, -0.009
	G	0.23	±0.03	0.009	±0.001
	н	1.45	+0.05, -0.08	0.057	+0.002, -0.003
	J	0.17	+0.05, -0.08	0.007	+0.002, -0.003
	K	1.69	+0.06, -0.07	0.067	+0.002, -0.003
	θ	0° to 8°	10.00, 0.07	0° to 8°	
(0°					
F G					