



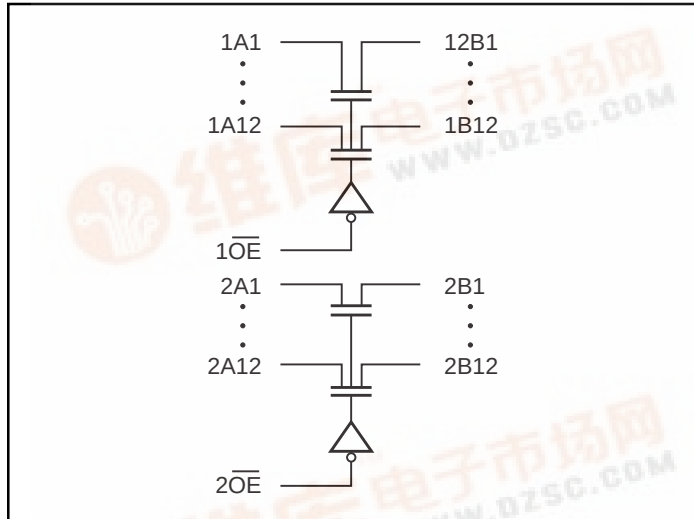
PI3B16211

3.3V, Hot Insertion, 24-Bit BusSwitch

Product Features

- Near-zero propagation delay
- 5-ohm switches connect inputs to outputs
- Fast Switching Speed - 4.5ns max.
- Permits Hot Insertion
- VCC operating range: 3.0V to 3.6V
- Industrial operating temperature: -40°C to +85°C
- Packages available:
 - 56-pin 240-mil wide thin plastic TSSOP (A)
 - 56-pin 300-mil wide plastic SSOP (V)
 - 56-pin 173-mil wide thin plastic TVSOP (K)

Logic Block Diagram



Truth Table

$\overline{1OE}$	$\overline{2OE}$	1A, 1B I/Os	2A, 2B I/Os
L	L	1A = 1B	2A = 2B
L	H	1A = 1B	Z
H	L	Z	2A = 2B
H	H	Z	Z

Note:

1. H=High Voltage Level
L=Low Voltage Level
Z=High Impedance

Product Pin Description

Pin Name	I/O	Description
$\overline{1OE}$, $\overline{2OE}$	I	Select Inputs
1A, 1B	I/O	Bus A
2A, 2B	I/O	Bus B

Product Description

Pericom Semiconductor's PI3B series of logic circuits are produced using the Company's advanced submicron CMOS technology.

The PI3B16211 is a 3.3 volt, hot insertion, 24-bit bus switch designed with a low ON resistance allowing inputs to be connected directly to outputs. This device operates as a 24-bit or as a 12-bit bus switch that provides high-speed bus switching.

Product Pin Configuration

NC	1	56	$\overline{1OE}$
1A1	2	55	$\overline{2OE}$
1A2	3	54	1B1
1A3	4	53	1B2
1A4	5	52	1B3
1A5	6	51	1B4
1A6	7	50	1B5
GND	8	49	GND
1A7	9	48	1B6
1A8	10	47	1B7
1A9	11	46	1B8
1A10	12	45	1B9
1A11	13	44	1B10
1A12	14	43	1B11
2A1	15	42	1B12
2A2	16	41	2B1
VCC	17	40	2B2
2A3	18	39	2B3
GND	19	38	GND
2A4	20	37	2B4
2A5	21	36	2B5
2A6	22	35	2B6
2A7	23	34	2B7
2A8	24	33	2B8
2A9	25	32	2B9
2A10	26	31	2B10
2A11	27	30	2B11
2A12	28	29	2B12





Maximum Ratings

(Above which the useful life may be impaired. For user guidelines, not tested.)

Storage Temperature	–65°C to +150°C
Ambient Temperature with Power Applied	–0°C to +85°C
Supply Voltage Range	–0.5V to +4.6V
DC Input Voltage	–0.5V to +4.6V
DC Output Current	120mA
Power Dissipation	0.5W

Note:

Stresses greater than those listed under MAXIMUM RATINGS may cause permanent damage to the device. This is a stress rating only and functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect reliability.

DC Electrical Characteristics (Over the Operating Range, $T_A = -40^{\circ}\text{C}$ to $+85^{\circ}\text{C}$, $V_{CC} = 3.0\text{V}$ to 3.6V)

Parameters	Description	Test Conditions ⁽¹⁾	Min.	Typ. ⁽²⁾	Max.	Units
V_{IH}	Input HIGH Voltage	Guaranteed Logic High Level	2.0			V
V_{IL}	Input LOW Voltage	Guaranteed Logic Low Level	–0.5		0.8	
I_{IH}	Input HIGH Current	$V_{CC} = \text{Max.}; V_{IN} = V_{CC}$			± 1	μA
I_{IL}	Input LOW Current	$V_{CC} = \text{Max.}; V_{IN} = \text{GND}$			± 1	
I_{OZH}	High Impedance Output Current	$0 \leq A, B \leq V_{CC}$			± 1	
V_{IK}	Clamp Diode Voltage	$V_{CC} = \text{Min.}, I_{IN} = -18\text{mA}$		–0.7	–1.2	V
R_{ON}	Switch ON Resistance ⁽³⁾	$V_{CC} = \text{Min.}, V_{IN} = 0.0\text{V}, I_{ON} = 48\text{mA}$ or 64mA $V_{CC} = \text{Min.}, V_{IN} = 2.4\text{V}, I_{ON} = 15\text{mA}$		5 10	8 15	Ω

Capacitance ($T_A = 25^{\circ}\text{C}$, $f = 1\text{MHz}$)

Parameters ⁽⁵⁾	Description	Test Conditions	Typ.	Units
C_{IN}	Input Capacitance	$V_{IN} = 0\text{V}$	3.0	pF
C_{OFF}	A/B Capacitance, Switch Off		8.5	
C_{ON}	A/B Capacitance, Switch On		17.0	

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical values are at $V_{CC} = 3.3\text{V}$, $T_A = 25^{\circ}\text{C}$ ambient and maximum loading.
3. Measured by the voltage drop between A and B pin at indicated current through the switch. ON resistance is determined by the lower of the voltages on the two (A,B) pins.
4. This parameter is determined by device characterization but is not production tested.



Power Supply Characteristics

Parameters	Description	Test Conditions ⁽¹⁾		Min.	Typ ⁽²⁾	Max.	Units
I _{CC}	Quiescent Power Supply Current	V _{CC} = Max.	V _{IN} = GND or V _{CC}			10	μA
ΔI _{CC}	Supply Current per Input @ TTL High	V _{CC} = Max.	V _{IN} = 3.0V ⁽³⁾			750	
I _{CCD}	Supply Current per Input per MHz ⁽⁴⁾	V _{CC} = Max. A & B Pin Open Control Input Toggling 50% Duty Cycle				0.25	mA/ MHz

Notes:

1. For Max. or Min. conditions, use appropriate value specified under Electrical Characteristics for the applicable device.
2. Typical values are at V_{CC} = 3.3V, +25°C ambient.
3. Per TTL driven input (control inputs only); A and B pins do not contribute to I_{CC}.
4. This current applies to the control inputs only and represent the current required to switch internal capacitance at the specified frequency. The A and B inputs generate no significant AC or DC currents as they transition. This parameter is not tested, but is guaranteed by design. R_L = 500 ohms, R = 500 ohms⁽⁴⁾

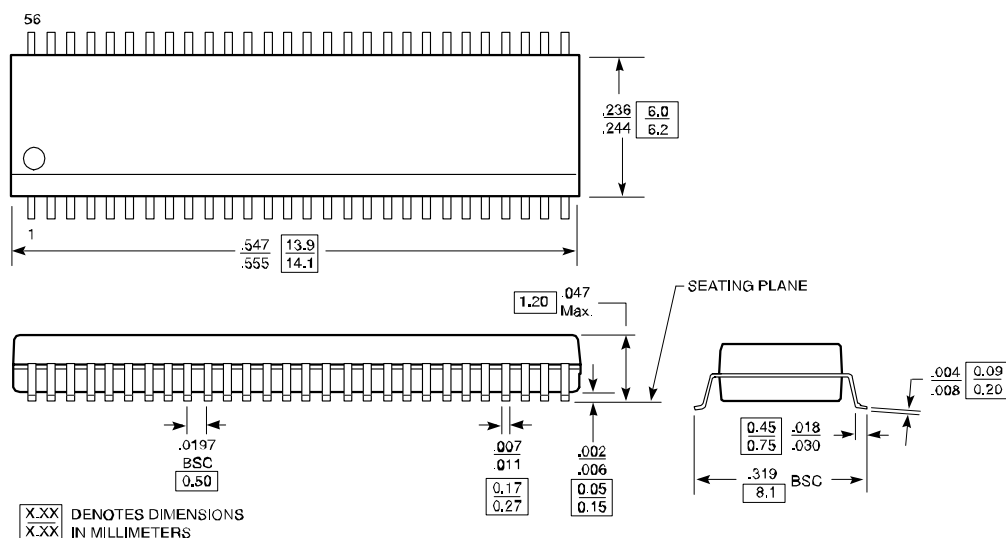
Switching Characteristics over Operating Range

Parameters	Description	Conditions ⁽¹⁾	Com.		Units
			Max.	Min.	
t _{PLH} t _{PHL}	Propagation Delay ^(2,3) Ax to Bx, Bx to Ax	C _L = 50pF, R _L = 500 ohms R = 500 ohms		0.25	ns
t _{PZH} t _{PZL}	Bus Enable Time BE to Ax or Bx		1	4.5	
t _{PHZ} t _{PLZ}	Bus Disable Time BE to Ax or Bx		1	5.0	

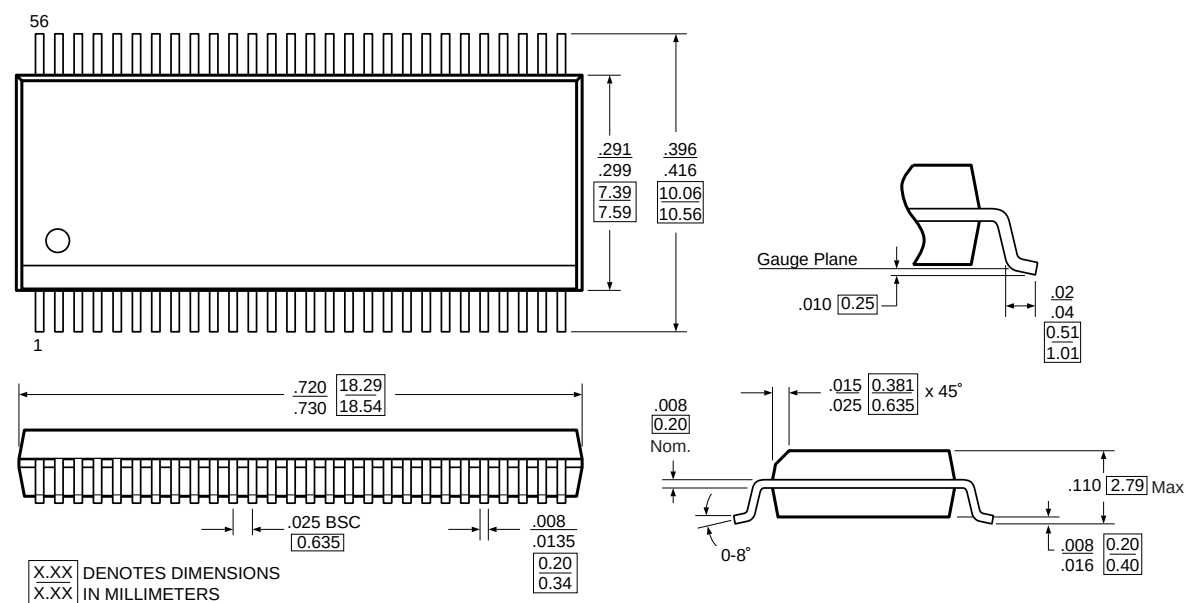
Notes:

1. See test circuit and waveforms.
2. This parameter is guaranteed but not tested on Propagation Delays.
3. The bus switch contributes no propagational delay other than the RC delay of the ON resistance of the switch and the load capacitance. The time constant for the switch alone is of the order of 0.25ns for 50pF load. Since this time constant is much smaller than the rise/fall times of typical driving signals, it adds very little propagational delay to the system. Propagational delay of the bus switch when used in a system is determined by the driving circuit on the driving side of the switch and its interaction with the load on the driven side.
4. Applies to t_{PZX}, t_{PXZ}.

56-Pin TSSOP (A) Package



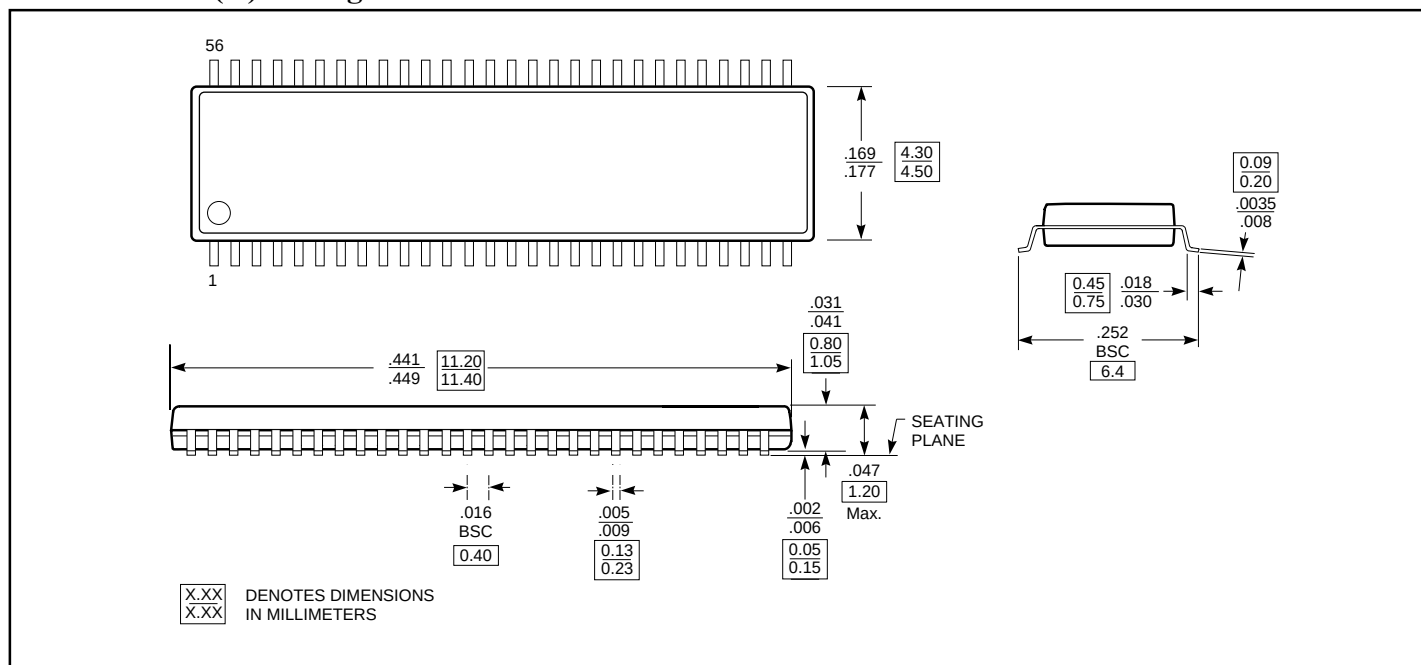
56-Pin SSOP (V) Package





PI3B16211
3.3V, Hot Insertion,
24-Bit Bus Switch

56-Pin TVSOP (K) Package



Part	Pin	Package	Width	Temperature
PI3B16211A	56	TSSOP	240-mil	-40°C to 85°C
PI3B16211V		SSOP	300-mil	
PI3B16211K		TVSOP	173-mil	