

# PF0015

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MOS FET Power Amplifier Module for Handy Mobile Phone

加急出货

**MOS FET Power Amplifier  
FOR AMPS 824 – 849 MHz**

## FEATURES

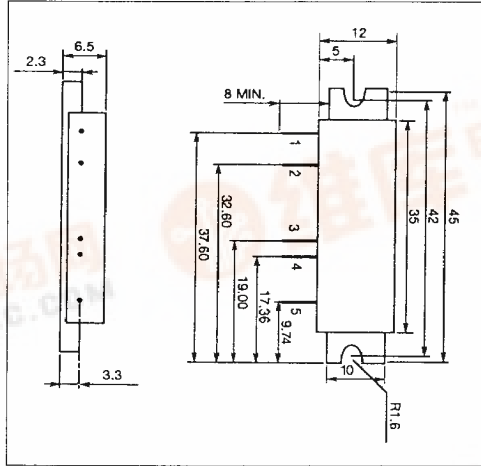
- Small outline 12 × 45 × 6.5 mm<sup>3</sup>
- Low voltage operation 6V
- Low power control current 300 μA
- High stability load VSWR ≥ 20

## ABSOLUTE MAXIMUM RATINGS (Ta = 25°C)

Item	Symbol	Rating	Unit
Supply Voltage	V <sub>DD</sub>	12	V
Supply Current	I <sub>DD</sub>	2	A
APC Voltage	V <sub>APC</sub>	± 8	V
Input Power	P <sub>in</sub>	20	mW
Operating Case Temperature	T <sub>C(op)</sub>	-30 ~ +100	°C
Storage Temperature	T <sub>stg</sub>	-30 ~ +100	°C

The absolute maximum ratings are limiting values, to be applied individually, beyond which the device may be permanently damaged. Functional operation under any of these conditions is not guaranteed. Exposing a circuit to its absolute maximum rating for extended periods of time may affect the device's reliability.

## OUTLINE DRAWING

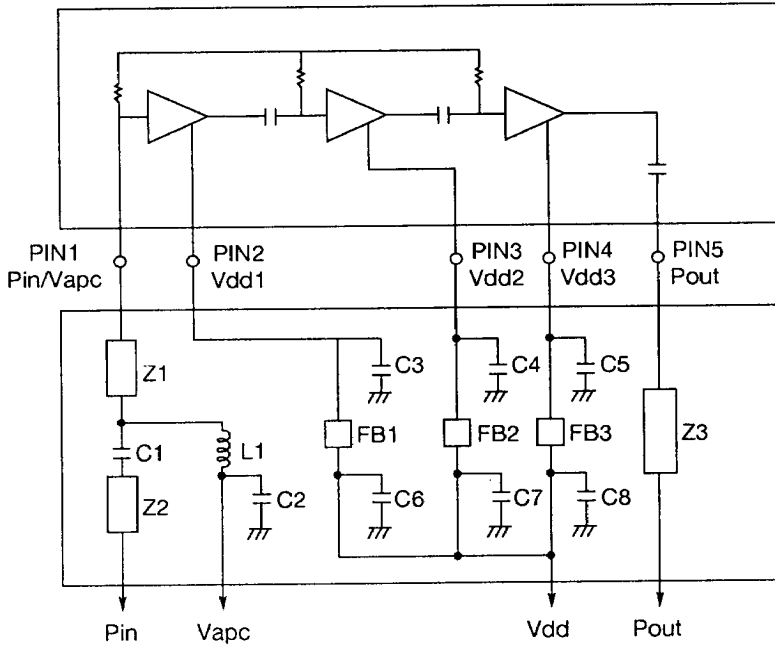


## ELECTRICAL CHARACTERISTICS (Ta = 25°C)

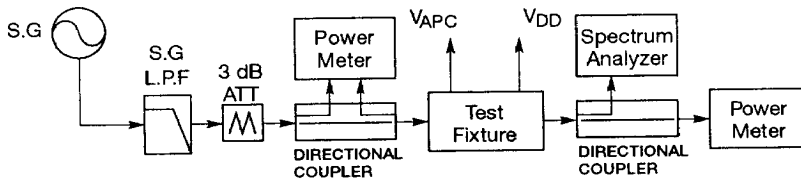
Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain Cutoff Current	I <sub>DS</sub>	V <sub>DD1</sub> = V <sub>DD2</sub> = V <sub>DD3</sub> = 12V, V <sub>apc</sub> = 0V	—	—	100	μA
Total Efficiency	η <sub>T</sub>	f = 824, 849 MHz, P <sub>in</sub> = 1 mW, V <sub>DD1</sub> = V <sub>DD2</sub> = V <sub>DD3</sub> = 6V, P <sub>out</sub> = 1.2W (at APC Control), Z <sub>in</sub> = Z <sub>out</sub> = 50 Ω	35	40	—	%
2nd Harmonic Distortion	2nd H.D.		—	-40	-30	dB
3rd Harmonic Distortion	3rd H.D.		—	-50	-30	dB
Input VSWR	VSWR(in)		—	1.8	3	—
Output VSWR	VSWR(out)		—	2	—	—
Stability	—	V <sub>DD1</sub> = V <sub>DD2</sub> = V <sub>DD3</sub> = 6V, P <sub>in</sub> = 1 mW, f = 824 MHz, R <sub>g</sub> = 50 Ω, P <sub>out</sub> = 1.2W (at APC Control), Output VSWR = 20 All Phases, t = 20 sec	No Parasitic Oscillation			—



■ TEST SYSTEM DIAGRAM



- C<sub>1</sub> = 0.02 μF Ceramic Chip
- C<sub>2</sub>, C<sub>3</sub>, C<sub>4</sub>, C<sub>5</sub> = 0.01 μF Ceramic Dip
- C<sub>6</sub>, C<sub>7</sub>, C<sub>8</sub> = 10 μF Tantalum
- L<sub>1</sub> = RFC 1mm φ, 15 turns
- FB = Ferrite Bead BL01RN1-A62-001 (MURATA) or equivalent
- Z<sub>1</sub>, Z<sub>2</sub>, Z<sub>3</sub> = 50 Ω Microstrip Line

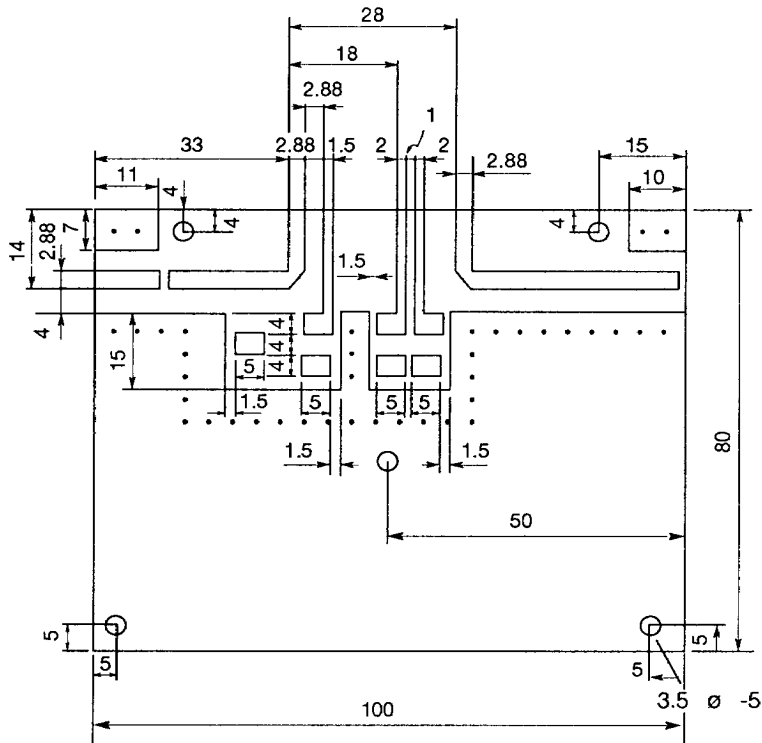


Output power  $P_{out}$  is defined at the root point of the module output pin  $P_{out}$ . The coefficient of output power loss in the PCB output line Z<sub>3</sub> is shown below.

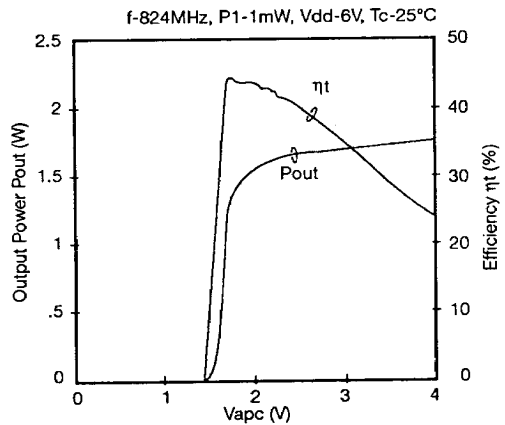
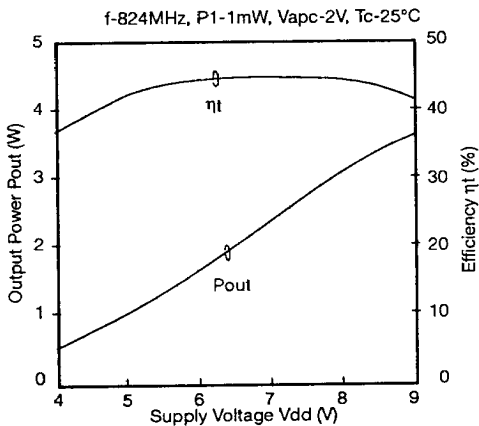
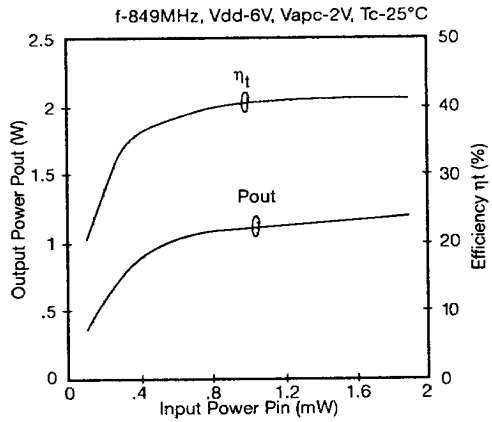
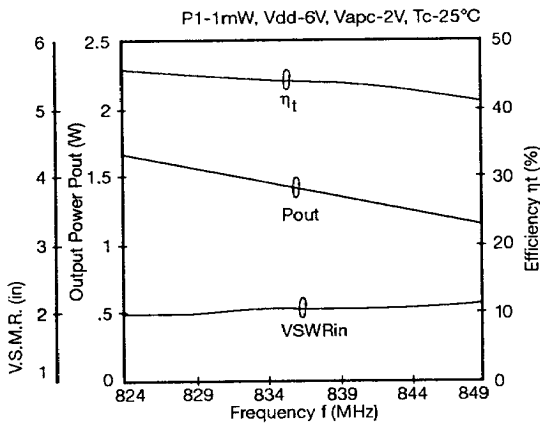
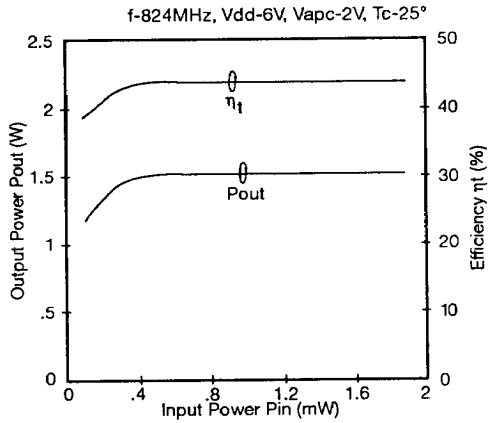
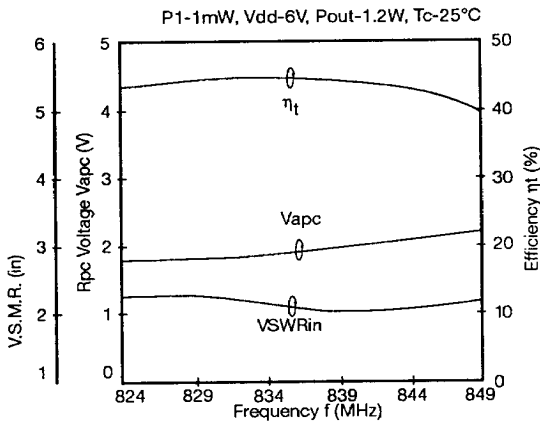
$$1/S_{21}^2 = 1/(0.9805)^2 = 1.04$$



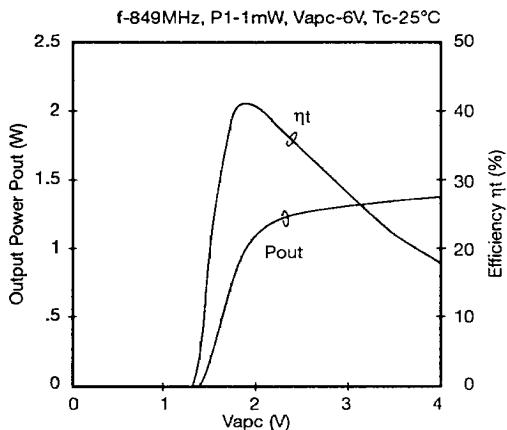
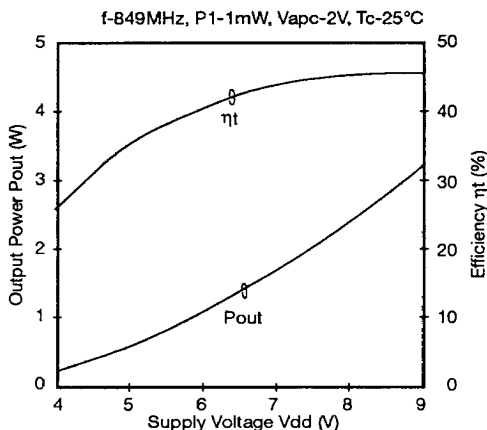
■ TEST FIXTURE PATTERN



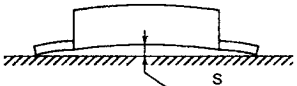
Grass Epoxy Double Sided PCB  
 (t = 1.6 mm,  $\epsilon_r = 4.8$ )



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• Mechanical Characteristics

Item	Conditions	Spec.
Torque for screw up the heatsink flange	M3 Screw-Bolts	4 ~ 6 kg/cm
Warp size of the heatsink flange: S		S = 0 +0.3/-0 mm

Note for Use

1. Unevenness and distortion at the surface of the heatsink attached PF0015 should be less than 0.05 mm.
2. It should not be existed any dust between PF0015 and heatsink.
3. PF0015 should be separated from PCB more than 1.5 mm.
4. Soldering temperature and soldering time should be less than 230°C, 10 sec. (Soldering position spaced from the root point of the lead frame: 2 mm).
5. Recommendation of thermal joint compounds is TYPE G746 (Manufacturer: Shin-Etu Chemical, Co., Ltd.) or equivalent.
6. To protect devices from electro-static damage, soldering iron, measuring-equipment and human body etc. should be grounded.