

| 4496205 0013588 094 ₩ HIT4 捷多邦, 专业PCB打样工/, 24小时加

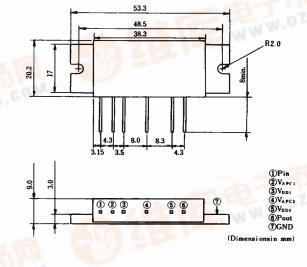
HITACHI/(OPTOELECTRON 武学 61E D

HIGH FREQUENCY POWER MOS FET MODULE

UHF Band 865-890 MHz

FEATURES

- Include Input and Output Matching Circuit
- Easy to Control Output Power
- Superior to Stability at Load Mismatching



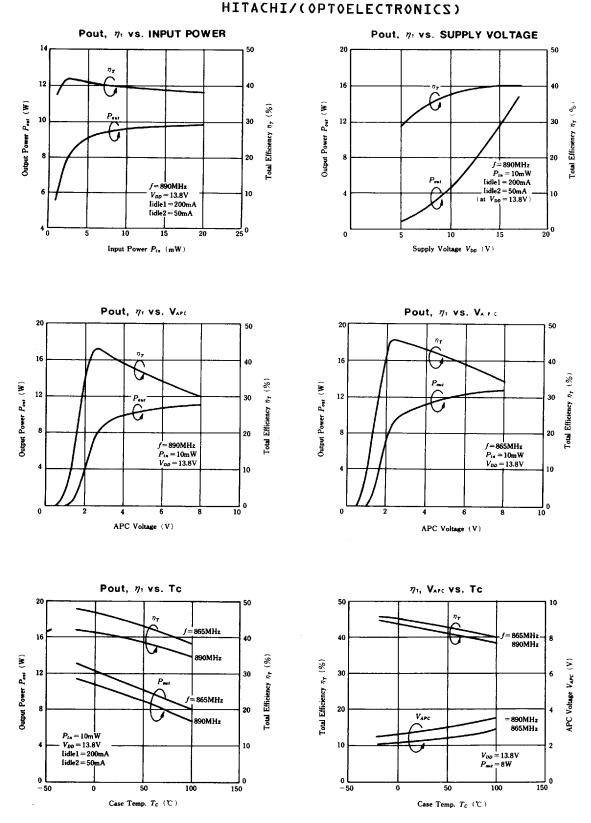
■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Item	Symbol	Rating	Unit V	
Supply Voltage	VDD	17		
Maximum Circuit Current	10	3.0	A	
APC Voltage	VAPC	±8	v	
Maximum Input Power	Pix	30	mW	
Operating Maximum Case Temperature	T. (0p)	0~+80	•c	
Storage Temperature	Tsta	-10~+100	°C	

■ ELECTRICAL CHARACTERISTICS (Ta-25°C)

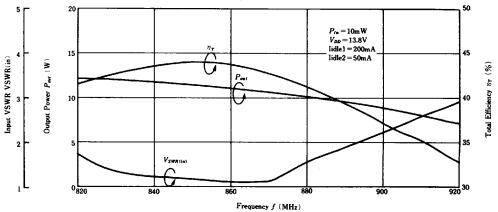
Item	Symbol	Test Condition	min.	typ.	max.	Unit
Drain Cutoff Current	Ips	$V_{DDI} = V_{DD2} = 17 \text{V}, V_{APCI} = V_{APC2} = 0$	-	-	500	μA
Output Power	Pout	f=865, 890MHz	8.0	9.0	-	W
Total Efficiency	η _τ (1)	Pin=10mW	35	40	-	%
2nd Harmonic Distortion	2nd H.D.	$V_{DDI} = V_{DD2} = 13.8 \text{V}$		-50	-30	dB
3rd Harmonic Distortion	3rd H.D.	$Iidle_1 = 200 \text{mA}$	-	-40	-30	dB
Input VSWR	VSWR(in)	lidle_=50mA	-	1.5	3.0	-
Output VSWR	VSWR(out)	$Zin = Zout = 50\Omega$		1.5		-
Total Efficiency	η _Γ (2)	f=865, 890 MHz, Pin=10mW, Pout=8W (at APC Control) $Zin=Zout=50 \Omega$	35	40		%



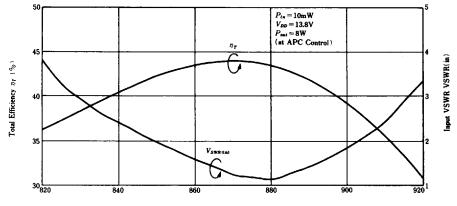


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Pout, nr, VSWR(in) vs. FREQUENCY



 η_{T} , VSWR(in) vs. FREQUENCY

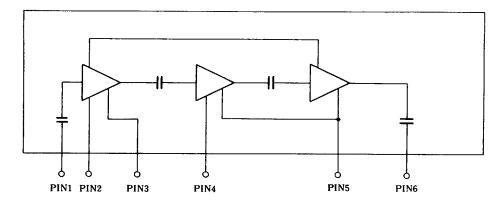


Frequency f (MHz)

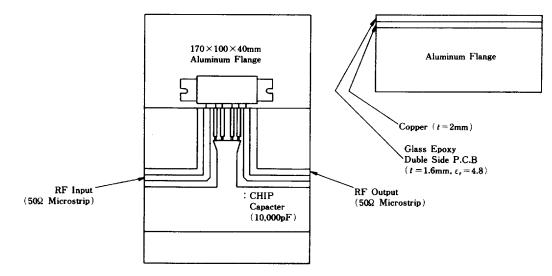
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INTERNAL DIAGRAM

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TEST FIXTURE



TEST SYSTEM DIAGRAM

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