Audio IC 室询BA3118供应商

General-purpose dual line amplifier

The BA3118L is a dual-channel line amplifier with a flat frequency characteristic. It has been designed for use in radio cassette players and car stereos.

The feedback resistors are all on the chip, which reduces the number of external components required and simplifies PCB design. The gain can be set to one of 10 gain settings from 6dB to 20dB (6dB, 8dB, 10dB, 10.9dB, 12.4dB, 13.4dB, 15.1dB, 16dB, 17.9dB and 20dB), and external fine trimming is possible.

Applications

Car stereos and radio cassette players

Features

- 1) 10 gain settings over the range 6dB to 20dB (max. step 2dB) without external resistors.
- 2) Low distortion.
- 3) Good crosstalk characteristics.
- 4) Low noise.
- 5) Flat frequency characteristic.

- Two channels on one chip allows compact set design.
- 7) Pin 9 is not connected which simplifies the PBC GND design.
- 8) Built-in feedback resistors reduces the number of external components required.
- 9) Low gain variance between the two channels.

Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits GG-	Unit
Supply voltage	Vcc	18	v
Power dissipation	Pd	400*	mW
Operating temperature	Topr	-25~85	ĉ
Storage temperature	Tstg	-55~125	ĉ

*1 Reduced by 4.0mW for each increase in Ta of 1°C over 25°C.

Recommended operating conditions (Ta = 25°C)

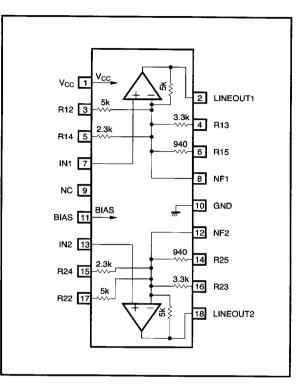
Parameter	Symbol	Min.	Тур.	Max.	Unit
Supply voltage range	Vcc	4	1	16	v



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Block diagram



•Electrical characteristics (unless otherwise specified $Ta = 25^{\circ}C$, $V_{cc} = 8.0V$, f = 1kHz and $G_{vc} = 20$ dB)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Quiescent current	la	—	3.8	5.6	mA	V _{IN} =0Vrms	
Open-circuit voltage gain	Gvc	19	20	21	dB	VIN=-20dBm、DIN AUDIO	
Maximum output voltage	Vом	1.1	1.9		Vrms	THD=1%、BPF400~30kHz	
Total harmonic distortion	THD	_	0.017	0.15	%	$V_0=0$ dBm, R _L =30k Ω	
Input conversion-noise voltage	V _{NIN}	_	1.1	2.1	μVrms	V _{IN} =0Vrms、Rg=0Ω、DIN AUDIO	
Interchannel crosstalk	СТ		92	-72	dB	$V_0=0$ dBm, $R_L=30$ k Ω	
Input resistance	Rin	30	45	60	kΩ	V _{IN} =0.1Vrms	
Ripple rejection	RR	35	44	_	dB	V _{BB} =-20dBm、f _{BB} =100Hz	

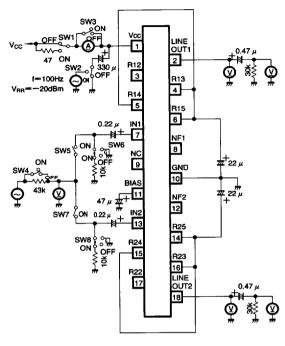
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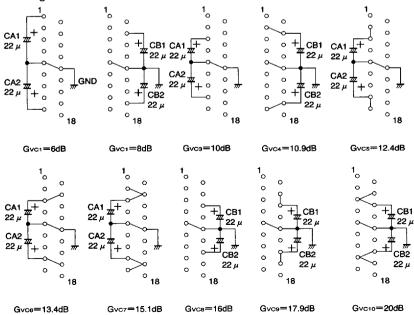


Measurement	circuit switch	setting table

SW No. Item	1	2	3	4	5	6	7	8
la	OFF	OFF	ON	OFF	OFF	ON	OFF	ON
Gvc	OFF	OFF	OFF	OFF	ON	OFF	ON	OFF
Vом	OFF	OFF	OFF	OFF	ON	OFF	ON	OFF
THD	OFF	OFF	OFF	OFF	ON	OFF	ON	OFF
VNIN	OFF	OFF	OFF	OFF	OFF	G	OFF	G
СТ	OFF	OFF	OFF	OFF	ON/OFF	OFF/ON	OFF/ON	ON/OFF
RIN	OFF	OFF	OFF	ON	ON/OFF	OFF	OFF/ON	OFF
RR	ON	ON	OFF	OFF	OFF	ON	OFF	ON



Gain setting pattern diagrams

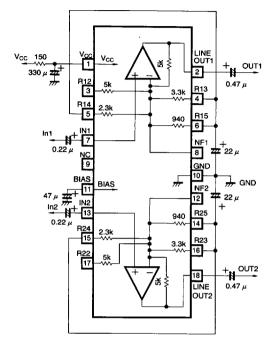


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Application example



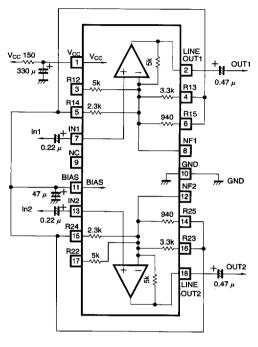


Fig. 2

Fig. 3

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