



## PRELIMINARY INFORMATION

Commercial  
IndustrialX9MME  
X9MMEIE<sup>2</sup>POT<sup>TM</sup> Digitally Controlled Potentiometer

## FEATURES

- Solid State Reliability
- Single Chip MOS Implementation
- Three Wire TTL Control
- Operates From Standard 5V Supply
- 99 Resistive Elements
  - Temperature Compensated
  - ± 20% End to End Resistance Range
- 100 Wiper Tap Points
- Wiper Position Digitally Controlled
- Wiper Position Stored in Nonvolatile Memory Then Automatically Recalled on Power-Up
- 100 Year Wiper Position Retention
- 8 Pin Mini-DIP Package
- 14 Pin SOIC Package

## DESCRIPTION

The Xicor X9MME is a solid state nonvolatile potentiometer and is ideal for digitally controlled resistance trimming.

The X9MME is a resistor array composed of 99 resistive elements. Between each element and at either end are tap points accessible to the wiper element. The position of the wiper element on the array is controlled by the CS, U/D, and INC inputs. The position of the wiper can be stored in nonvolatile memory and is recalled upon a subsequent power-up.

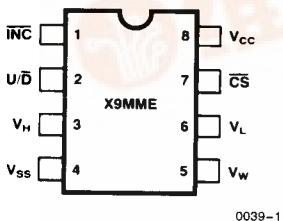
The resolution of the X9MME is equal to the maximum resistance value divided by 99. As an example; for the X9503 (50 K $\Omega$ ) each tap point represents 505 $\Omega$ .

Xicor E<sup>2</sup> products are designed and tested for applications requiring extended endurance. Refer to Xicor reliability reports for further endurance information.

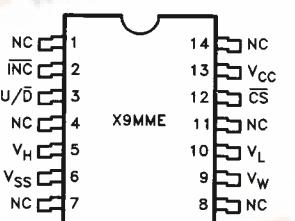
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## PIN CONFIGURATIONS

## PLASTIC



## SOIC



## PIN NAMES

V <sub>H</sub>	High Terminal of Pot
V <sub>W</sub>	Wiper Terminal of Pot
V <sub>L</sub>	Low Terminal of Pot
V <sub>SS</sub>	Ground
V <sub>CC</sub>	System Power
U/D	Up/Down Control
INC	Wiper Movement Control
CS	Chip Select for Wiper Movement/Storage
NC	No Connect

## X9MME, X9MMEI

### ANALOG CHARACTERISTICS

#### Electrical Characteristics

End to End Resistance Tolerance	.....	± 20%
Power Rating at 25°C	.....	
X9102	.....	16 mW
X9103, X9503 and X9104	.....	10 mW
Wiper Current	.....	± 1 mA Max.
Typical Wiper Resistance	.....	40Ω at 1 mA
Typical Noise	.....	
X9102	< - 120 dB/√Hz Ref: 1V	
X9103, X9503 and X9104	< - 95 dB/√Hz Ref: 1V	
<b>Resolution</b>		
Resistance	.....	1%
<b>Linearity</b>		
Absolute Linearity <sup>(1)</sup>	.....	± 1.0 MI <sup>(2)</sup>
Relative Linearity <sup>(3)</sup>	.....	± 0.2 MI <sup>(2)</sup>
<b>Temperature Coefficient</b>		
-40°C to +85°C	.....	
X9102	.....	± 600 ppm/°C Typical
X9103, X9503 and X9104	.....	± 300 ppm/°C Typical
Ratiometric Temperature Coefficient	.....	± 20 ppm
<b>Wiper Adjustability</b>		
Unlimited Wiper Adjustment (Volatile Mode While Chip is Selected)	.....	
Nonvolatile Storage of Wiper Position	.....	10,000 Cycles Typical

#### Environmental Characteristics

Temperature Range	.....	
Operating	X9MME	..... 0°C to + 70°C
X9MMEI	.....	- 40°C to + 85°C

Storage	.....	- 65°C to + 150°C
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#### Physical Characteristics

Marking Includes:	.....	
Manufacturer's Trademark	.....	
Resistance Value or Code	.....	
Date Code	.....	

### ABSOLUTE MAXIMUM RATINGS\*

Temperature Under Bias	.....	- 65°C to + 135°C
Storage Temperature	.....	- 65°C to + 150°C
Voltage on CS, INC, U/D and Vcc Referenced to Ground	.....	- 1.0V to + 7.0V
Voltage on VH and VL Referenced to Ground	.....	- 8.0V to + 8.0V
Lead Temperature (Soldering, 10 Seconds)	.....	+ 300°C
Wiper Current	.....	± 1 mA
ΔV =  VH - VL	.....	
X9102	.....	4V
X9103, X9503 and X9104	.....	10V

#### \*COMMENT

Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. This is a stress rating only and the functional operation of the device at these or any other conditions above those indicated in the operational sections of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

### D.C. OPERATING CHARACTERISTICS

X9MME TA = 0°C to + 70°C, VCC = + 5V ± 10%, unless otherwise specified.

X9MMEI TA = - 40°C to + 85°C, VCC = + 5V ± 10%, unless otherwise specified.

Symbol	Parameter	Limits			Units	Test Conditions
		Min.	Typ. <sup>(4)</sup>	Max.		
I <sub>CC</sub>	Supply Current		25	35	mA	
I <sub>LI</sub>	Input Leakage Current			± 10	μA	V <sub>IN</sub> = 0V to 5.5V, INC, U/D, CS
V <sub>IH</sub>	Input High Voltage	2.0		V <sub>CC</sub> + 1.0	V	
V <sub>IL</sub>	Input Low Voltage	- 1.0		0.8	V	
R <sub>W</sub>	Wiper Resistance		40	100	Ω	± 1 mA
V <sub>VH</sub> <sup>(5)</sup>	V <sub>H</sub> Voltage	- 5.0		+ 5.0	V	
V <sub>VL</sub> <sup>(5)</sup>	V <sub>L</sub> Voltage	- 5.0		+ 5.0	V	
C <sub>IN</sub> <sup>(6)</sup>	CS, INC, U/D, Input Capacitance			10	pF	

**Notes:** (1) Absolute Linearity is utilized to determine actual wiper voltage versus expected voltage as determined by wiper position when used as a potentiometer.

$$\text{Absolute Linearity} = (V_{W(n)}(\text{actual}) - V_{W(n)}(\text{expected})) = \pm 1 \text{ MI Max.}$$

$$(2) 1 \text{ MI} = R_{\text{TOT}}/99 \text{ or } \frac{V_H - V_L}{99} = \text{Minimum Increment.}$$

(3) Relative Linearity is utilized to determine the actual change in voltage between successive tap position when used as a potentiometer. It is a measure of the error in step size.

$$\text{Relative Linearity} = V_{W(n+1)} - [V_{W(n)} + \text{MI}] = \pm 0.2 \text{ MI Max.}$$

Typical values of Linearity are shown in Figures 3, 6, 9 and 12.

(4) Typical values are for TA = 25°C and nominal supply voltage.

$$(5) \Delta V \text{ for X9102} = |V_H - V_L| \leq 4V. \Delta V \text{ for X9103, X9503 and X9104} = |V_H - V_L| \leq 10V.$$

(6) This parameter is periodically sampled and not 100% tested.

# X9MME, X9MMEI

## A.C. CONDITIONS OF TEST

Input Pulse Levels	0V to 3.0V
Input Rise and Fall Times	10 ns
Input	1.5V

## MODE SELECTION

CS	INC	U/D	Mode
L	—	H	Wiper Up
L	—	L	Wiper Down
—	H	X	Store Wiper Position

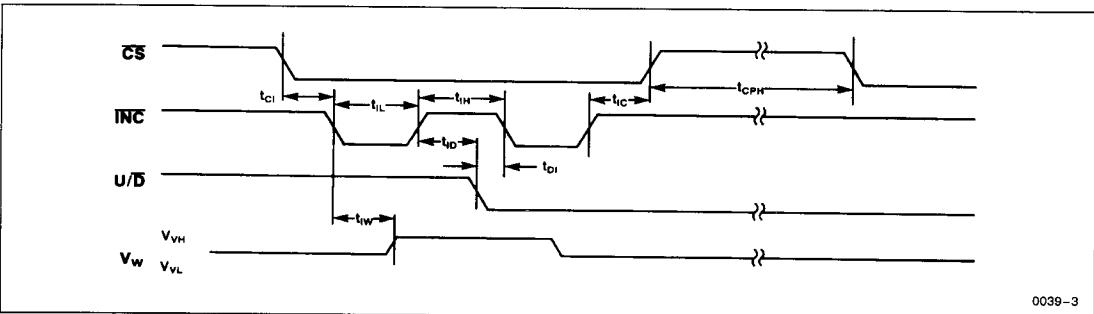
## A.C. CHARACTERISTICS

X9MME  $T_A = 0^\circ\text{C}$  to  $+70^\circ\text{C}$ ,  $V_{CC} = +5\text{V} \pm 10\%$ , unless otherwise specified.

X9MMEI  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ ,  $V_{CC} = +5\text{V} \pm 10\%$ , unless otherwise specified.

Symbol	Parameter	Limits			Units
		Min.	Typ.(7)	Max.	
$t_{CI}$	CS to INC Setup	100			ns
$t_{ID}$	INC High to U/D Change	100			ns
$t_{DI}$	U/D to INC Setup	2.9			$\mu\text{s}$
$t_{IL}$	INC Low Period	1			$\mu\text{s}$
$t_{IH}$	INC High Period	3			$\mu\text{s}$
$t_{IC}$	INC Inactive to CS Inactive	1			$\mu\text{s}$
$t_{CPH}$	CS Deselect Time	20			ms
$t_{IW}$	INC to $V_W$ Change		100	500	$\mu\text{s}$

## A.C. Timing



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Note: (7) Typical values are for  $T_A = 25^\circ\text{C}$  and nominal supply voltage.

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## X9MME, X9MMEI

### PIN DESCRIPTIONS

#### **V<sub>H</sub>**

The high terminal of the X9MME is capable of handling an input voltage from  $-5V$  to  $+5V$ .

#### **V<sub>L</sub>**

The low terminal input is limited from  $-5V$  to  $+5V$ .

#### **V<sub>W</sub>**

The wiper terminal series resistance is typically less than  $40\Omega$ . The value of the wiper is controlled by the use of U/D and INC.

#### **Up/Down (U/D)**

The U/D input controls the direction of the wiper movement and the value of the nonvolatile counter.

#### **Increment (INC)**

The INC input is negative-edge triggered. Toggling INC will move the wiper and either increment or decrement the counter in the direction indicated by the logic level on the U/D input.

#### **Chip Select (CS)**

The device is selected when the CS input is LOW. The current counter value is stored in nonvolatile memory when CS is returned HIGH with INC HIGH.

### DEVICE OPERATION

The INC, U/D and CS inputs control the movement of the wiper along the resistor array. HIGH to LOW transitions on INC, with CS LOW, increment (U/D = HIGH) or decrement (U/D = LOW) an internal counter. The output of the counter is decoded to position the wiper. When CS is brought HIGH the counter value is automatically stored in the nonvolatile memory. Upon power-up the nonvolatile memory contents are restored to the counter.

With the wiper at position 99, additional increments (U/D = HIGH) will not move the wiper. With the wiper at position 0, additional decrements (U/D = LOW) will not move the wiper.

The state of U/D may be changed while CS remains LOW, allowing a gross then fine adjustment during system calibration.

If V<sub>CC</sub> is removed while CS is LOW the contents of the nonvolatile memory may be lost.

The end to end resistance of the array will fluctuate once V<sub>CC</sub> is removed.

### APPLICATIONS

The combination of a digital interface and nonvolatile memory in a silicon based trimmer pot provides many application opportunities that could not be addressed by either mechanical potentiometers or digital to analog circuits. The X9MME addresses and solves many issues that are of concern to designers of a wide range of equipment.

Consider the possibilities:

Automated assembly line calibration versus mechanical tweaking of potentiometers.

Protection against drift due to vibration or contamination.

Eliminate precise alignment of PWB mounted potentiometers with case access holes.

Eliminate unsightly access holes on otherwise aesthetically pleasing enclosures.

Product enhancements such as keyboard adjustment of volume or brightness control.

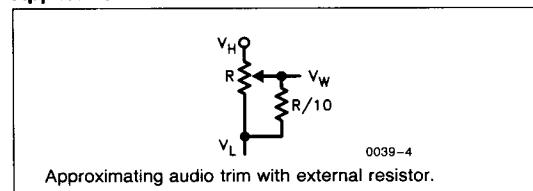
Front panel microprocessor controlled calibration of test instruments.

Remote location calibration via radio, modem or LAN link.

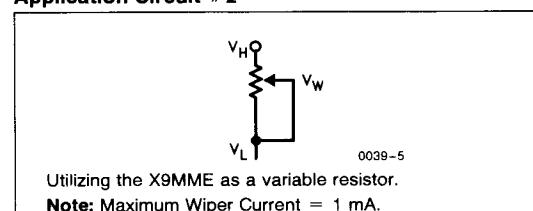
Calibration of hard to reach instruments in aircraft or other confined spaces.

### APPLICATION CIRCUITS

#### Application Circuit #1



#### Application Circuit #2



## X9MME, X9MMEI

Figure 1: Typical Frequency Response for X9102

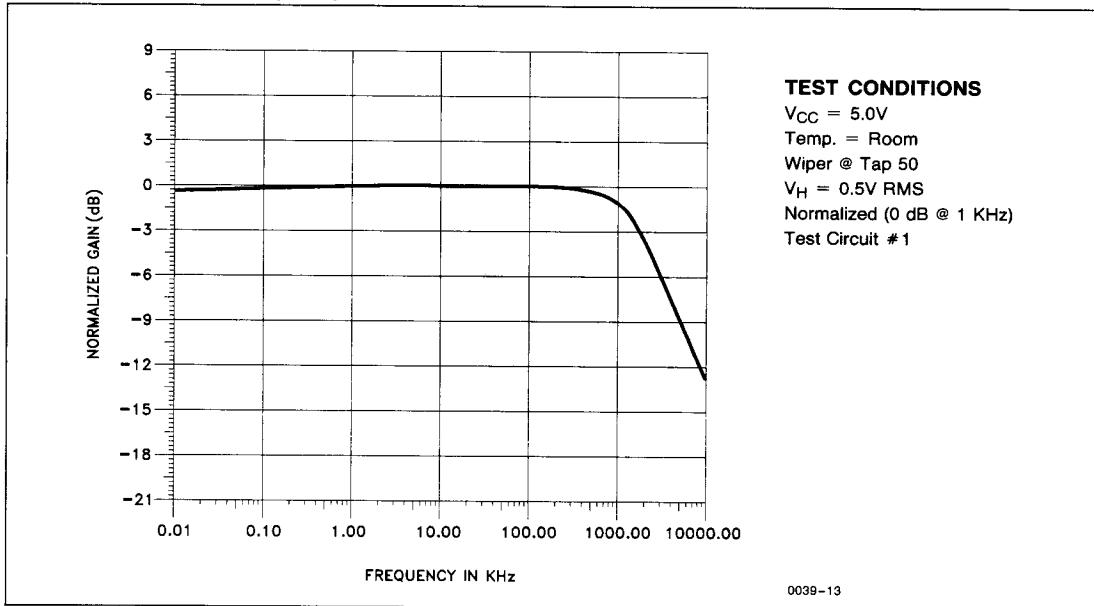
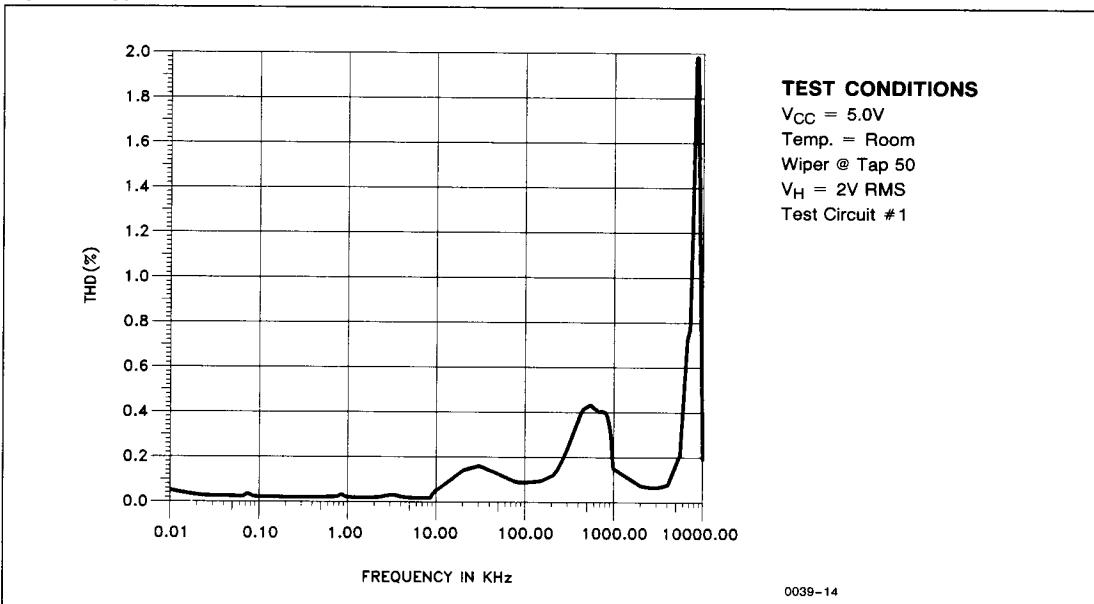


Figure 2: Typical Total Harmonic Distortion for X9102



## X9MME, X9MMEI

Figure 3: Typical Linearity for X9102

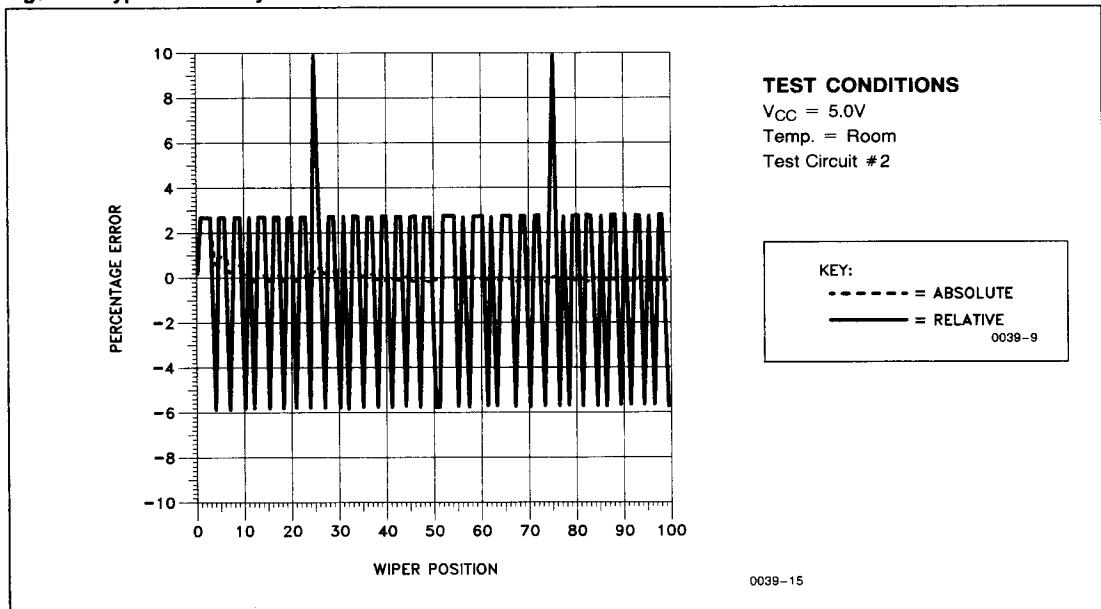
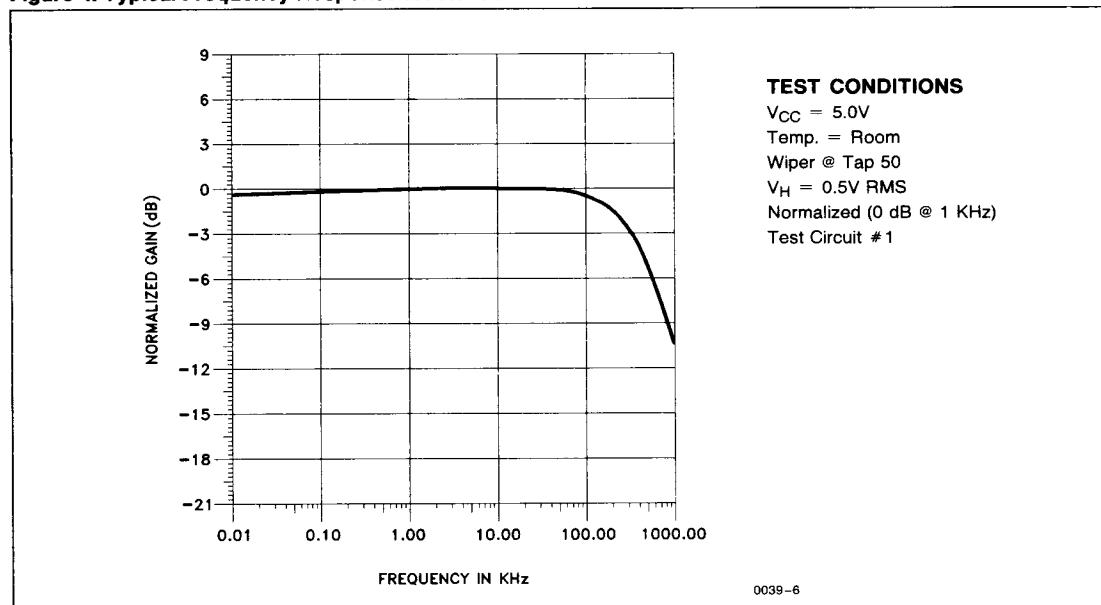
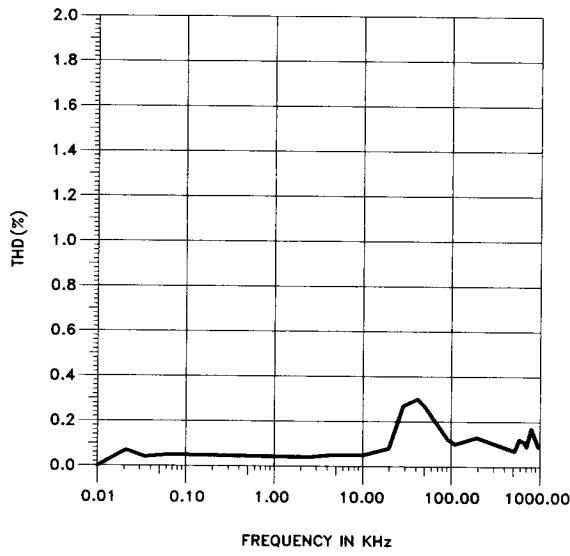


Figure 4: Typical Frequency Response for X9103



## X9MME, X9MMEI

Figure 5: Typical Total Harmonic Distortion for X9103



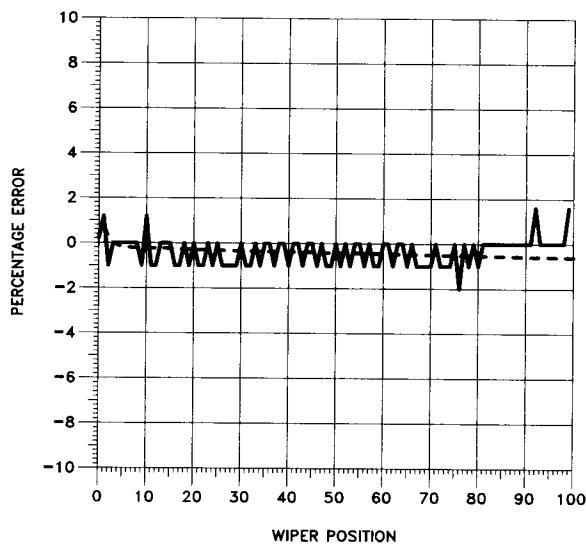
### TEST CONDITIONS

$V_{CC} = 5.0V$   
Temp. = Room  
Wiper @ Tap 50  
 $V_H = 2V$  RMS  
Test Circuit #1

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Figure 6: Typical Linearity for X9103



### TEST CONDITIONS

$V_{CC} = 5.0V$   
Temp. = Room  
Test Circuit #2

KEY:  
- - - - - = ABSOLUTE  
— = RELATIVE  
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## X9MME, X9MMEI

Figure 7: Typical Frequency Response for X9503

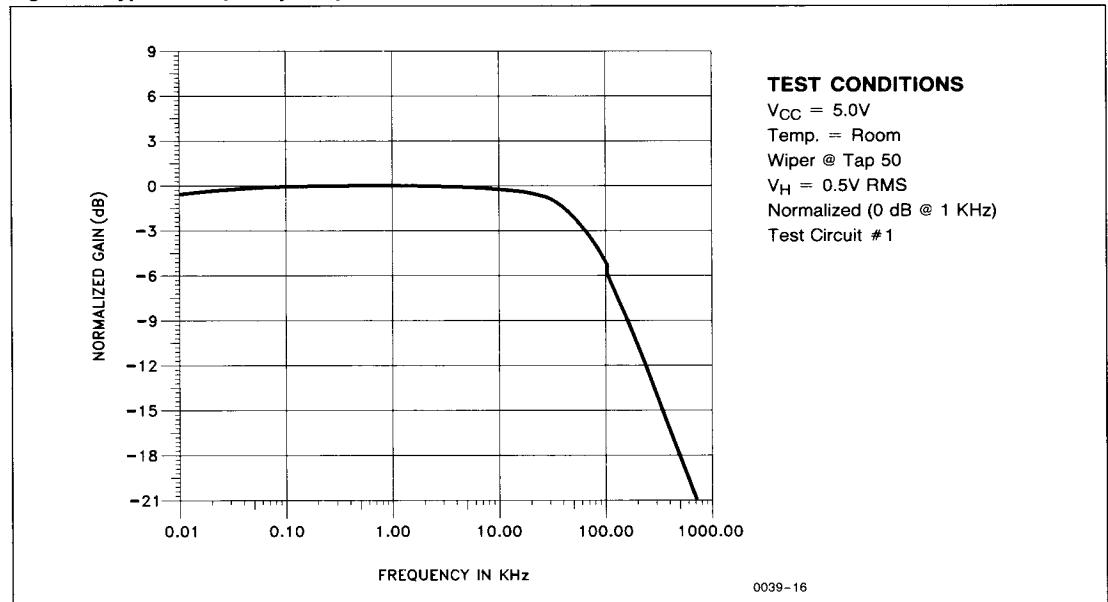
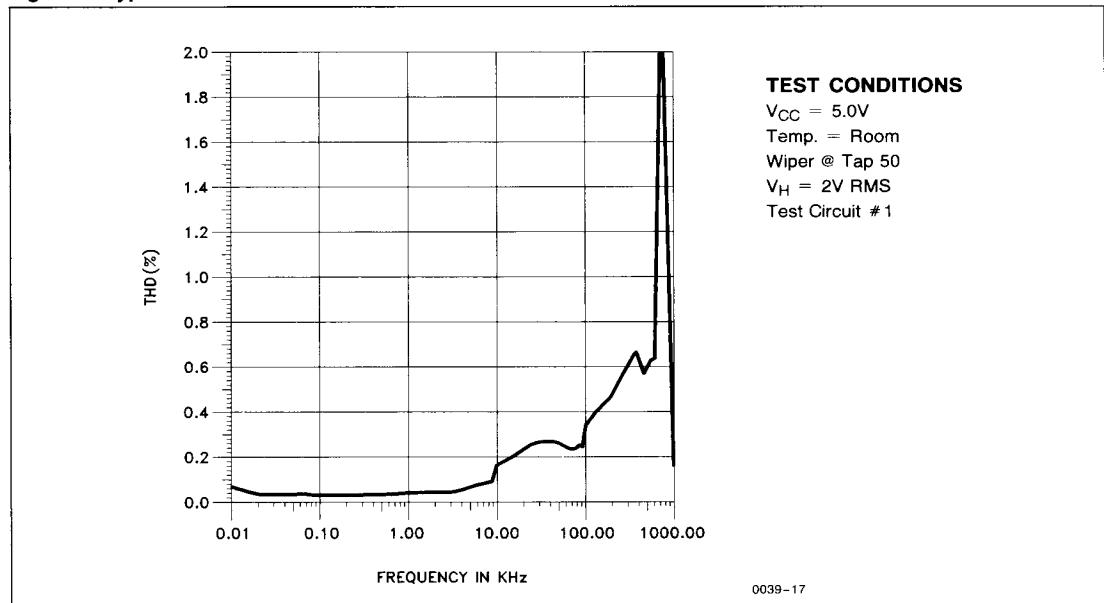
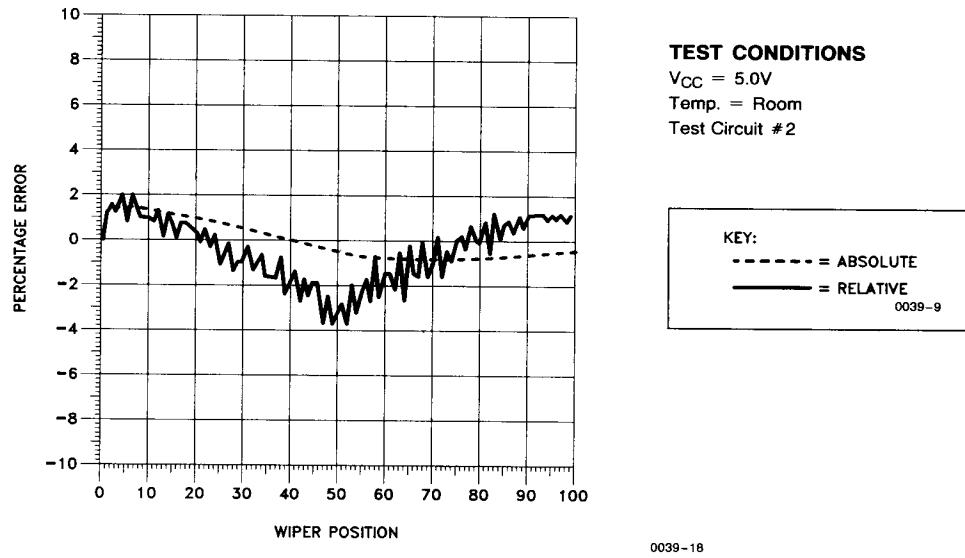


Figure 8: Typical Total Harmonic Distortion for X9503



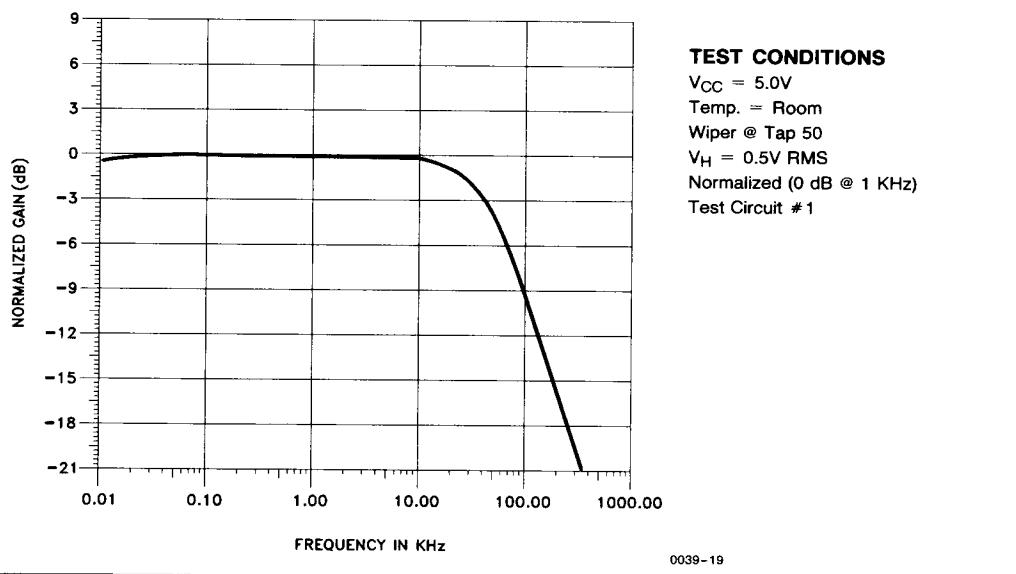
## X9MME, X9MMEI

Figure 9: Typical Linearity for X9503



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Figure 10: Typical Frequency Response for X9104



## X9MME, X9MMEI

Figure 11: Typical Total Harmonic Distortion for X9104

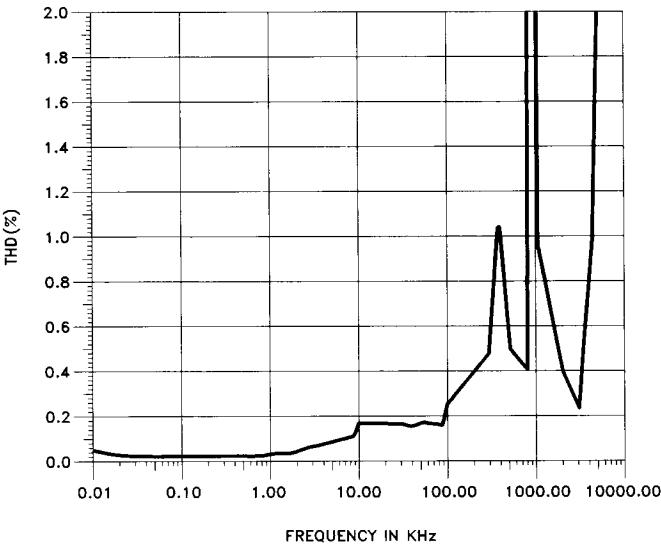
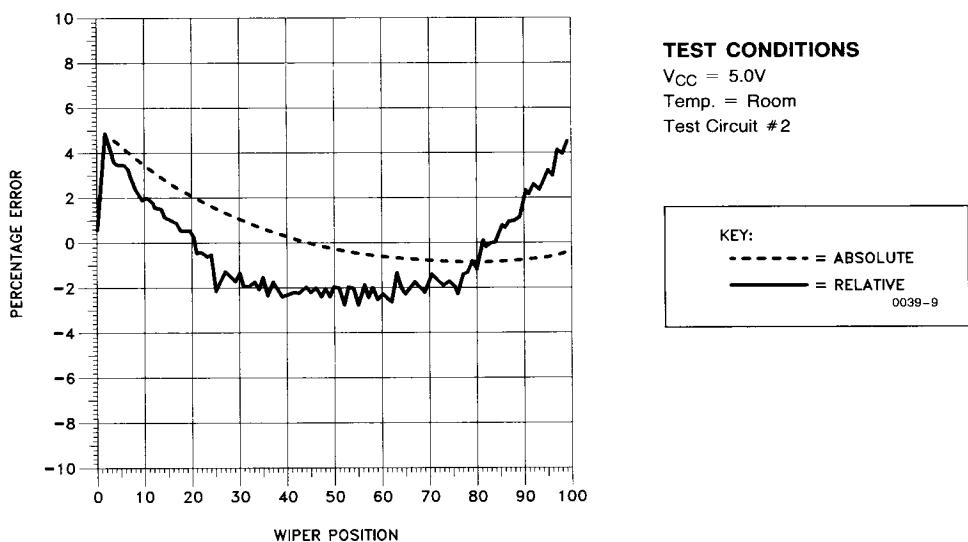
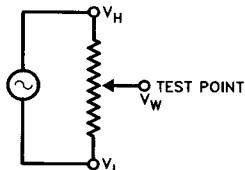


Figure 12: Typical Linearity for X9104



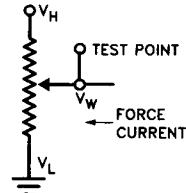
## X9MME, X9MMEI

Test Circuit #1



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Test Circuit #2



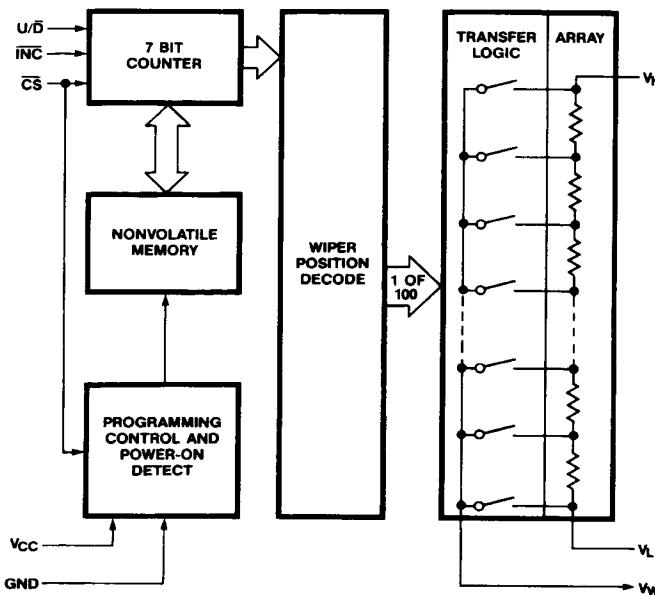
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### Standard Parts

Minimum Resistance	Wiper Increments	Maximum Resistance	Part Number
40Ω	10.1Ω	1 KΩ	X9102
40Ω	101Ω	10 KΩ	X9103
40Ω	505Ω	50 KΩ	X9503
40Ω	1010Ω	100 KΩ	X9104

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### FUNCTIONAL DIAGRAM



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