

Data Encryption Devices

T-52-33-17

WD2001/2002/20C03

Features

- ☐ Certified by National Bureau of Standards
- ☐ Encrypts/decrypts 64-bit data words using 56-bit key word
- ☐ Provides data transfer rate up to 1.8 Mbps with a 3 Mhz clock using the WD2001/2002 and up to 3.88 Mbps with a 6 Mhz clock using the WD20C03
- ☐ ECB (Electronic Code Book) mode for WD2001/2002 and ECB or CBC modes (Cipher Block Chaining) for the WD20C03
- ☐ Stored key on chip is not externally accessible
- ☐ Provides parity check on key word loading

- ❑ Offers command bit programming via the DAL bus or input pins
- ❑ The WD20C03 provides battery back-up mode for static key and data registers
- ❑ Standard eight-bit micro-processor and TTL (input and output) compatible

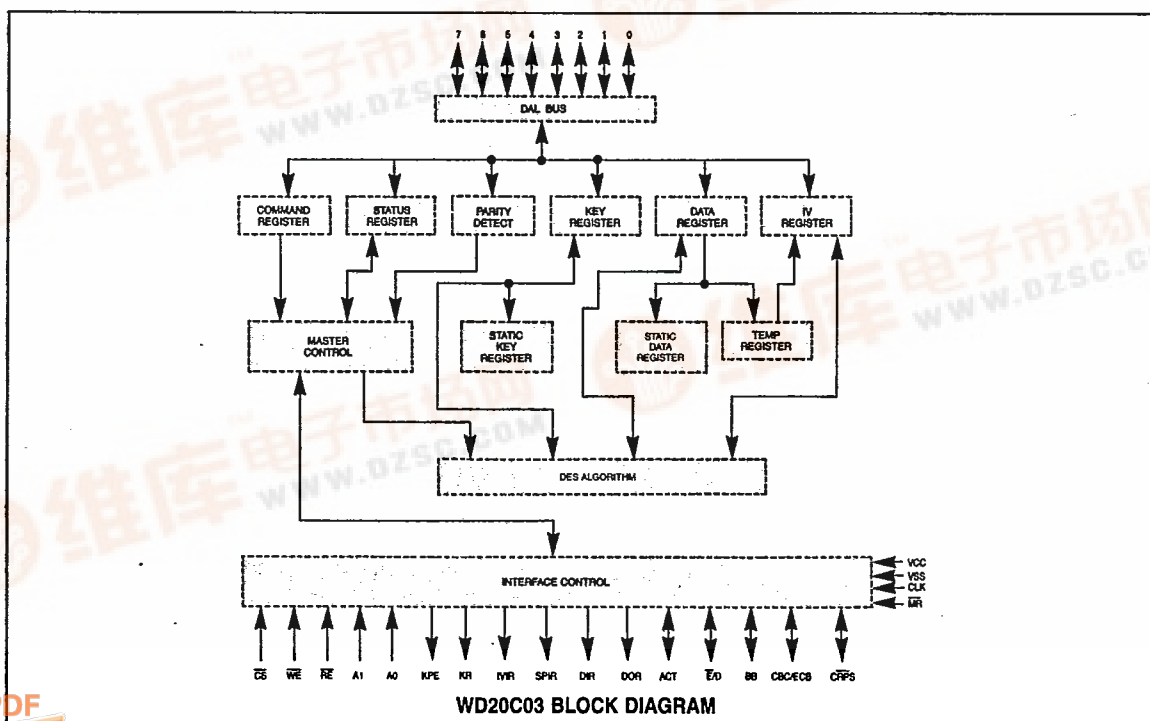
Description

The WD2001/2002/20C03 are programmable data encryption/decryption devices that facilitate the design of systems, converting clear text to cipher text and vice versa. They are fabricated in NMOS (WD2001 and WD2002) and CMOS (WD20C03) silicon gate

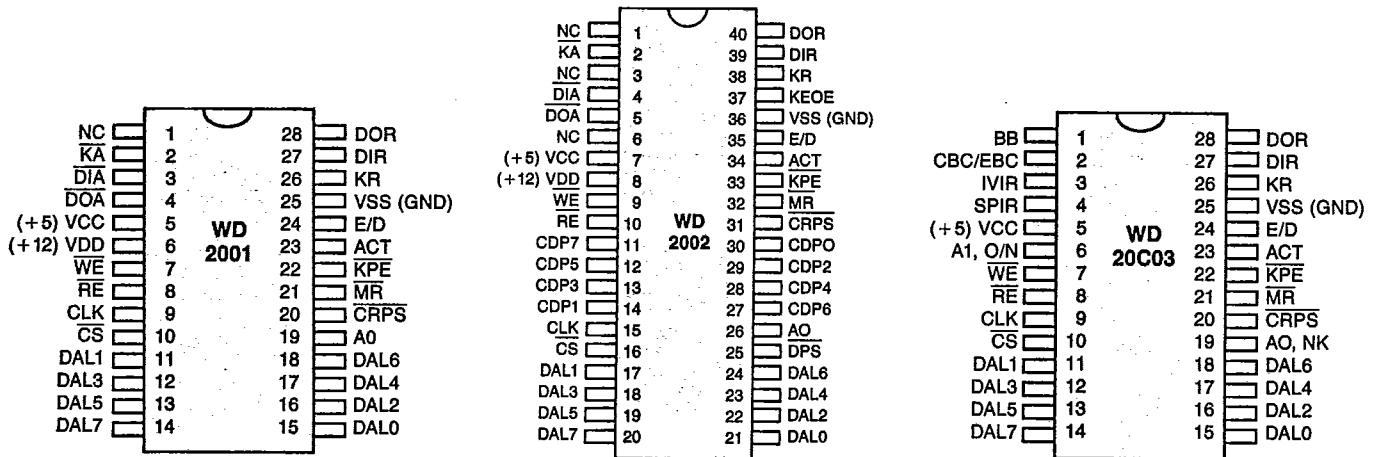
technology. The WD2001 and WD20C03 are pin to pin compatible.

These devices may be programmed for encryption or decryption using the algorithm specified in the Federal Information Processing Data Encryption Standard (FIPS #46). They are available for single port (WD2001/20C03) or dual port (WD2002) operation and interface easily to both microcomputers and hard-wired logic circuits.

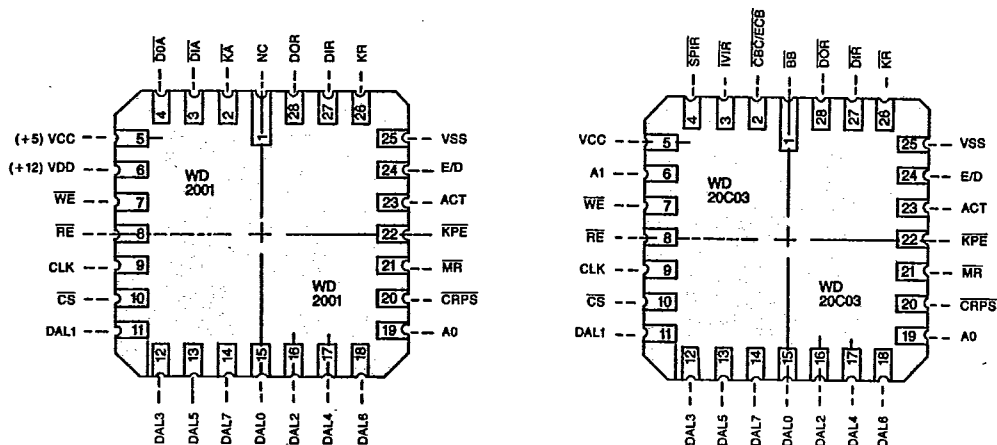
They may be used to secure brokerage transactions, electronic funds transfers, banking/business accounting and other communications applications.



Data Encryption Devices



PIN DESIGNATIONS (DIP)



PIN DESIGNATIONS (QUAD)

NOTE: These devices cannot be shipped outside of the United States of America without authorization from the State Department and Department of Defense.

Product specifications subject to change without notice.

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