# **SWITCHMODE™ Power Rectifier**

#### **Features and Benefits**

- Low Forward Voltage
- Low Power Loss/High Efficiency
- High Surge Capacity
- 175°C Operating Junction Temperature
- 16 A Total (8 A Per Diode Leg)
- Pb-Free Packages are Available\*

# **Applications**

- Power Supply Output Rectification
- Power Management
- Instrumentation

#### **Mechanical Characteristics**

- Case: Epoxy, Molded
- Epoxy Meets UL 94 V-0 @ 0.125 in
- Weight: 1.9 Grams (Approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- ESD Rating: Human Body Model 3B Machine Model C



# ON Semiconductor®

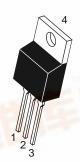
http://onsemi.com

# **ULTRAFAST RECTIFIER** 16 AMPERES, 200 VOLTS $t_{rr} = 35 \text{ ns}$





## **MARKING DIAGRAM**



TO-220AB CASE 221A **PLASTIC** 



= Assembly Location

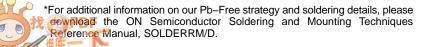
= Year WW = Work Week

BYV32-200 = Device Code = Pb-Free Package

**AKA** = Diode Polarity

# **ORDERING INFORMATION**

Device	Package	Shipping
BYV32-200	TO-220	50 Units / Rail
BYV32-200G	TO-220 (Pb-Free)	50 Units / Rail



# **MAXIMUM RATINGS**

Rating	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	200	V
Average Rectified Forward Current, T <sub>C</sub> = 156°C Per Leg Total Device	I <sub>F(AV)</sub>	8.0 16	А
Peak Rectified Forward Current (Square Wave, 20 kHz), T <sub>C</sub> = 154°C – Per Diode Leg	I <sub>FM</sub>	16	Α
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)	I <sub>FSM</sub>	100	Α
Operating Junction Temperature and Storage Temperature	T <sub>J</sub> , T <sub>stg</sub>	-65 to +175	°C

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

# THERMAL CHARACTERISTICS

Characteristic	Conditions	Symbol	Value	Unit
Maximum Thermal Resistance, Junction-to-Case	Min. Pad	$R_{ heta JC}$	3.0	°C/W
Maximum Thermal Resistance, Junction-to-Ambient	Min. Pad	$R_{\theta JA}$	60	

## **ELECTRICAL CHARACTERISTICS**

Characteristic	Symbol	Min	Typical	Max	Unit
Instantaneous Forward Voltage (Note 1) (i <sub>F</sub> = $5.0 \text{ A}$ , T <sub>j</sub> = $100^{\circ}\text{C}$ ) (i <sub>F</sub> = $20 \text{ A}$ , T <sub>j</sub> = $25^{\circ}\text{C}$ )	VF		0.74 1.01	0.85 1.15	V
Instantaneous Reverse Current (Note 1) (Rated dc Voltage, $T_j = 100^{\circ}\text{C}$ ) (Rated dc Voltage, $T_j = 25^{\circ}\text{C}$ )	İR	- -	21 3.5	600 50	μΑ
Maximum Reverse Recovery Time $ (I_F=1.0 \text{ A, di/dt}=50 \text{ A/}\mu\text{s})                                    $	t <sub>rr</sub>	- -	-	35 25	ns

<sup>1.</sup> Pulse Test: Pulse Width = 300 s, Duty Cycle ≤ 2.0%

http://onsomi.com

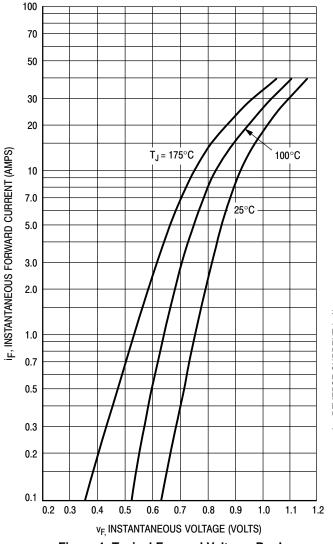


Figure 1. Typical Forward Voltage, Per Leg

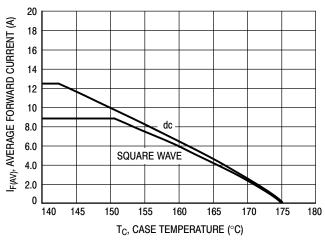


Figure 4. Current Derating, Case, Per Leg

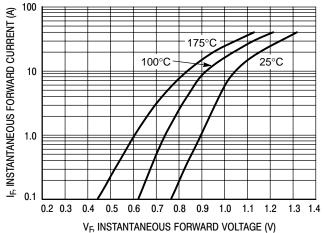


Figure 2. Maximum Forward Voltage

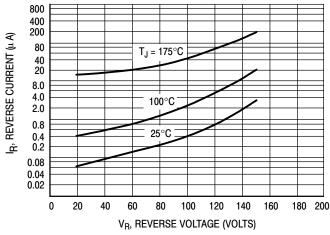


Figure 3. Typical Reverse Current, Per Leg\*

\* The curves shown are typical for the highest voltage device in the voltage grouping. Typical reverse current for lower voltage selections can be estimated from these same curves if  $V_R$  is sufficiently below rated  $V_R$ .

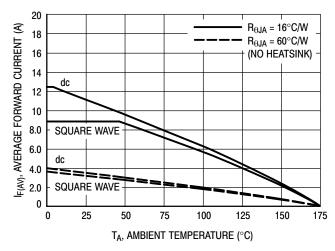


Figure 5. Current Derating, Ambient, Per Leg

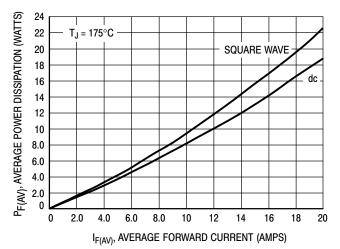


Figure 6. Power Dissipation, Per Leg

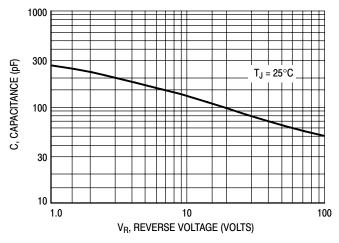
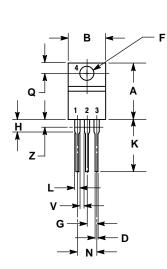


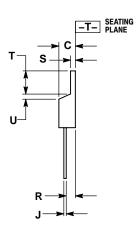
Figure 7. Typical Capacitance, Per Leg

#### PACKAGE DIMENSIONS

## **TO-220 THREE-LEAD** TO-220AB

CASE 221A-09 **ISSUE AD** 





- 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- CONTROLLING DIMENSION: INCH.
  DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIMETERS	
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
C	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.018	0.025	0.46	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

#### STYLE 6:

PIN 1. ANODE

- CATHODE 2.
- ANODE CATHODE

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