



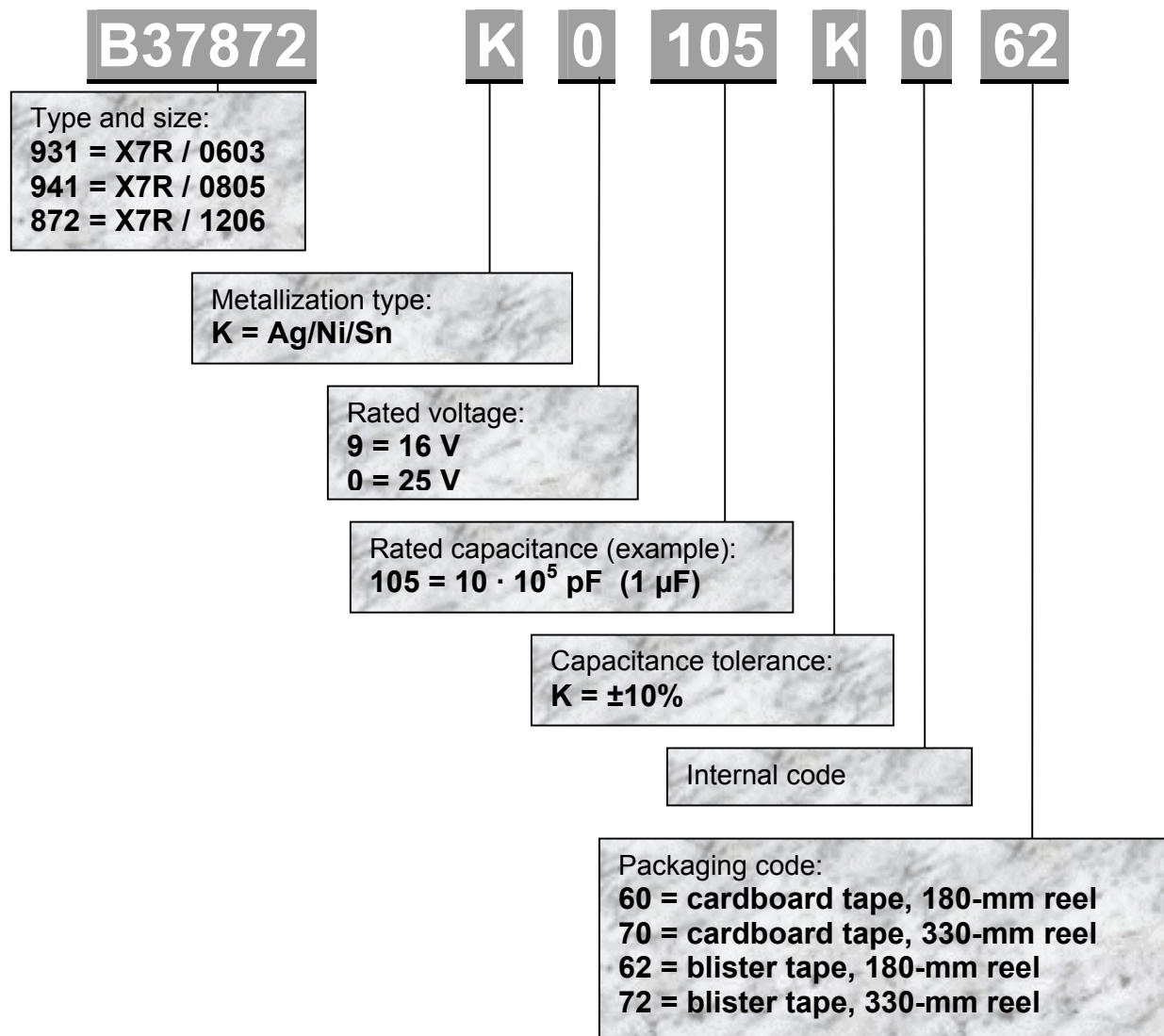
Multilayer ceramic capacitor

HighCV, X7R, 16 V and 25 V

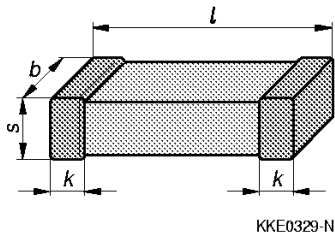
Series/Type: Chip
Ordering code: B37***K

Date: 15.07.2005
Version: 1

Ordering code



Dimensional drawing



Size [inch / mm]	l [mm]	b [mm]	s [mm]	k [mm]
0603 / 1608	1.6 ±0.15	0.80 ±0.1	0.8 ±0,1	0.10 – 0.4
0805 / 2012	2.0 ±0.20	1.25 ±0.15	1.35 max.	0.13 – 0.75
1206 / 3216	3.2 ±0.20	1.60 ±0.15	1.2 ±0.1	0.25 – 0.75

see also "Ordering codes and chip thickness", dimensions in accordance to CECC 32101-801

Electrical data

Temperature characteristic:	X7R
Climatic category (IEC 60068-1):	55/125/56
Standard:	EIA
Dielectric:	Class 2
Rated voltage:	16 V / 25 V
Capacitance ¹⁾ test conditions	
Test frequency:	(1.0 ±0.2) kHz
Test voltage:	(1.0 ±0.2) V _{RMS}
Max. relative capacitance change:	±15%
Dissipation factor tanδ (limit value):	< 25 · 10 ⁻³
Time constant τ at +25 °C:	> 500 s
Operating temperature range:	-55 °C ... +125 °C
Capacitance values:	220 nF ... 2.2 μF

¹⁾ Subject to aging, please see "General Technical Information" at www.epcos.com/ceramic_capacitors or the data book "Multilayer Ceramic Capacitors".



Multilayer ceramic capacitor

B37***K

HighCV, X7R, 16 V and 25 V

Chip

Ordering codes and chip thickness

Case size	C _R	Ordering code	Thickness	Packing quantity	
[inch]	[μF]		[mm]	Ø 180-mm reel [pcs]	Ø 330-mm reel [pcs]
16 V types					
0603	0.220	B37931K9224K060*	0.8 ±0.1	4000	16000
0805	0.220	B37941K9224K060*	0.8 ±0.1	4000	16000
	0.330	B37941K9334K060*	0.8 ±0.1	4000	16000
	0.470	B37941K9474K060*	0.8 ±0.1	4000	16000
	1.0	B37941K9105K062**	1.25 ±0.1	3000	12000
1206	1.0	B37872K9105K062**	1.2 ±0.1	3000	12000
	2.2	B37872K9225K062**	1.2 ±0.1	3000	12000

Size	C _R	Ordering code	Thickness	Packing quantity	
[inch]	[μF]		[mm]	Ø 180-mm reel [pcs]	Ø 330-mm reel [pcs]
25 V types					
0603	0.220	B37931K0224K060*	0.8 ±0.1	4000	16000
0805	0.220	B37941K0224K060*	0.8 ±0.1	4000	16000
	0.330	B37941K0334K060*	0.8 ±0.1	4000	16000
	0.470	B37941K0474K060*	0.8 ±0.1	4000	16000
	1.0	B37941K0105K062**	1.25 ±0.1	3000	12000
1206	1.0	B37872K0105K062**	1.2 ±0.1	3000	12000
	2.2	B37872K0225K062**	1.2 ±0.1	3000	12000

* Ordering Code Example

Standard tolerance: ± 10%

Standard packaging: Cardboard tape, 180-mm reel

** Ordering Code Example

Standard tolerance: ± 10%

Standard packaging: Blister tape, 180-mm reel



Multilayer ceramic capacitor

B37*K**

HighCV, X7R, 16 V and 25 V

Chip

Further information

Please see General Technical Information at www.epcos.com/ceramic_capacitors or the data book "Multilayer Ceramic Capacitors" for further information on:

- Soldering directions
- Taping and packing
- Surface mounting instructions
- Effects of mechanical stress



Important notes

The following applies to all products named in this publication:

1. Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule, EPCOS is either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether an EPCOS product with the properties described in the product specification is suitable for use in a particular customer application.
2. We also point out that **in individual cases, a malfunction of passive electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of a passive electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of a passive electronic component.
3. **The warnings, cautions and product-specific notes must be observed.**
4. In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as "hazardous")**. Useful information on this will be found in our Material Data Sheets on the Internet (www.epcos.com/material). Should you have any more detailed questions, please contact our sales offices.
5. We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.

We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available.
6. Unless otherwise agreed in individual contracts, **all orders are subject to the current version of the "General Terms of Delivery for Products and Services in the Electrical Industry" published by the German Electrical and Electronics Industry Association (ZVEI)**.
7. The trade names EPCOS, CeraDiode, CSSP, PhaseCap, PhaseMod, SIFI, SIKOREL, SilverCap, SIMID, SIOV, SIP5D, SIP5K, TOPcap, UltraCap, WindCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at www.epcos.com/trademarks.