### 查询CL6K4-G供应商 iupertex inc.

## Linear, Fixed, 100mA **Constant Current LED Driver**

#### **Features**

- 100mA ±5% constant current drive
- Built-in reverse polarity protection
- Dimmable via PWM supply
- Overtemperature protection
- Tab ground allows direct heatsinking to chassis
- 90V max rating for transient immunity

#### **Applications**

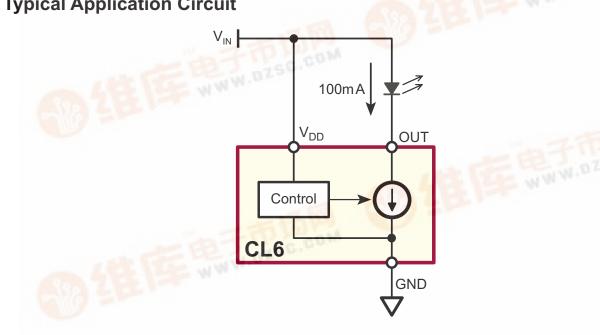
- Flashlights
- Specialty lighting
- Low voltage signage
- Low voltage lighting
- This device is not rated for automotive applications

## **General Description**

The CL6 is a fixed, linear current regulator designed for driving high brightness LEDs at 100mA from nominal 12V, 24V, or 48V supplies. With a maximum rating of 90V, it is able to withstand transients without the need for additional transient protection circuitry.

The CL6 is offered in both D-PAK and TO-220 packages. The tab on the TO-220 is ground, allowing heatsinking directly to a chassis without the need for electrically insulating spacers.

Overtemperature protection shuts off the LED current when the die temperature rises above 135°C (nominal). Full LED current resumes when the die temperature falls below 105°C (nominal).





#### **Typical Application Circuit**

#### **Ordering Information**

Device	Package	Options
Device	3-Lead TO-220	TO-252 (D-PAK)
CL6	CL6N5-G	CL6K4-G

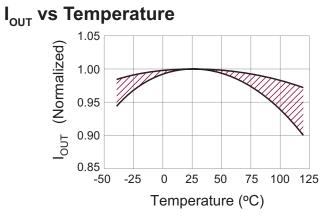
-G indicates package is RoHS compliant ('Green')

#### **Absolute Maximum Ratings**

Supply voltage, V <sub>DD</sub>	-25V to +100V
Output voltage, V <sub>OUT</sub>	-25V to +100V
Operating junction temperature	-40°C1
Storage temperature	-65°C to +150°C

Absolute Maximum Ratings are those values beyond which damage to the device may occur. Functional operation under these conditions is not implied. Continuous operation of the device at the absolute rating level may affect device reliability. All voltages are referenced to device ground.

1. Maximum junction temperature internally limited.

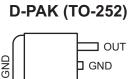


#### **Thermal Characteristics**

Guaranteed by design - not production tested

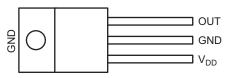
# n Inin

#### **Pin Configurations**





**TO-220** 



#### **Pin Designation**

Pin	Name	Description
V <sub>DD</sub>	V <sub>DD</sub>	Supply voltage for the CL6
OUT	Output	Connect the LED between this pin and the supply voltage
GND	Ground	Circuit common

Symbol	Parameter		Min	Тур	Max	Units	Conditions							
ρ	Thermal resistance,	TO-220	-	2.5	-	°C/W								
$ heta_{jc}$	junction to case	D-PAK	-	1.3	-	-0/00								
Ο	Thermal resistance,	TO-220	-	62	-	°C/W								
$ heta_{_{ja}}$	junction to ambient	D-PAK	-	40	-	-0/00	Soldered to 2cm <sup>2</sup> exposed copper area							
T	Overtemperature limit	120	135	150	°C									
T <sub>HYS</sub>	Overtemperature hystere	-	30	-	°C									

# Recommended Operating Conditions (all voltages with respect to GND pin)

Symbol	Parameter		Min	Тур	Max	Units	Conditions
V <sub>DD</sub>	Supply voltage	Normal Extended	6.5 6.5	-	28 90	V	
V <sub>OUT</sub>	Voltage at OUT pin <sup>1</sup>	Normal Extended	4.0 4.0	-	28 90	V	
T,	Junction temperature	2	-40	-	119	°C	

1. Continuous operation at high  $V_{our}$  voltages may result in activation of overtemperature protection. Use appropriate heatsinking. 2. Maximum junction temperature internally limited.

#### **Electrical Characteristics**

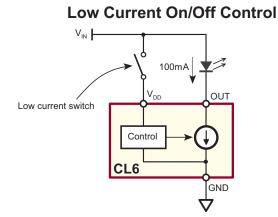
(Over normal recommended operating conditions unless otherwise specified. All voltages with respect to GND pin. Production tested @ 25°C.)

Symbol	Parameter	Min	Тур	Max	Units	Conditions
I <sub>DD</sub>	Current into $V_{_{DD}}$ pin	3.0	5.0	10	mA	
I <sub>OUT</sub>	Current into OUT pin <sup>1</sup>	95 90 50	100 100 -	105 110 120	mA	Normal conditions, 25°C Normal conditions, full temp <sup>2</sup> Extended conditions
I <sub>OUT(OFF)</sub>	Current into OUT pin with $V_{DD}$ pin open	-	-	10	μA	V <sub>DD</sub> = open
V <sub>OFF</sub>	Voltage at V <sub>DD</sub> to shut off LED current	-	-	1.0	V	Ι <sub>ουτ</sub> < 10μΑ
t <sub>on</sub>	V <sub>DD</sub> applied on delay	-	-	100	μs	
t <sub>off</sub>	V <sub>DD</sub> removed off delay	-	-	100	μs	

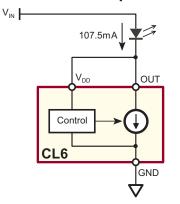
1. Thermal considerations may limit current to lower values. Use appropriate heat sinking.

2. Guaranteed by design – not production tested.

#### **Application Circuits**

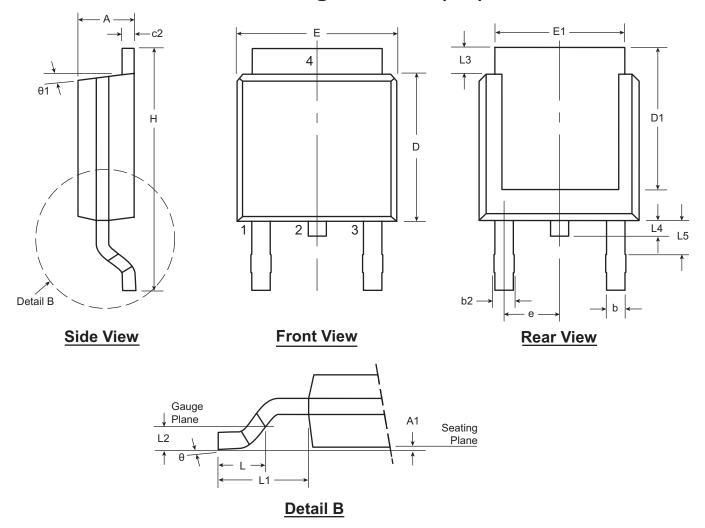


#### **2-Terminal Operation**



Minimum  $V_{\rm IN}$  is increased by LED drop. ILED is increased by IDD

## 3-Lead TO-252 D-PAK Package Outline (K4)



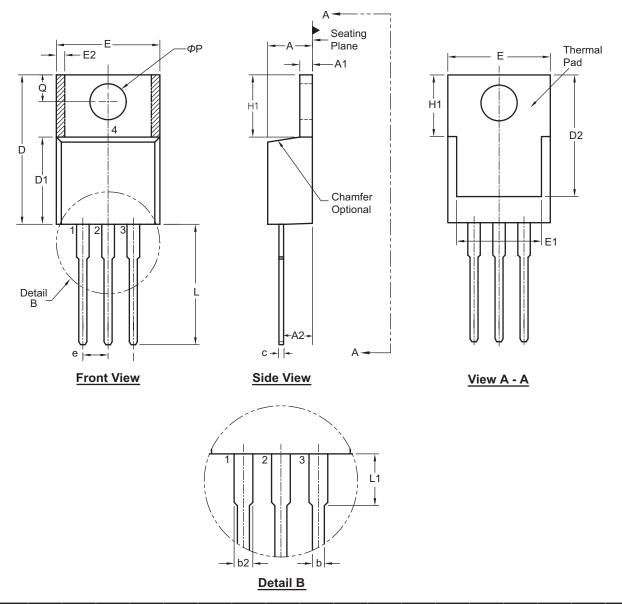
#### Notes:

1. 4 terminal locations are shown, only 3 are functional. Lead number 2 was removed.

Symb	ol	A	A1	b	b2	c2	D	D1	E	E1	е	Н	L	L1	L2	L3	L4	L5	θ	θ1
	MIN	.086	-	.025	.030	.018	.235	.205	.250	.170		.370	.055			.035	-	.045	0°	0°
Dimension (inches)	NOM	-	-	-	-	-	.240	-	-	-	.090 BSC	-	.060	.108 REF	.020 BSC	-	-	-	-	-
	MAX	.094	.005	.035	.045	.035	.245	-	.265	-		.410	.070		500	.050	.040	.060	10 <sup>0</sup>	15 <sup>0</sup>

JEDEC Registration TO-252, Variation AA, Issue E, June 2004. **Drawings not to scale.** 

## 3-Lead TO-220 (Power Package) Package Outline (N5)



Symbo	I	Α	A1	A2	b	b2	с	D	D1	D2	E	E1	E2	е	H1	L	L1	Q	ΦΡ
	MIN	.140	.020	.080	.015	.045	.014	.560	.330	.480	.380	.270	-		.230	.500	-	.100	.139
Dimension (inches)	NOM	-	-	-	.027	.057	-	-	-	-	-	-	-	.100 BSC	-	-	-	-	-
	MAX	.190	.055	.115	.040	.070	.024	.650	.355	.507	.420	.350	.030		.270	.580	.250	.135	.161

JEDEC Registration TO-220, Variation AB, Issue K, April 2002. Drawings not to scale.

(The package drawing(s) in this data sheet may not reflect the most current specifications. For the latest package outline information go to <u>http://www.supertex.com/packaging.html</u>.)

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**Supertex inc.** 1235 Bordeaux Drive, Sunnyvale, CA 94089 TEL: (408) 222-8888 / FAX: (408) 222-4895

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