

## Continental Device India Limited

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company

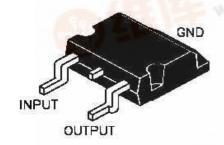




## **3-TERMINAL POSITIVE VOLTAGE REGULATOR**

CL7805D2T

D<sup>2</sup>PAK (TO-263) Plastic Package



The Voltages Available allow these Regulators to be used in Logic Systems, Instrumentation, Hi-Fi Audio Circuits and other Solid State Electronic Equipment

### **ABSOLUTE MAXIMUM RATINGS**

| DESCRIPTION   | SYMBOL           | VALUE        | UNIT       |
|---|------------------|--------------|------------|
| Input Voltage   | V <sub>IN</sub>  | 35           | V          |
| Continuous Total Dissipation at T <sub>a</sub> =25°C        | $P_{D}$          | 2.0          | W          |
| free air Temperature  |                  | W W 4-2.0    |            |
| Continuous Total Dissipation at T <sub>c</sub> =25°C        | $P_{D}$          | 15           | W          |
| case Temperature  | Fibri            | 15           | VV         |
| Operating free-air, case, or Virtual Junction               | T <sub>OPR</sub> | 0 to 150     | °C         |
| Temperature Range   | O. K             |              |            |
| Storage Temperature Range                                   | $T_{stg}$        | - 65 to +150 | ٥C         |
| Lead Temperature 1.6mm (1/16 inch) from Case for 10 seconds | T <sub>L</sub>   | 260          | <b>°</b> C |

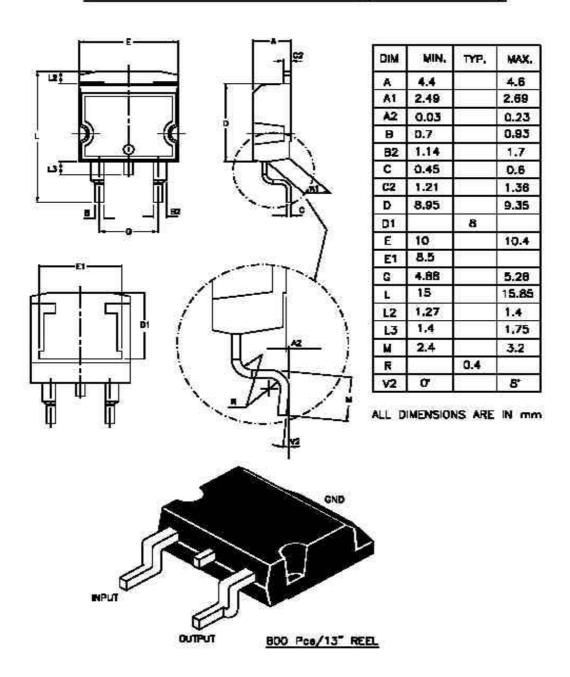
# ELECTRICAL CHARACTERISTICS (T<sub>i</sub>=25°C unless specified otherwise)

## V<sub>I</sub>=10V, I<sub>O</sub>=500mA

| DECORIDED N                  | 0)/140.01               | OVERDOL TECT CONDITION AND TVD MAY UNIT   |                           |      |       |      |       |
|------------------------------|-------------------------|---|---------------------------|------|-------|------|-------|
| DESCRIPTION                  | SYMBOL                  | TEST CONDITION  |                           | MIN  | TYP   | MAX  | UNIT  |
| Output Voltage               | Vo                      | thin am   | T <sub>j</sub> =25°C      | 4.80 |       | 5.20 | V     |
|                              | WWW.D                   | $I_O=5\text{mA} \sim 1\text{A}$<br>$V_I=7\text{V} \sim 20\text{V}, P \leq 15\text{W}$ | T <sub>j</sub> =0 ~ 125°C | 4.75 |       | 5.25 | V     |
| Line Regulation              | R <sub>EGV</sub>        | V <sub>I</sub> =7.0 ~ 25V   | T <sub>j</sub> =25°C      |      |       | 100  | mV    |
|                              |                         | V <sub>I</sub> =8.0 ~ 12V   |                           |      |       | 50   | mV    |
| Ripple Rejection             | R <sub>R</sub>          | $V_1$ =8.0 ~ 18V, f=120Hz   | $T_j=0 \sim 125^{\circ}C$ | 62   | 354   | to?  | dB    |
| Load Regulation              | R <sub>EGL</sub>        | I <sub>O</sub> =5mA ~ 1.5A  | T <sub>j</sub> =25°C      | da-  | 7 11  | 100  | mV    |
|                              |                         | I <sub>O</sub> =250mA ~ 750mA   | Let life                  | WW   | M.or. | 50   | mV    |
| Output Resistance            | R <sub>O</sub>          | f=1KHz  | T <sub>j</sub> =0 ~ 125°C |      | 0.017 |      | Ω     |
| Output Voltage Drift         | $\Delta V_{O}/\Delta T$ | I <sub>O</sub> =5mA   | T <sub>j</sub> =0 ~ 125°C |      | - 1.1 |      | mV/ºC |
| Output Noise Voltage         | V <sub>NO</sub>         | f=10Hz ~ 100KHz   | T <sub>j</sub> =25°C      |      | 40    |      | μV    |
| Dropout Voltage              | $V_d$                   | I <sub>O</sub> =1A  | T <sub>j</sub> =25°C      |      | 2.0   |      | V     |
| Quiescent Current            | IQ                      |   | T <sub>j</sub> =25°C      |      |       | 8.0  | mA    |
| Quiescent Current Change     | $\Delta I_{Q}$          | V <sub>I</sub> =7.0 ~ 25V   | T <sub>j</sub> =0 ~ 125°C |      |       | 1.3  | mA    |
|                              |                         | I <sub>O</sub> =5mA ~ 1A  | 1                         |      |       | 0.5  | mA    |
| Short Circuit Output Current | I <sub>sc</sub>         |   | T <sub>j</sub> =25°C      |      | 750   |      | mA    |
| Peak Output Current          | I <sub>PK</sub>         |   | T <sub>i</sub> =25°C      |      | 2.2   |      | Α     |

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# PACKAGE D2PAK (TO-263)



D<sup>2</sup>PAK (TO-263)

#### **Component Disposal Instructions**

- 1. CDIL Semiconductor Devices are RoHS compliant, customers are requested to please dispose as per prevailing Environmental Legislation of their Country.
- 2. In Europe, please dispose as per EU Directive 2002/96/EC on Waste Electrical and Electronic Equipment (WEEE).

#### **Customer Notes**

#### **Disclaimer**

The product information and the selection guides facilitate selection of the CDIL's Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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