

Am54S/74S350

Four-Bit Shifter With Three-State Outputs

Distinctive Characteristics

- Shifts 4-bits of data to 0, 1, 2 or 3 places under control of two select lines.
- Three-state outputs for bus organized systems.

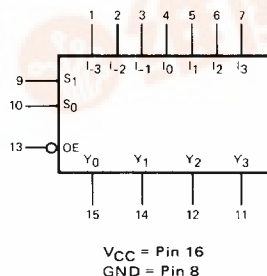
- 6.5ns typical data propagation delay.
- 100% reliability assurance testing in compliance with MIL-STD-883.

FUNCTIONAL DESCRIPTION

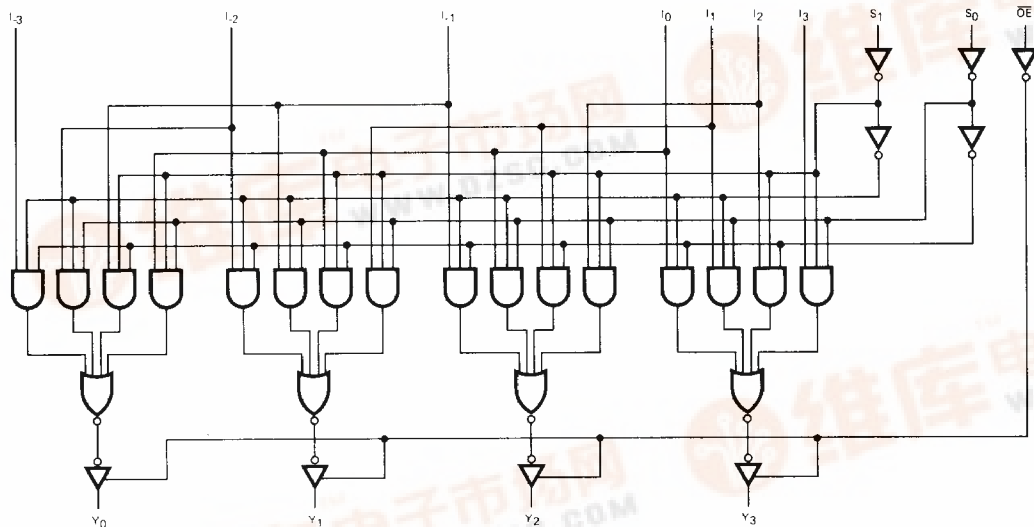
The Am54S/74S350 is a combinatorial logic circuit that accepts a four-bit data word and shifts the word 0, 1, 2 or 3 places. The number of places to be shifted is determined by a two-bit select field S_0 and S_1 . An active-LOW enable controls the three-state outputs. This feature allows expansion of shifting over a larger number of places with one delay.

By suitable interconnection, the Am54S/74S350 can be used to shift any number of bits any number of places up or down. Shifting can be logical, with logic zeroes pulled in at either or both ends of the shifting field; arithmetic, where the sign bit is repeated during a shift down; or end around, where the data word forms a continuous loop.

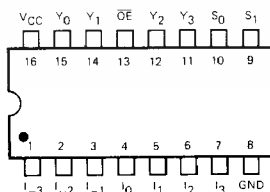
LOGIC SYMBOL



LOGIC DIAGRAM



CONNECTION DIAGRAM Top View



Note: Pin 1 is marked for orientation

Am54S/74S350

MAXIMUM RATINGS (Above which the useful life may be impaired)

Storage Temperature	−65°C to +150°C
Temperature (Ambient) Under Bias	−55°C to +125°C
Supply Voltage to Ground Potential (Pin 16 to Pin 8) Continuous	−0.5 V to +7 V
DC Voltage Applied to Outputs for High Output State	−0.5 V to +V _{CC} max.
DC Input Voltage	−0.5 V to +5.5 V
DC Output Current, Into Outputs	30 mA
DC Input Current	−30 mA to +5.0 mA

ELECTRICAL CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (Unless Otherwise Noted)

SN74S350	T _A = 0°C to +70°C	V _{CC} = 5.0 V ± 5% (COM'L)	MIN. = 4.75 V	MAX. = 5.25 V
SN54S350	T _A = −55°C to +125°C	V _{CC} = 5.0 V ± 10% (MIL)	MIN. = 4.5 V	MAX. = 5.5 V

Parameters	Description	Test Conditions (Note 1)	Min.	Typ. (Note 2)	Max.	Units
V _{OH}	Output HIGH Voltage	V _{CC} = MIN., V _{IN} = V _{IH} or V _{IL}	MIL, I _{OH} = −2mA COM'L, I _{OH} = −6.5mA	2.4 2.4	3.4 3.2	Volts
V _{OL}	Output LOW Voltage	V _{CC} = MIN., I _{OL} = 20mA V _{IN} = V _{IH} or V _{IL}			0.5	Volts
V _{IH}	Input HIGH Level	Guaranteed input logical HIGH voltage for all inputs	2.0			Volts
V _{IL}	Input LOW Level	Guaranteed input logical LOW voltage for all inputs			0.8	Volts
V _I	Input Clamp Voltage	V _{CC} = MIN., I _{IN} = −18mA			−1.2	Volts
I _{IL}	Unit Load Input LOW Current	V _{CC} = MAX., V _{IN} = 0.5 V			−2.0	mA
I _{IH}	Unit Load Input HIGH Current	V _{CC} = MAX., V _{IN} = 2.7 V			50	μA
I _O	Off State (High Impedance) Output Current	V _{CC} = MAX., V _O = 2.4 V V _O = 0.5 V			50 −50	μA
I _I	Input HIGH Current	V _{CC} = MAX., V _{IN} = 5.5 V			1.0	mA
I _{SC}	Output Short Circuit Current (Note 3)	V _{CC} = MAX., V _{OUT} = 0.0 V	−40		−100	mA
I _{CC}	Power Supply Current	V _{CC} = MAX., All outputs open, All inputs = GND		60	85	mA

- Notes: 1. For conditions shown as MIN. or MAX., use the appropriate value specified under Electrical Characteristics for the applicable device type.
2. Typical limits are at V_{CC} = 5.0V, 25°C ambient and maximum loading.
3. Not more than one output should be shorted at a time. Duration of the short circuit test should not exceed one second.

Switching Characteristics (T_A = +25°C)

Parameters		Description	Test Conditions	Min.	Typ.	Max.	Units		
t _{PLH}	Data Input to Output	Select to Output	V _{CC} = 5.0 V, C _L = 15 pF, R _L = 280 Ω		5	7.5	ns		
t _{PHL}					8	12			
t _{PLH}				11	17	ns			
t _{PHL}				13	20				
t _{ZH}	Output Control $\overline{OE}$ to Output	Output Control $\overline{OE}$ to Output	V _{CC} = 5.0 V, C _L = 15 pF, R _L = 280 Ω			19.5	ns		
t _{ZL}						21			
t _{HZ}	Output Control $\overline{OE}$ to Output			Output Control $\overline{OE}$ to Output	V _{CC} = 5 V, C _L = 5 pF, R _L = 280 Ω		5	8	ns
t _{LZ}								10	