

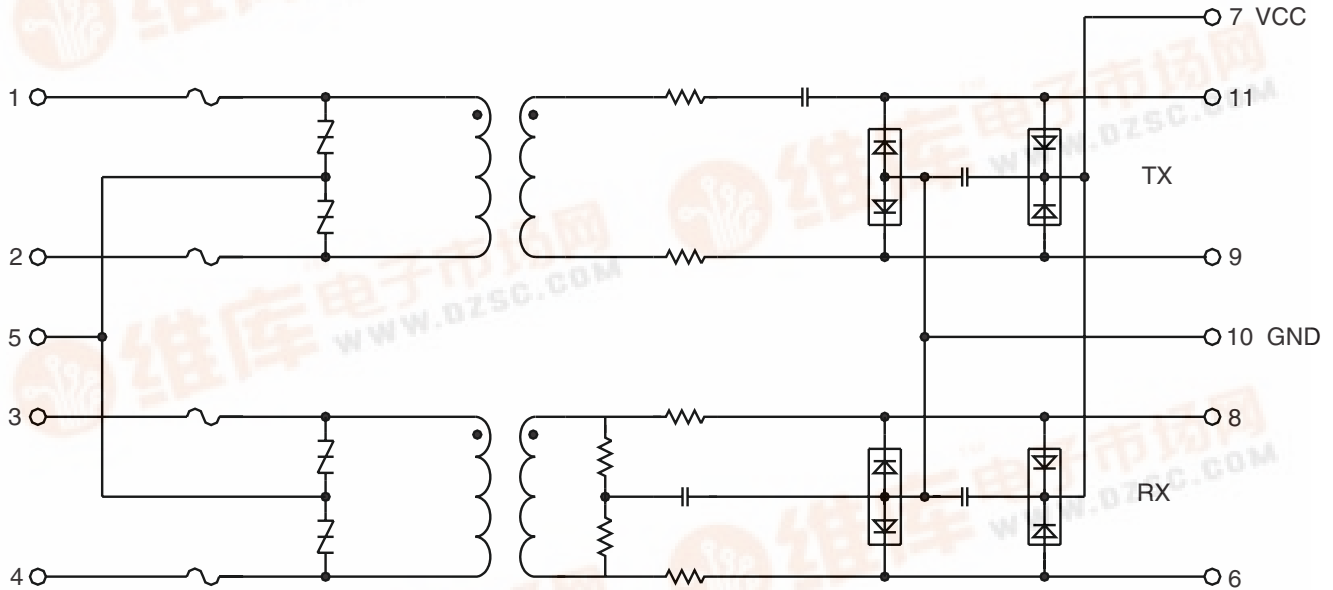


**“Slim Line” Hybrid for Dallas E1
APC82235**

FEATURES

- Supports Dallas chipset for E1 applications DS2155/Q55 LIU 3.3V IC 120 Ω impedance
- Minimum board space requirement - ideal for multiple interface line cards
- Longitudinal protection circuit designed to meet FCC part 68 as well as Telcordia GR1089 for 1st level and 2nd level lightning and 1st level power line cross events
- Approval for safety standards to IEC60950, BS EN60950 supplementary isolation 1500V, working voltage 250V
- “Slim Line” E1 hybrids have been designed to minimize the board space requirements for line interface circuitry on the E1 multichannel line cards

SCHEMATIC



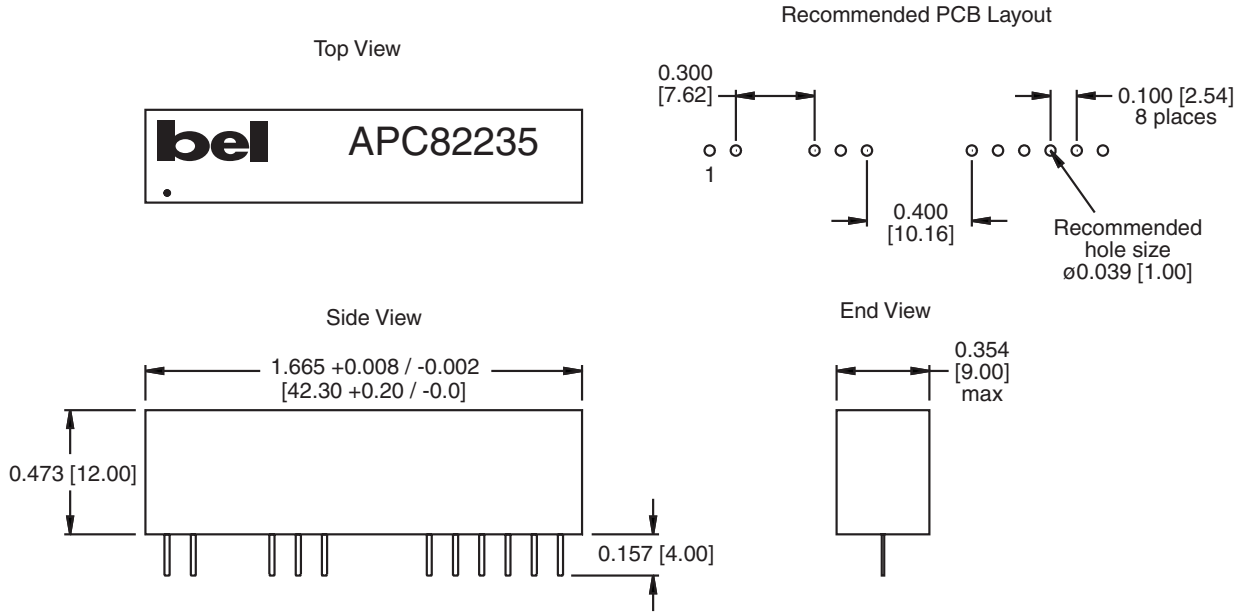
NOTES: TYPICAL SCHEMATIC (SELV COMPONENTS WILL VARY SUBJECT TO APPLICATION)





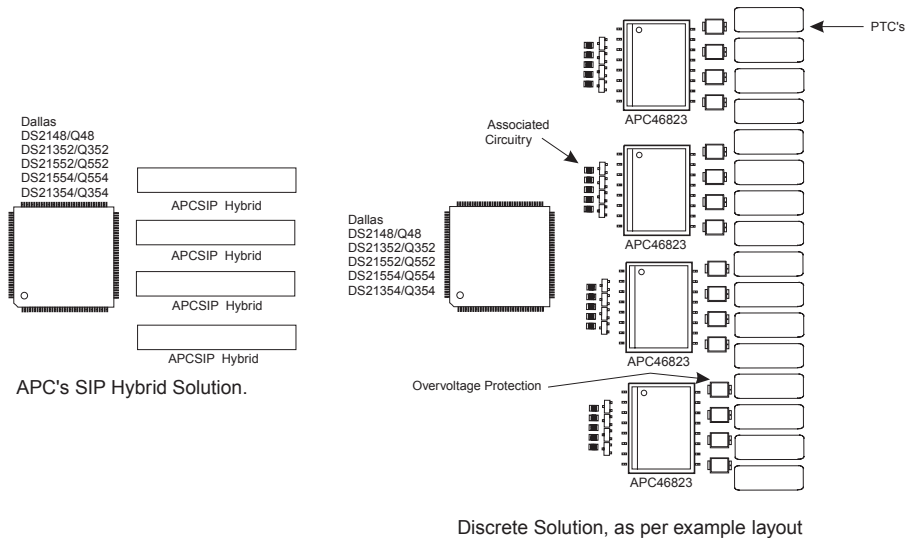
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MECHANICAL



NOTE: ALL DIMENSIONS IN MM

HYBRID V DISCRETE SOLUTIONS



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