



**Advanced Power
Electronics Corp.**

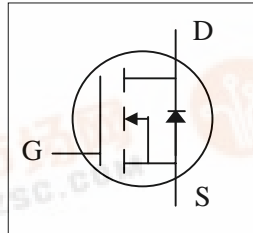
AP80N30W

RoHS-compliant Product

N-CHANNEL ENHANCEMENT MODE

POWER MOSFET

- ▼ Simple Drive Requirement
- ▼ Lower On-resistance
- ▼ High Speed Switching

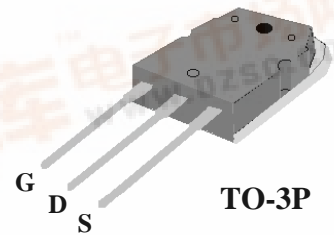


BV_{DSS}	300V
$R_{DS(ON)}$	66mΩ
I_D	88A

Description

AP80N30 from APEC provide the designer with the best combination of fast switching , low on-resistance and cost-effectiveness .

The TO-3P package is preferred for commercial & industrial applications with higher power level preclusion than TO-220 device.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	300	V
V_{GS}	Gate-Source Voltage	±30	V
$I_D@T_C=25^\circ C$	Drain Current, $V_{GS} @ 10V$	88	A
I_{DM}	Pulsed Drain Current ¹	270	A
I_{DR}	Body-Drain Diode Reverse Drain Current	88	A
$I_{DR(PULSE)}$	Body-Drain Diode Reverse Drain Peak Current ¹	270	A
$P_D@T_C=25^\circ C$	Total Power Dissipation	150	W
I_{AR}	Avalanche Current ³	30	A
E_{AR}	Single Pulse Avalanche Energy ³	45	mJ
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature	150	°C

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Maximum Thermal Resistance, Junction-case	0.833	°C/W
Rthj-a	Maximum Thermal Resistance, Junction-ambient	40	°C/W





AP80N30W

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =10mA	300	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =10V, I _D =40A	-	-	66	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =250uA	3	-	4.5	V
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =40A	-	38	-	S
I _{DSS}	Drain-Source Leakage Current	V _{DS} =300V, V _{GS} =0V	-	-	10	uA
	Drain-Source Leakage Current (T _j =125°C)	V _{DS} =300V, V _{GS} =0V	-	-	200	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±30V, V _{DS} =0V	-	-	±0.1	uA
Q _g	Total Gate Charge ²	I _D =80A	-	113	180	nC
Q _{gs}	Gate-Source Charge	V _{DS} =240V	-	31	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =10V	-	44	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =150V	-	40	-	ns
t _r	Rise Time	I _D =40A	-	130	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =10Ω, V _{GS} =10V	-	150	-	ns
t _f	Fall Time	R _D =3.75Ω	-	115	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	5700	9120	pF
C _{oss}	Output Capacitance	V _{DS} =30V	-	525	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	10	-	pF

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =80A, V _{GS} =0V	-	-	1.5	V
t _{rr}	Reverse Recovery Time ²	I _S =12A, V _{GS} =0V	-	310	-	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs	-	3.5	-	μC

Notes:

- 1.PW ≤ 10 μs, duty cycle ≤ 1%.
- 2.Pulse test
- 3.STch = 25°C · Tch ≤ 150°C

THIS PRODUCT IS SENSITIVE TO ELECTROSTATIC DISCHARGE, PLEASE HANDLE WITH CAUTION.

USE OF THIS PRODUCT AS A CRITICAL COMPONENT IN LIFE SUPPORT OR OTHER SIMILAR SYSTEMS IS NOT AUTHORIZED.

APEC DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

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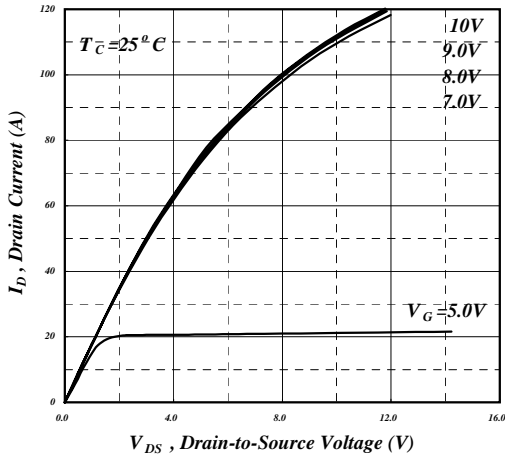


Fig 1. Typical Output Characteristics

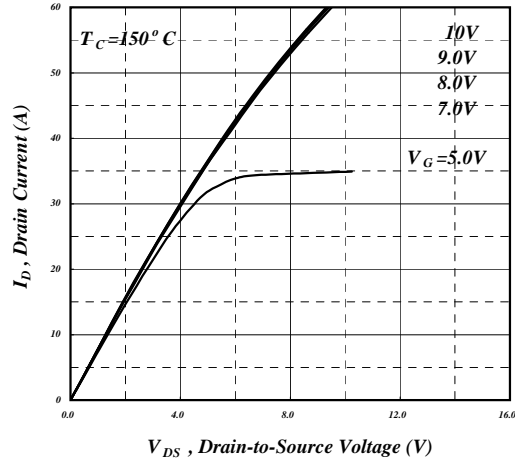


Fig 2. Typical Output Characteristics

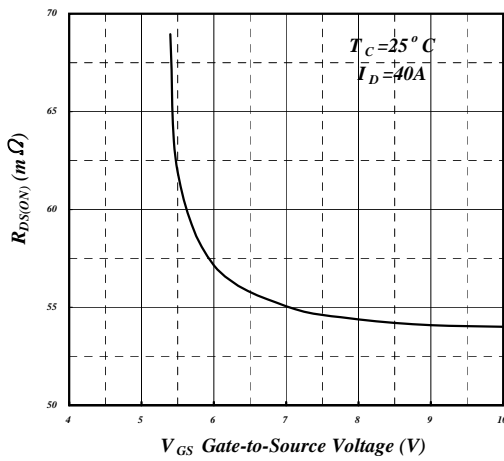


Fig 3. On-Resistance v.s. Gate Voltage

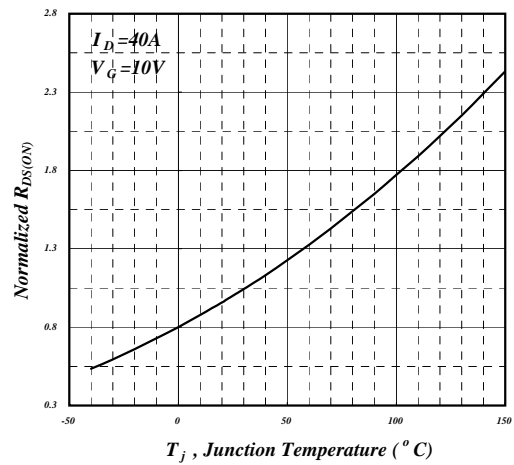


Fig 4. Normalized On-Resistance v.s. Junction Temperature

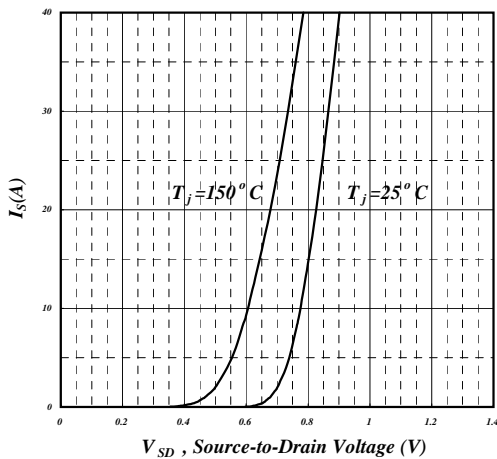


Fig 5. Forward Characteristic of Reverse Diode

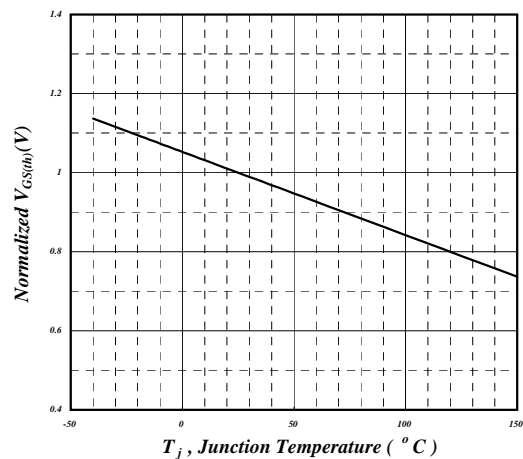


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

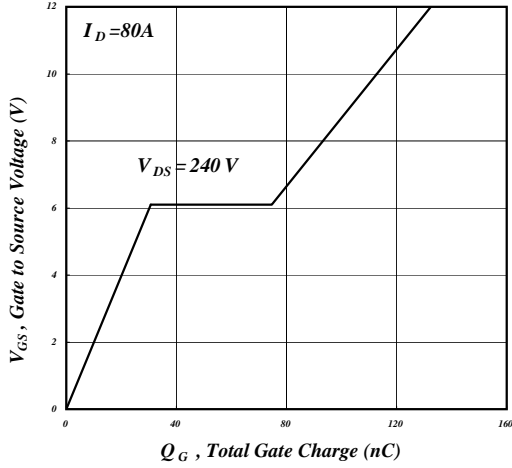


Fig 7. Gate Charge Characteristics

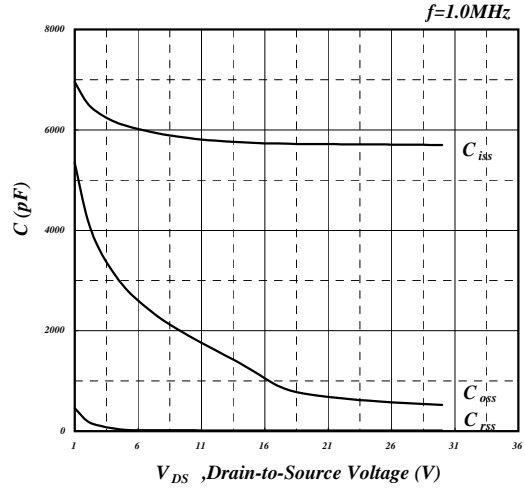


Fig 8. Typical Capacitance Characteristics

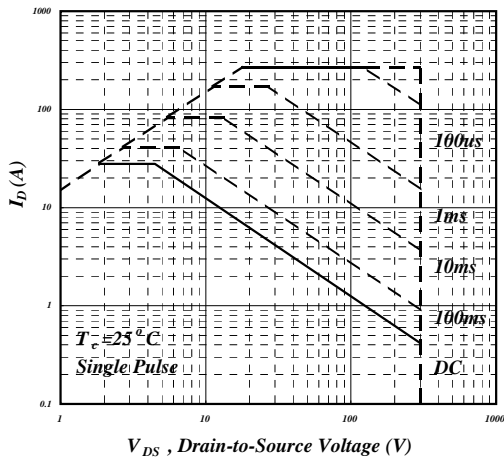


Fig 9. Maximum Safe Operating Area

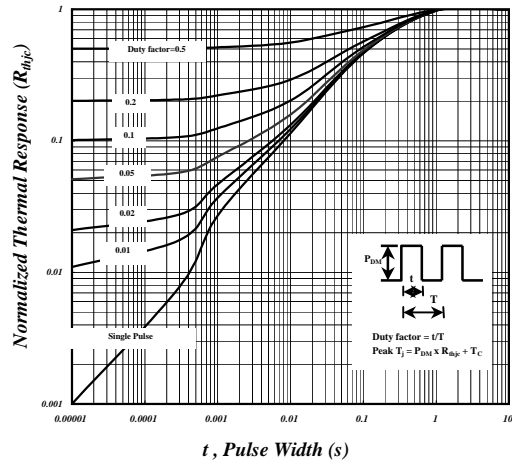


Fig 10. Effective Transient Thermal Impedance

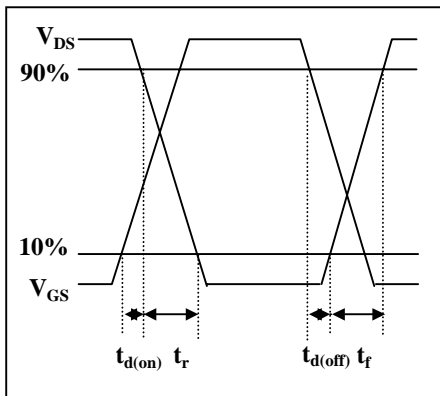


Fig 11. Switching Time Waveform

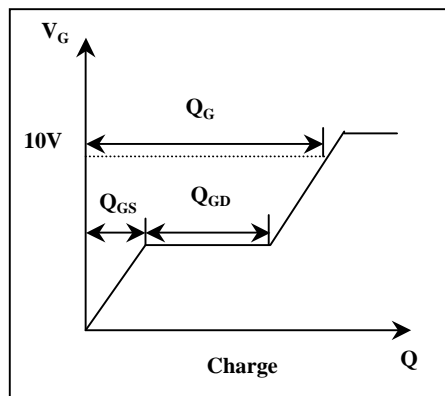
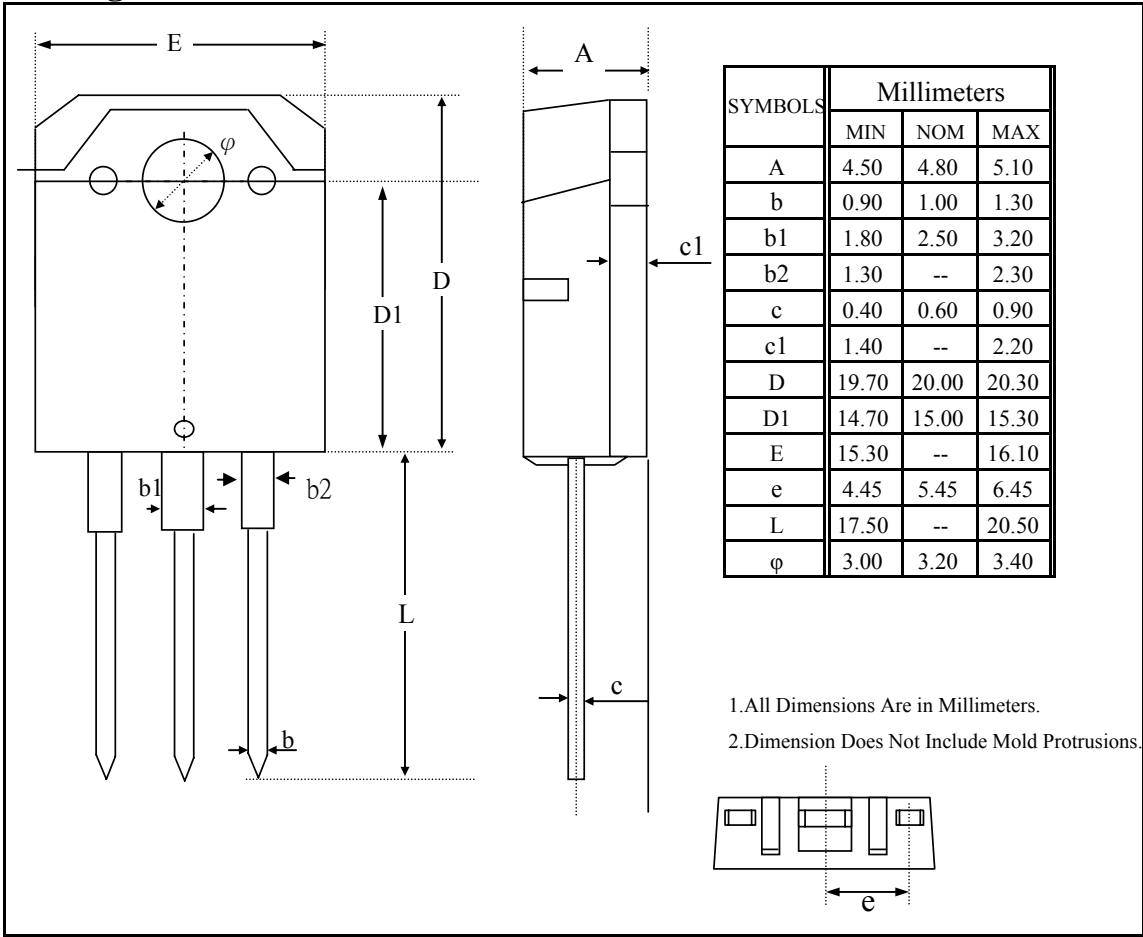


Fig 12. Gate Charge Waveform



Package Outline : TO-3P



Part Marking Information & Packing : TO-3P

