



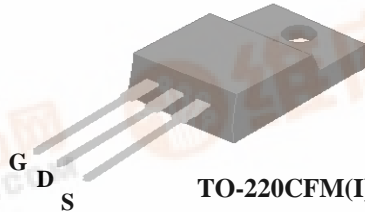
**Advanced Power
Electronics Corp.**

AP9575GI

RoHS-compliant Product

*P-CHANNEL ENHANCEMENT MODE
POWER MOSFET*

- ▼ Lower Gate Charge
- ▼ Simple Drive Requirement
- ▼ Fast Switching Characteristic

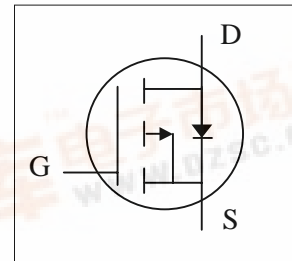


BV_{DSS}	-60V
$R_{DS(ON)}$	70mΩ
I_D	-16A

Description

The Advanced Power MOSFETs from APEC provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220CFM isolation package is universally preferred for all commercial-industrial through hole applications.



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	-60	V
V_{GS}	Gate-Source Voltage	±20	V
$I_D @ T_C=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	-16	A
$I_D @ T_C=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	-10	A
I_{DM}	Pulsed Drain Current ¹	-60	A
$P_D @ T_C=25^\circ C$	Total Power Dissipation	31.25	W
	Linear Derating Factor	0.25	W/°C
T_{STG}	Storage Temperature Range	-55 to 150	°C
T_J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Data

Symbol	Parameter	Value	Units
Rthj-c	Thermal Resistance Junction-case	Max. 4.0	°C/W
Rthj-a	Thermal Resistance Junction-ambient	Max. 65	°C/W



AP9575GI

Electrical Characteristics @T_j=25°C(unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250uA	-60	-	-	V
R _{DS(ON)}	Static Drain-Source On-Resistance ²	V _{GS} =-10V, I _D =-10A	-	-	70	mΩ
		V _{GS} =-4.5V, I _D =-8A	-	-	90	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{DS} =V _{GS} , I _D =-250uA	-1	-	-3	V
g _{fs}	Forward Transconductance	V _{DS} =-10V, I _D =-10A	-	14	-	S
I _{DSS}	Drain-Source Leakage Current (T _j =25°C)	V _{DS} =-60V, V _{GS} =0V	-	-	-10	uA
		V _{DS} =-48V, V _{GS} =0V	-	-	-25	uA
I _{GSS}	Gate-Source Leakage	V _{GS} = ±20V	-	-	±100	nA
Q _g	Total Gate Charge ²	I _D =-10A	-	21	34	nC
Q _{gs}	Gate-Source Charge	V _{DS} =-50V	-	5	-	nC
Q _{gd}	Gate-Drain ("Miller") Charge	V _{GS} =-4.5V	-	11	-	nC
t _{d(on)}	Turn-on Delay Time ²	V _{DS} =-30V	-	10	-	ns
t _r	Rise Time	I _D =-10A	-	18	-	ns
t _{d(off)}	Turn-off Delay Time	R _G =3.3Ω, V _{GS} =-10V	-	24	-	ns
t _f	Fall Time	R _D =3Ω	-	42	-	ns
C _{iss}	Input Capacitance	V _{GS} =0V	-	1760	2800	pF
C _{oss}	Output Capacitance	V _{DS} =-25V	-	160	-	pF
C _{rss}	Reverse Transfer Capacitance	f=1.0MHz	-	130	-	pF
R _g	Gate Resistance	f=1.0MHz	-	5	8	Ω

Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
V _{SD}	Forward On Voltage ²	I _S =-10A, V _{GS} =0V	-	-	-1.3	V
t _{rr}	Reverse Recovery Time ²	I _S =-10A, V _{GS} =0V,	-	46	-	ns
Q _{rr}	Reverse Recovery Charge	dI/dt=-100A/μs	-	100	-	nC

Notes:

1.Pulse width limited by Max. junction temperature.

2.Pulse test

THIS PRODUCT IS ELECTROSTATIC SENSITIVE, PLEASE HANDLE WITH CAUTION.

THIS PRODUCT HAS BEEN QUALIFIED FOR USE IN CONSUMER APPLICATIONS. APPLICATIONS OR USE IN LIFE SUPPORT OR OTHER SIMILAR MISSION-CRITICAL DEVICES OR SYSTEMS ARE NOT AUTHORIZED.

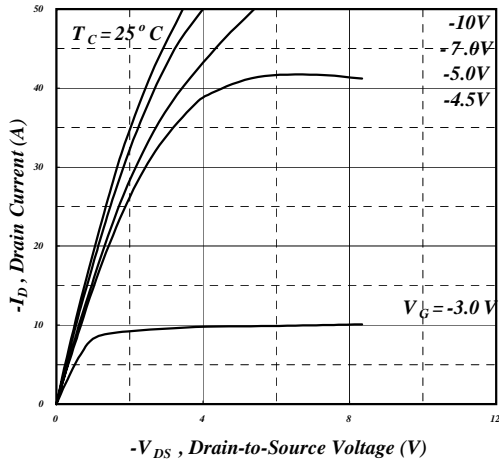


Fig 1. Typical Output Characteristics

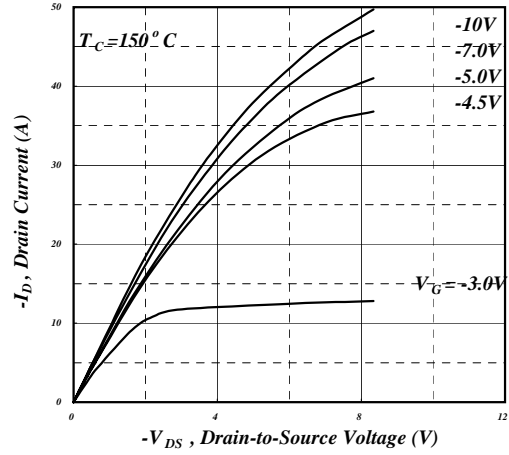


Fig 2. Typical Output Characteristics

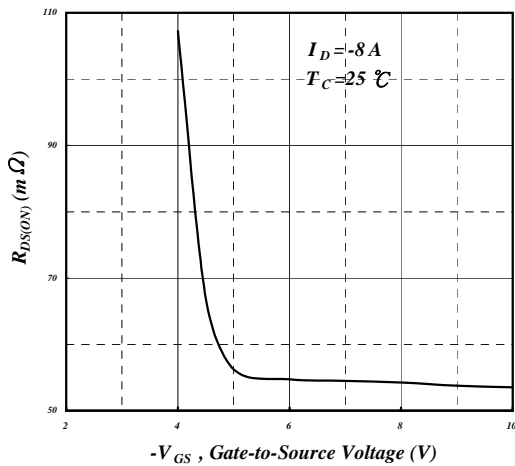


Fig 3. On-Resistance v.s. Gate Voltage

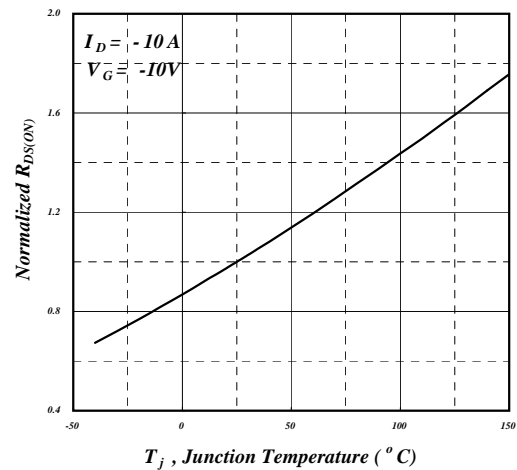


Fig 4. Normalized On-Resistance v.s. Junction Temperature

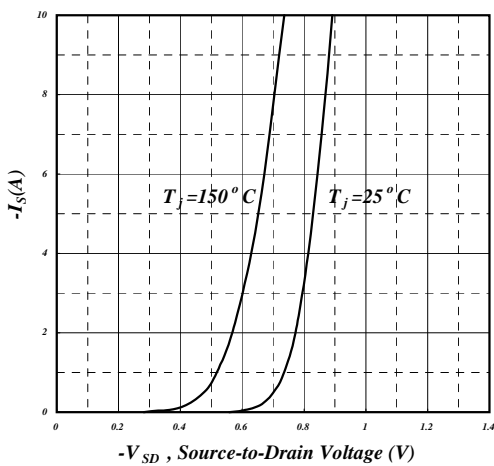


Fig 5. Forward Characteristic of Reverse Diode

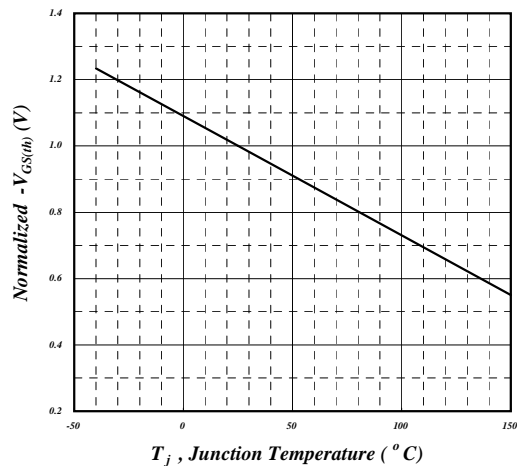


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

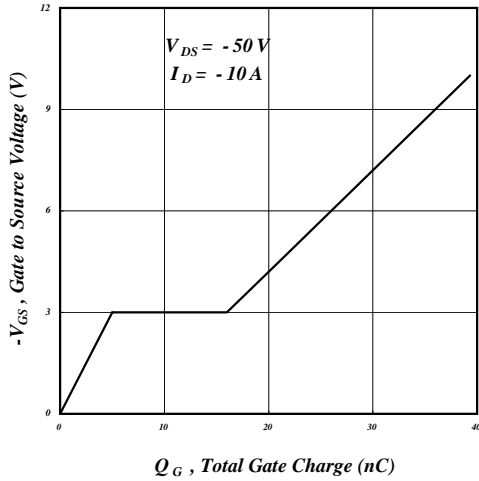


Fig 7. Gate Charge Characteristics

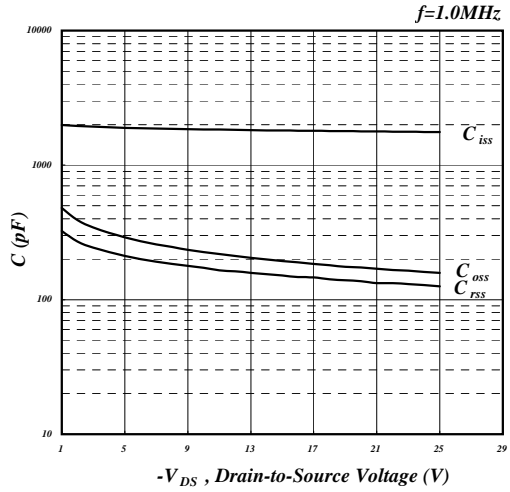


Fig 8. Typical Capacitance Characteristics

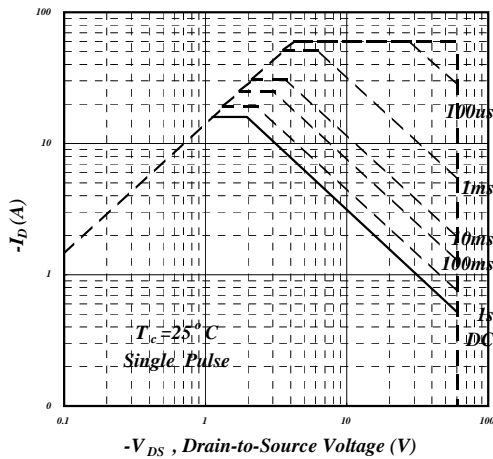


Fig 9. Maximum Safe Operating Area

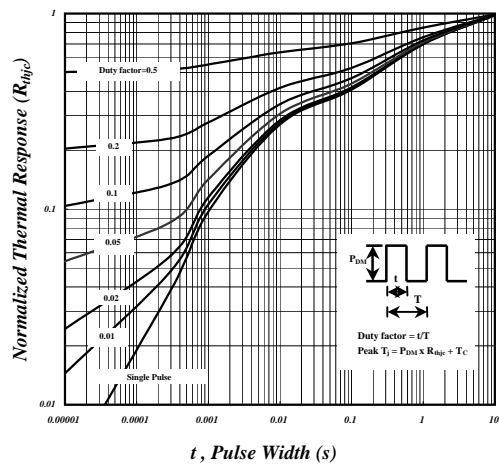


Fig 10. Effective Transient Thermal Impedance

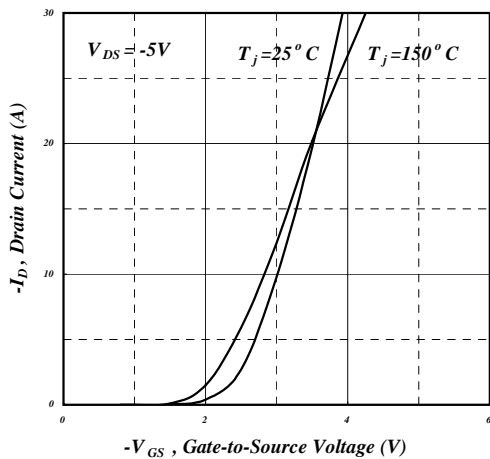


Fig 11. Transfer Characteristics

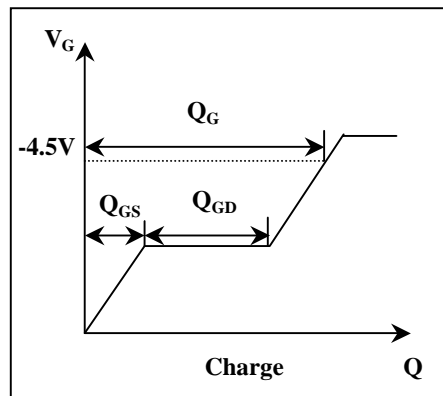


Fig 12. Gate Charge Waveform