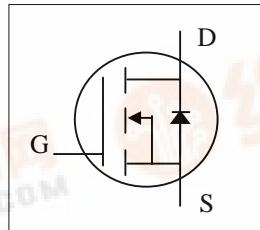


**AP01L60T**

**Advanced Power  
Electronics Corp.**

**N-CHANNEL ENHANCEMENT MODE  
POWER MOSFET**

- ▼ Low Gate Charge
- ▼ Fast Switching Characteristics
- ▼ Simple Drive Requirement

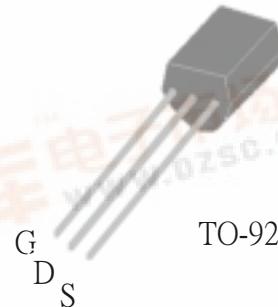


$BV_{DSS}$	600V
$R_{DS(ON)}$	12Ω
$I_D$	160mA

**Description**

Advanced Power MOSFETs utilized advanced processing techniques to achieve the lowest possible on-resistance, extremely efficient and cost-effectiveness device.

The TO-92 package is universally used for all commercial-industrial applications.

**Absolute Maximum Ratings**

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	600	V
$V_{GS}$	Gate-Source Voltage	$\pm 30$	V
$I_D @ T_A=25^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	160	mA
$I_D @ T_A=100^\circ C$	Continuous Drain Current, $V_{GS} @ 10V$	100	mA
$I_{DM}$	Pulsed Drain Current <sup>1</sup>	300	mA
$P_D @ T_C=25^\circ C$	Total Power Dissipation	0.83	W
$T_{STG}$	Storage Temperature Range	-55 to 150	°C
$T_J$	Operating Junction Temperature Range	-55 to 150	°C

**Thermal Data**

Symbol	Parameter	Value	Unit
$R_{thj-a}$	Thermal Resistance Junction-ambient	Max.	150 °C/W



**AP01L60T**

### Electrical Characteristics@ $T_j=25^\circ\text{C}$ (unless otherwise specified)

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$\text{BV}_{\text{DSS}}$	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=1\text{mA}$	600	-	-	V
$\Delta \text{BV}_{\text{DSS}}/\Delta T_j$	Breakdown Voltage Temperature Coefficient	Reference to $25^\circ\text{C}$ , $I_{\text{D}}=1\text{mA}$	-	0.8	-	$\text{V}/^\circ\text{C}$
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance <sup>2</sup>	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=0.5\text{A}$	-	-	12	$\Omega$
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=250\mu\text{A}$	2	-	4	V
$g_{\text{fs}}$	Forward Transconductance	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=0.5\text{A}$	-	0.8	-	S
$I_{\text{DSS}}$	Drain-Source Leakage Current ( $T_j=25^\circ\text{C}$ )	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	$\mu\text{A}$
	Drain-Source Leakage Current ( $T_j=150^\circ\text{C}$ )	$V_{\text{DS}}=480\text{V}, V_{\text{GS}}=0\text{V}$	-	-	100	$\mu\text{A}$
$I_{\text{GSS}}$	Gate-Source Leakage	$V_{\text{GS}}= \pm 30\text{V}$	-	-	$\pm 100$	nA
$Q_g$	Total Gate Charge <sup>3</sup>	$I_{\text{D}}=1\text{A}$	-	4.0	-	nC
$Q_{\text{gs}}$	Gate-Source Charge	$V_{\text{DS}}=480\text{V}$	-	1.0	-	nC
$Q_{\text{gd}}$	Gate-Drain ("Miller") Charge	$V_{\text{GS}}=10\text{V}$	-	1.1	-	nC
$t_{\text{d(on)}}$	Turn-on Delay Time <sup>3</sup>	$V_{\text{DD}}=300\text{V}$	-	6.6	-	ns
$t_r$	Rise Time	$I_{\text{D}}=1\text{A}$	-	5.0	-	ns
$t_{\text{d(off)}}$	Turn-off Delay Time	$R_G=3.3\Omega, V_{\text{GS}}=10\text{V}$	-	11.7	-	ns
$t_f$	Fall Time	$R_D=300\Omega$	-	9.2	-	ns
$C_{\text{iss}}$	Input Capacitance	$V_{\text{GS}}=0\text{V}$	-	170	-	pF
$C_{\text{oss}}$	Output Capacitance	$V_{\text{DS}}=25\text{V}$	-	30.7	-	pF
$C_{\text{rss}}$	Reverse Transfer Capacitance	f=1.0MHz	-	5.1	-	pF

### Source-Drain Diode

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$I_s$	Continuous Source Current ( Body Diode )	$V_D=V_G=0\text{V}, V_S=1.2\text{V}$	-	-	160	mA
$V_{\text{SD}}$	Forward On Voltage <sup>3</sup>	$I_s=160\text{mA}, V_{\text{GS}}=0\text{V}$	-	-	1.2	V

### Notes:

- 1.Pulse width limited by safe operating area.
- 3.Pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .



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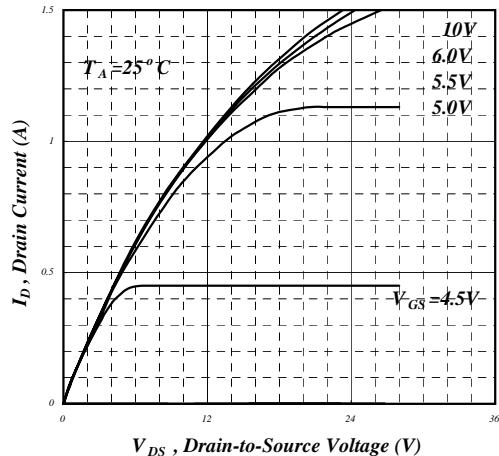


Fig 1. Typical Output Characteristics

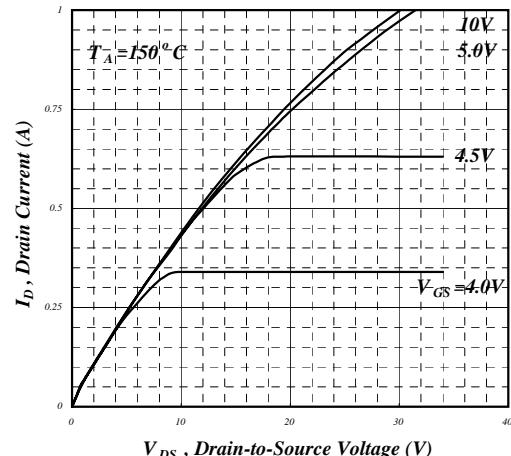


Fig 2. Typical Output Characteristics

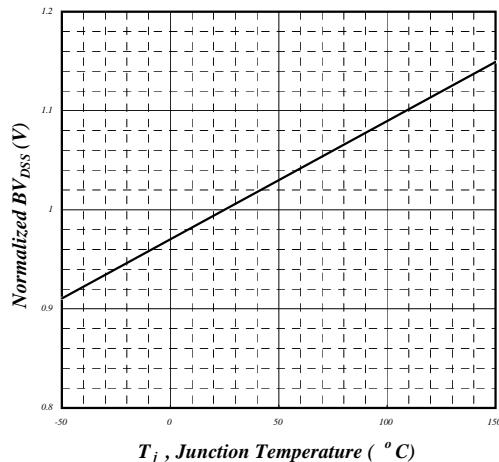


Fig 3. Normalized  $BV_{DSS}$  v.s. Junction Temperature

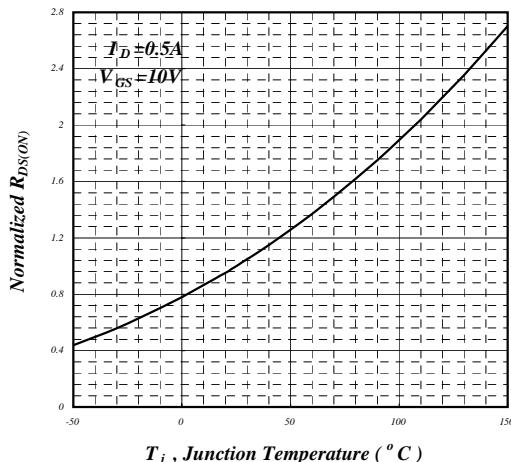


Fig 4. Normalized On-Resistance v.s. Junction Temperature

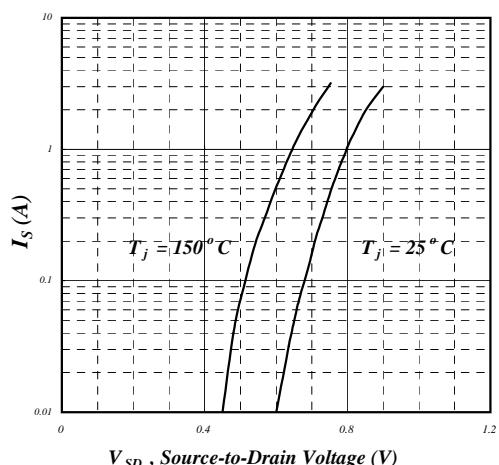


Fig 5. Forward Characteristic of Reverse Diode

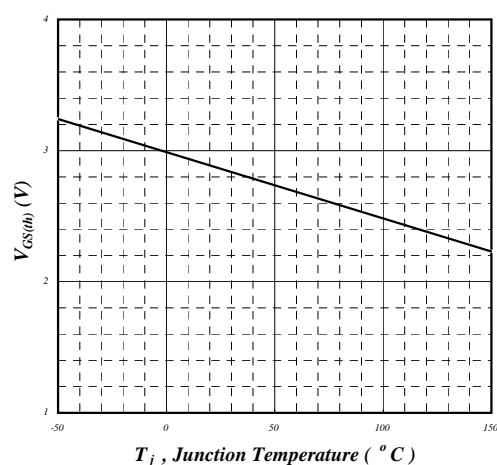
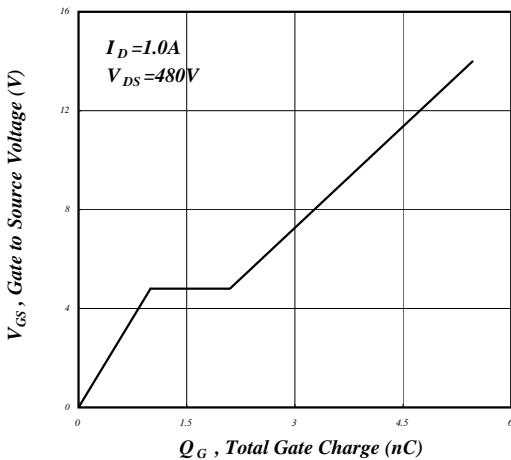
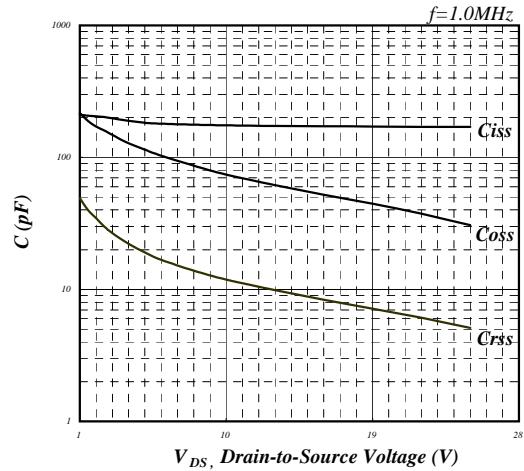


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

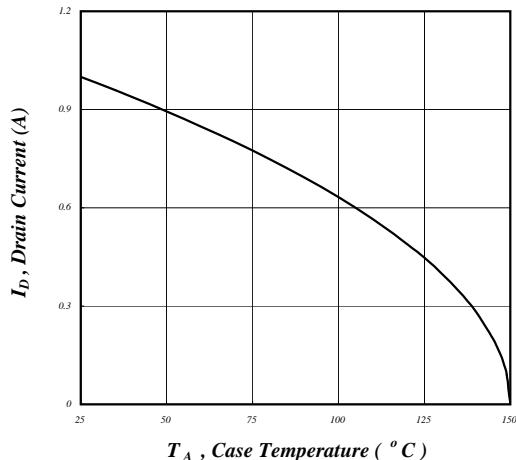
# AP01L60T



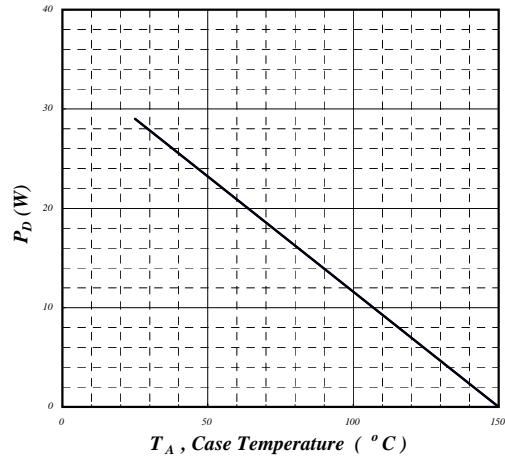
**Fig 7. Gate Charge Characteristics**



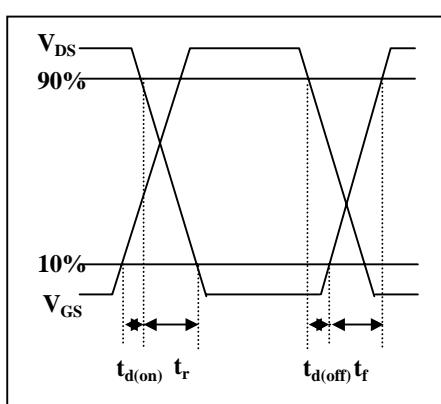
**Fig 8. Typical Capacitance Characteristics**



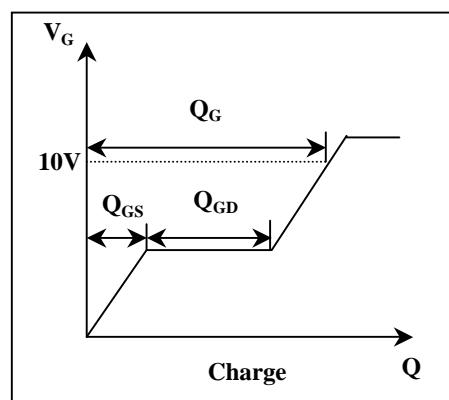
**Fig 9. Maximum Drain Current v.s. Case Temperature**



**Fig 10. Typical Power Dissipation**



**Fig 11. Switching Time Waveform**



**Fig 12. Gate Charge Waveform**