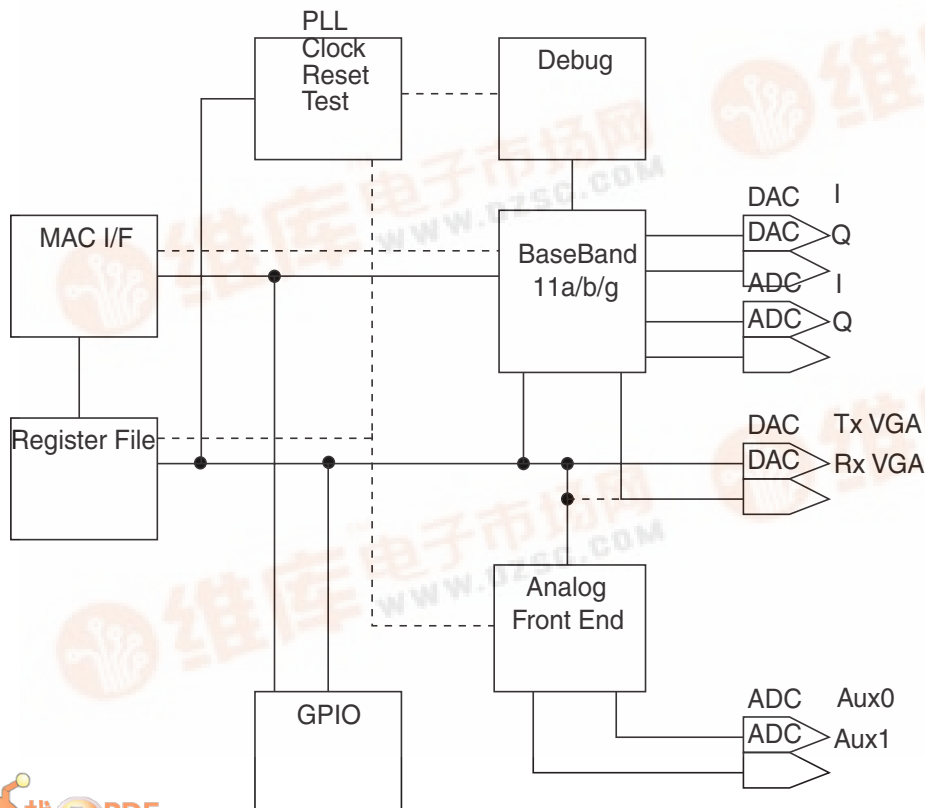


Features

- On-chip OFDM Baseband Supports All the Transmission Modes of the IEEE 802.11a Specification and the OFDM Modes of the IEEE 802.11g Specification (6, 9, 12, 18, 24, 36, 48, and 54 Mbps)
- On-chip DSSS/CCK Baseband Supports All Transmission Modes of the IEEE 802.11b Specification (1, 2, 5.5, 11 Mbps)
- Single 20 MHz or 40 MHz Input Clock Generates the Internal System Clock and the 44 MHz Clock for the DSSS/CCK Baseband
- Low Pin Count Flexible MAC Layer Interface
- Flexible RF Interface Allows Glueless Communication with Various Dual Mode (802.11a/g) RF Devices
- On-chip Automatic Gain Control
- On-chip DC Offset Remove Mechanism
- Analog and Digital Gain Vector for the RF Transceiver
- SPI Interface for Configuration/Control
- 8 GPIO to Control External 11a/g PAs, On/Off of RF Devices and Antenna Diversity
- Digital or Analog Interface to the RF Devices
- Up to 4 Auxiliary Analog to Digital Converters (Depending On the Package)
- Up to 2 Auxiliary Purpose Digital to Analog Converters
- 1.8 Volt Core Voltage Supply
- 3.3 Volt I/O Voltage Supply
- 100-ball TFBGA Package

802.11a/b/g BaseBand Controller



802.11a/b/g Baseband Controller

AT76C517 Summary

NOTE: This is a summary document. The complete document is available under NDA. For more information, please contact your local Atmel sales office.





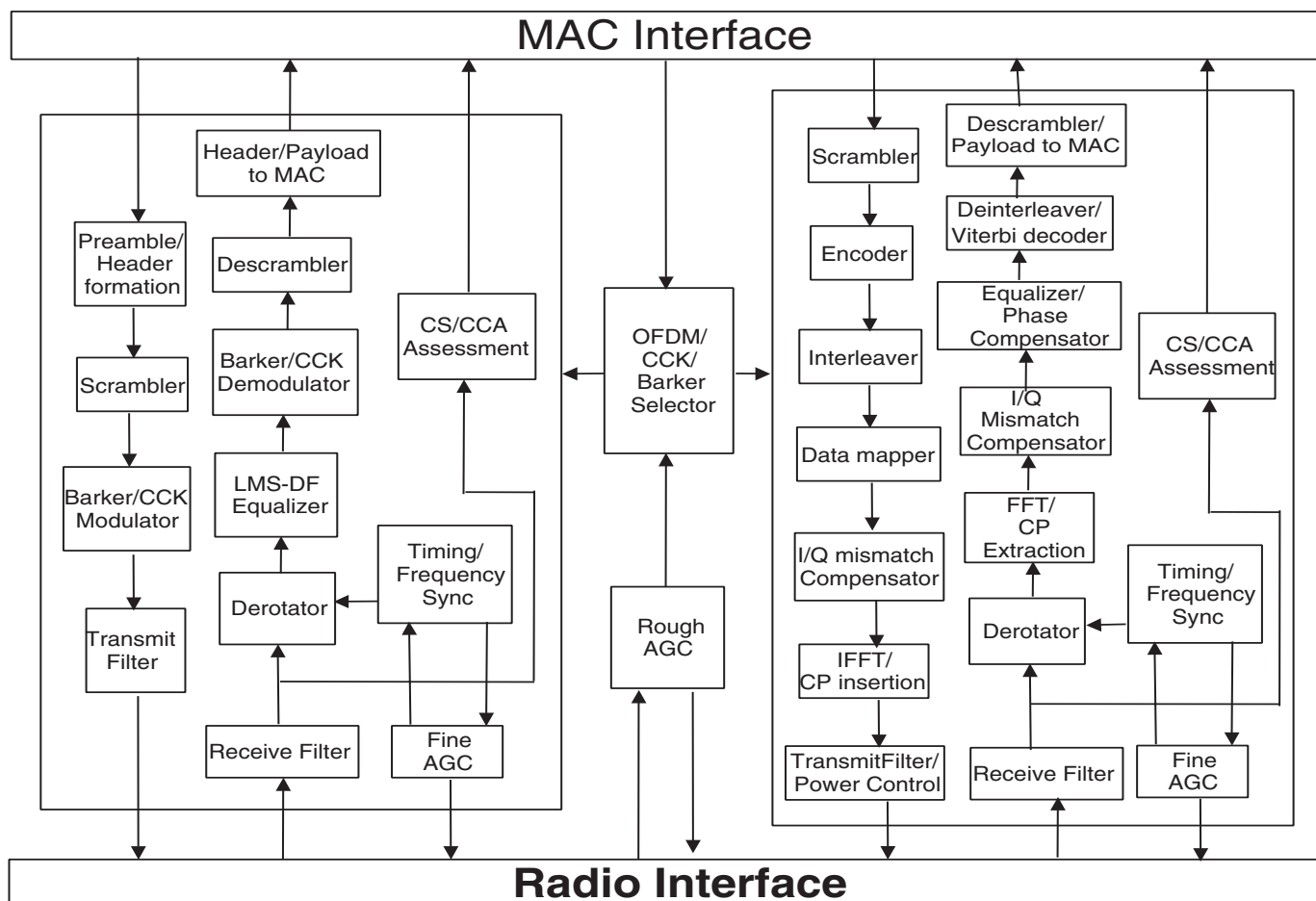
1. Description

The AT76C517 provides complete modem functionality for an 802.11a/b/g wireless LAN chip set. It contains signal processing functionality for both DSSS/CCK and OFDM modulation/demodulation. It supports both short and long PLCP preamble formats for 802.11b rates, while it incorporates efficient compensation algorithms for improving the performance of the OFDM modem part.

The AT76C517 provides an interface to a MAC processor consisting of a bi-directional data bus, data synchronization signals and a serial-to-parallel port interface. The AT76C517 can exchange either payload data or TX/RX vector and payload data with the MAC over a bi-directional data bus in a half-duplex fashion using data synchronization signals. The interface allows the baseband and the MAC controllers to operate at different speeds incorporating FIFOs in both directions. The serial-to-parallel port interface provides access to all internal registers and is able to run up to 20 MHz.

The AT76C517 is able to interface with various 802.11a/g radio devices using analog or proprietary digital I/Q interface, analog or digital signals for automatic gain control, gain lock indication, antenna diversity control, LNA "gain on" and various other signals which depict the state of internal critical circuits related to the front-end logic of the modem.

2. Functional diagram





Atmel Corporation

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 487-2600

Regional Headquarters

Europe

Atmel Sarl
Route des Arsenaux 41
Case Postale 80
CH-1705 Fribourg
Switzerland
Tel: (41) 26-426-5555
Fax: (41) 26-426-5500

Asia

Room 1219
Chinachem Golden Plaza
77 Mody Road Tsimshatsui
East Kowloon
Hong Kong
Tel: (852) 2721-9778
Fax: (852) 2722-1369

Japan

9F, Tonetsu Shinkawa Bldg.
1-24-8 Shinkawa
Chuo-ku, Tokyo 104-0033
Japan
Tel: (81) 3-3523-3551
Fax: (81) 3-3523-7581

Atmel Operations

Memory

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

Microcontrollers

2325 Orchard Parkway
San Jose, CA 95131, USA
Tel: 1(408) 441-0311
Fax: 1(408) 436-4314

La Chantrerie
BP 70602
44306 Nantes Cedex 3, France
Tel: (33) 2-40-18-18-18
Fax: (33) 2-40-18-19-60

ASIC/ASSP/Smart Cards

Zone Industrielle
13106 Rousset Cedex, France
Tel: (33) 4-42-53-60-00
Fax: (33) 4-42-53-60-01

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Scottish Enterprise Technology Park
Maxwell Building
East Kilbride G75 0QR, Scotland
Tel: (44) 1355-803-000
Fax: (44) 1355-242-743

RF/Automotive

Theresienstrasse 2
Postfach 3535
74025 Heilbronn, Germany
Tel: (49) 71-31-67-0
Fax: (49) 71-31-67-2340

1150 East Cheyenne Mtn. Blvd.
Colorado Springs, CO 80906, USA
Tel: 1(719) 576-3300
Fax: 1(719) 540-1759

Biometrics/Imaging/Hi-Rel MPU/ High-Speed Converters/RF Datacom

Avenue de Rochepleine
BP 123
38521 Saint-Egreve Cedex, France
Tel: (33) 4-76-58-30-00
Fax: (33) 4-76-58-34-80

Literature Requests

www.atmel.com/literature

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