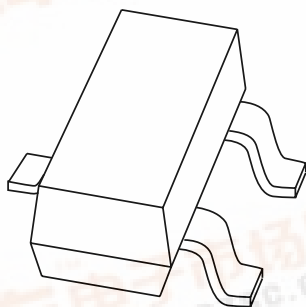


DISCRETE SEMICONDUCTORS

DATA SHEET



BAP63-05 Silicon PIN diode

Objective specification

2000 Feb 17

Silicon PIN diode

BAP63-05

FEATURES

- High speed switching for RF signals
- Low diode capacitance
- Low diode forward resistance
- Low series inductance
- For applications up to 3 GHz.

APPLICATIONS

- RF attenuators and switches.

DESCRIPTION

Two planar PIN diodes in common cathode configuration in a SOT23 small plastic SMD package.

PINNING

PIN	DESCRIPTION
1	anode (a1)
2	anode (a2)
3	common cathode

The figure shows two representations of the BAP63-05 component. On the left is a 'Top view' of the SOT23 package, a rectangular component with three pins: pin 1 at the top right, pin 2 at the top left, and pin 3 at the bottom center. On the right is the circuit symbol, which is an oval containing two diode symbols pointing towards a central common cathode point. Pin 1 is on the right, pin 2 is on the left, and pin 3 is at the bottom. The symbol is labeled 'MAM108'.

Marking code:

Fig.1 Simplified outline (SOT23) and symbol.

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
Per diode					
V _R	continuous reverse voltage		–	50	V
I _F	continuous forward current		–	100	mA
P _{tot}	total power dissipation	T _s = 90 °C	–	250	mW
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–65	+150	°C

Silicon PIN diode

BAP63-05

ELECTRICAL CHARACTERISTICS

$T_j = 25\text{ }^{\circ}\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	TYP.	MAX.	UNIT
Per diode					
V_F	forward voltage	$I_F = 100\text{ mA}$	0.95	1.2	V
I_R	reverse current	$V_R = 35\text{ V}$	–	10	μA
C_d	diode capacitance	$V_R = 0$; $f = 100\text{ MHz}$	0.3	–	pF
		$V_R = 5\text{ V}$; $f = 1\text{ MHz}$	0.21	0.3	pF
r_D	diode forward resistance	$I_F = 5\text{ mA}$; $f = 100\text{ MHz}$; note 1	1.2	2	Ω
		$I_F = 10\text{ mA}$; $f = 100\text{ MHz}$; note 1	1	–	Ω
τ_L	charge carrier life time	when switched from $I_F = 10\text{ mA}$ to $I_R = 6\text{ mA}$; $R_L = 100\text{ }\Omega$; measured at $I_R = 3\text{ mA}$	75	–	ns
L_S	series inductance		1.4	–	nH

Note

1. Guaranteed on AQL basis: inspection level S4, AQL 1.0.

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
$R_{th\ j-s}$	thermal resistance from junction to soldering point	220	K/W

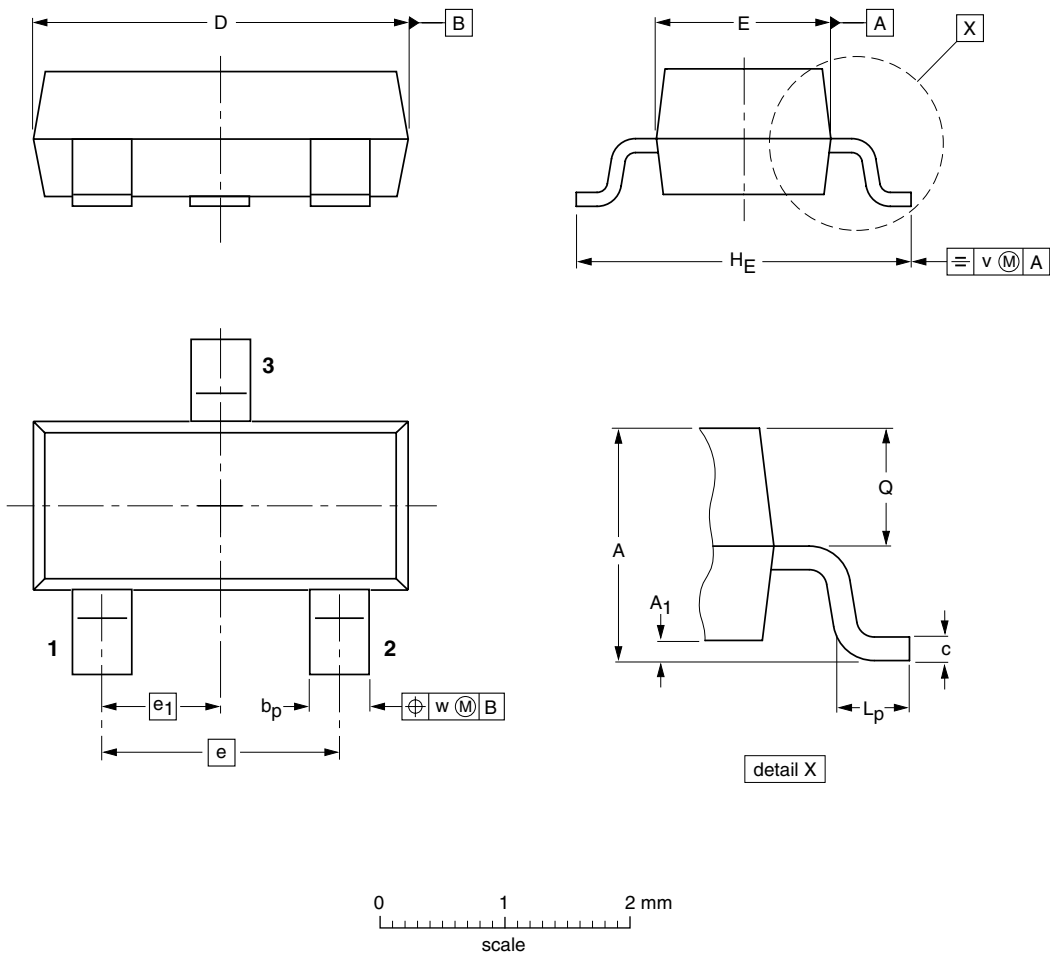
Silicon PIN diode

BAP63-05

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



DIMENSIONS (mm are the original dimensions)

UNIT	A	A ₁ max.	b _p	c	D	E	e	e ₁	H _E	L _p	Q	v	w
mm	1.1 0.9	0.1	0.48 0.38	0.15 0.09	3.0 2.8	1.4 1.2	1.9	0.95	2.5 2.1	0.45 0.15	0.55 0.45	0.2	0.1

OUTLINE VERSION	REFERENCES				EUROPEAN PROJECTION	ISSUE DATE
	IEC	JEDEC	EIAJ			
SOT23		TO-236AB				97-02-28- 99-09-13

Silicon PIN diode

BAP63-05

DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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