

Structure : Silicon Monolithic Integrated Circuit

Product name : Basic 3 Outputs Video Drivers

Type : **BA7623F**

Features : Since the input is coming directly with no bias at the base pin, the DC potential for the previous stage can be used for direct DC coupling. Each output can drive two loads ($75\Omega \times 2$)

○Absolute Maximum Ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Supply voltage	V _{Max}	8.0	V
Power dissipation	Pd	550 *	mW
Operating temperature	Topr	-25~+75	°C
Storage temperature	Tstg	-55~+125	°C

* Deratings is done at 5.5mW/°C above Ta=25°C

○Operating Range (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit
Power supply voltage	Vcc	4.5	5.0	5.5	V

* This product is not designed for protection against radioactive rays.

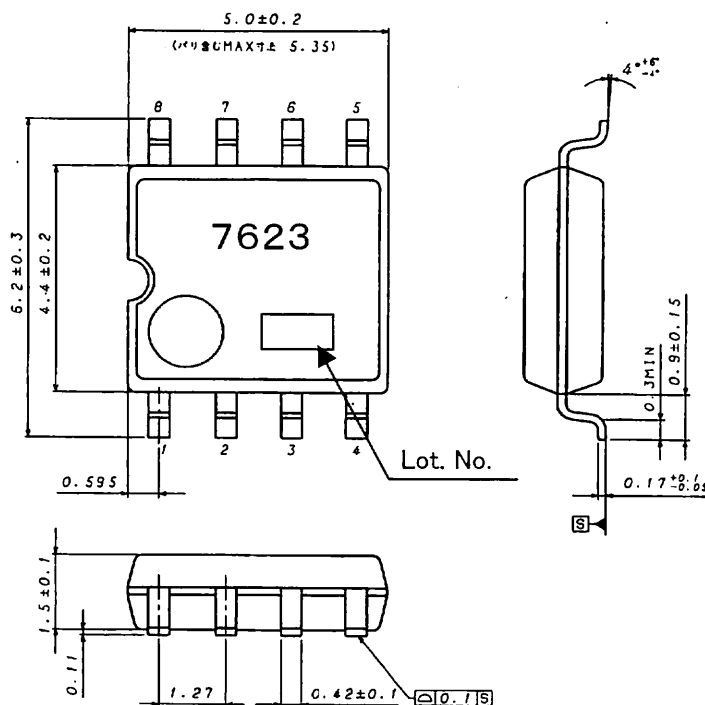
Application example

The product described in this specification is designed to be used with ordinary electronic equipment or devices (such as audio-visual equipment, office-automation equipment, communications devices, electrical appliances, and electronic toys). Should you intend to use this product with equipment or devices which require an extremely high level of reliability and the malfunction of which would directly endanger human life (such as medical instruments, transportation equipment, aerospace machinery, nuclear-reactor controllers, fuel controllers and other safety devices), please be sure to consult with our sales representative in advance.

○Electrical characteristics (Unless otherwise noted, $T_a = 25^\circ\text{C}$, $V_{CC} = 5.0\text{V}$, and load is two system drive)

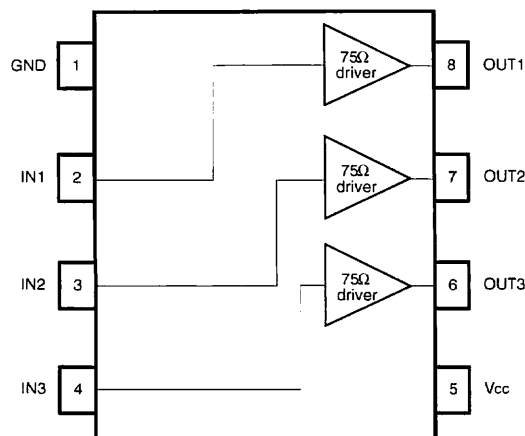
Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Circuit current	I_{CC}	-	25.2	37.8	mA	No signal
Maximum output level	V_{om}	2.9	3.4	-	Vp-p	$f = 1\text{kHz}$, $\text{THD} = 1.0\%$
Voltage gain	G_v	-1.0	-0.5	0	dB	$f = 1\text{kHz}$, $V_{IN} = 2.0\text{Vp-p}$
Frequency characteristic	G_f	-3	0	+1	dB	$10\text{MHz}/1\text{MHz}$, $V_{IN} = 1.0\text{Vp-p}$
Differential gain 75Ω drive1	DG1	-	0.4	1.0	%	$V_{IN} = 2.0\text{Vp-p}$, Standard staircase signal
Differential phase 75Ω drive1	DP1	-	0.4	1.0	deg	$V_{IN} = 2.0\text{Vp-p}$, Standard staircase signal
Differential gain 75Ω drive2	DG2	-	0.7	2.0	%	$V_{IN} = 2.0\text{Vp-p}$, Standard staircase signal
Differential phase 75Ω drive2	DP2	-	0.7	2.0	deg	$V_{IN} = 2.0\text{Vp-p}$, Standard staircase signal
Interchannel crosstalk	C_T	-	-60	-	dB	$f = 4.43\text{MHz}$, $V_{IN} = 2.0\text{Vp-p}$
Total harmonic distortion	T_{HD}	-	0.1	0.5	%	$f = 1\text{kHz}$, $V_{IN} = 1.0\text{Vp-p}$

○Outer dimensions



SOP8 (Unit: mm)

Block diagram



Pin number and pin name

Pin No.	Pin name
1	GND
2	IN1
3	IN2
4	IN3
5	VCC
6	OUT3
7	OUT2
8	OUT1

Cautions on use

1) Absolute maximum ratings

If applied voltage, operating temperature range, or other absolute maximum ratings are exceeded, the LSI may be damaged. Do not apply voltages or temperatures that exceed the absolute maximum ratings. If you think of a case in which absolute maximum ratings are exceeded, enforce fuses or other physical safety measures and investigate how not to apply the conditions under which absolute maximum ratings are exceeded to the LSI.

2) GND potential

Make the GND pin voltage such that it is the lowest voltage even when operating below it. Actually confirm that the voltage of each pin does not become a lower voltage than the GND pin, including transient phenomena.

3) Thermal design

Perform thermal design in which there are adequate margins by taking into account the allowable power dissipation in actual states of use.

4) Shorts between pins and miss-installation

When mounting the LSI on a board, pay adequate attention to orientation and placement discrepancies of the LSI. If it is miss-installed and the power is turned on, the LSI may be damaged. It also may be damaged if it is shorted by a foreign substance coming between pins of the LSI or between a pin and a power supply or a pin and a GND.

5) Operation in strong magnetic fields

Adequately evaluate use in a strong magnetic field, since there is a possibility of malfunction.

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U.S.A / San Diego	TEL : +1(858)625-3630	FAX : +1(858)625-3670
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Japan /
(Internal Sales)

Tokyo	2-1-1, Yaesu, Chuo-ku, Tokyo 104-0082	TEL : +81(3)5203-0321	FAX : +81(3)5203-0300
Yokohama	2-4-8, Shin Yokohama, Kohoku-ku, Yokohama, Kanagawa 222-8575	TEL : +81(45)476-2131	FAX : +81(45)476-2128
Nagoya	Dainagayo Building 9F 3-28-12, Meieki, Nakamura-ku, Nagoya, Aichi 450-0002	TEL : +81(52)581-8521	FAX : +81(52)561-2173
Kyoto	579-32 Higashi Shiokouji-cho, Karasuma Nishi-iru, Shiokoujidori, Shimogyo-ku, Kyoto 600-8216	TEL : +81(75)311-2121	FAX : +81(75)314-6559

(Contact address for overseas customers in Japan)

Yokohama	TEL : +81(45)476-9270	FAX : +81(045)476-9271
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