

BCR08AS-12

Triac

Low Power Use

REJ03G0292-0200 Rev.2.00 Mar 22, 2007

WWW.DZSG

Features

 $I_{T (RMS)} : 0.8 A$ V_{DRM} : 600 V

 I_{FGTI} , I_{RGTI} , I_{RGT} : 5 mA

 I_{FGT} : 10 mA

- Non-Insulated Type
- Planar Passivation Type
- Completed Pb Free

Outline

RENESAS Package code: PLZZ0004CA-A (Package name: UPAK)

RENESAS Package code: PLZZ0004CB-A (Package name: SOT-89)





- T₁ Terminal
- 2. T₂ Terminal
- 3. Gate Terminal
- 4. T₂ Terminal

Applications

Hybrid IC, solid state relay, electric fan, washing machine, and other general purpose control applications

Maximum Ratings

Tryond 1C, sond state relay, electric rail, washin	ig macinie, and other ge	eneral purpose control appi	ilcations				
Maximum Ratings							
Parameter	Symbol	Voltage class	Unit				
Farameter	Symbol	12 (Mark BF)	Onit				
Repetitive peak off-state voltage ^{Note1}	V _{DRM}	600	V				
Non-repetitive peak off-state voltage Note1	V _{DSM}	720	V				

Parameter	Symbol	Ratings	Unit	Conditions
RMS on-state current	I _{T (RMS)}	0.8	А	Commercial frequency, sine full wave 360° conduction, Ta = 40°C ^{Note3}
Surge on-state current	I _{TSM}	8	А	60Hz sinewave 1 full cycle, peak value, non-repetitive
I ² t for fusing	l ² t	0.26	A ² s	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current
Peak gate power dissipation	P _{GM}	1	W	
Average gate power dissipation	P _{G (AV)}	0.1	W	
Peak gate voltage	V _{GM}	10	V	
Peak gate current	I _{GM}	1	Α	
Junction temperature	Tj	- 40 to +125	°C	
Storage temperature	Tstg	- 40 to +125	°C	
Mass	_	50	mg	Typical value

Notes: 1. Gate open.



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Electrical Characteristics

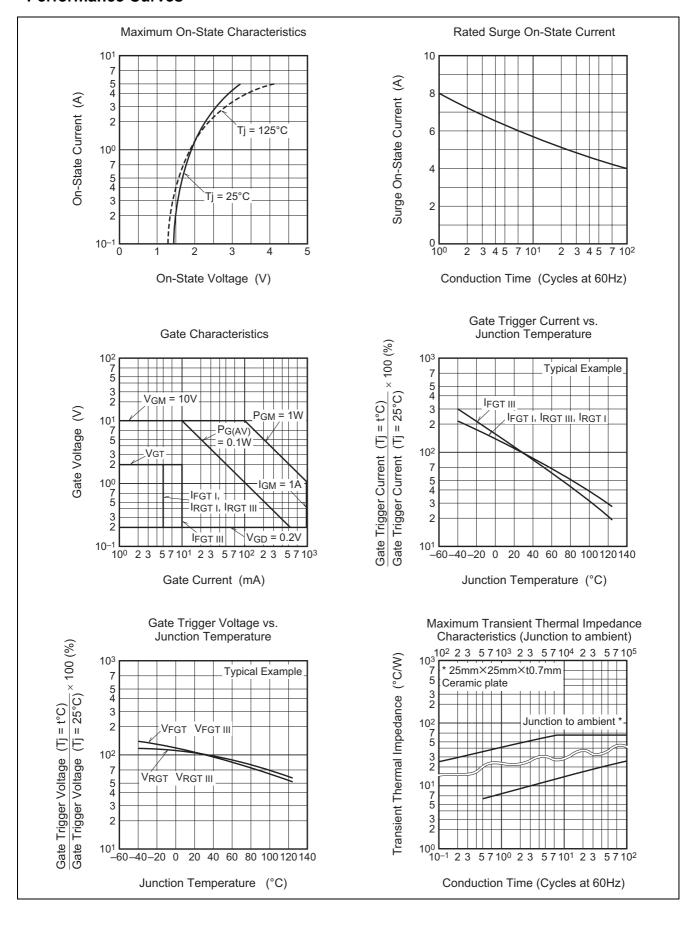
Parameter		Symbol	Min.	Тур.	Max.	Unit	Test conditions
Repetitive peak off-state current		I _{DRM}	_	_	2.0	mA	Tj = 125°C, V _{DRM} applied
On-state voltage		V_{TM}	_	_	2.0	V	Tc = 25°C, I _{TM} = 1.2 A, Instantaneous measurement
Gate trigger voltage ^{Note2}	I	$V_{FGT_{I}}$	_	_	2.0	V	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	V_{RGT_I}	_	_	2.0	V	$R_G = 330 \Omega$
	III	V_{RGTIII}	_	_	2.0	V	
	IV	V_{FGTIII}	_	_	2.0	V	
Gate trigger current ^{Note2}	I	I_{FGTI}	_	_	5	mA	$Tj = 25$ °C, $V_D = 6$ V, $R_L = 6$ Ω,
	II	I_{RGTI}		_	5	mA	$R_G = 330 \Omega$
	III	I _{RGTIII}	_	_	5	mA	
	IV	I_{FGTIII}	_	_	10	mA	
Gate non-trigger voltage		V_{GD}	0.1	_	_	V	$Tj = 125$ °C, $V_D = 1/2 V_{DRM}$
Thermal resistance		R _{th (j-a)}	_	_	65	°C/W	Junction to ambient ^{Note3}
Critical-rate of rise of off-state commutating voltage Note4		(dv/dt)c	0.5	_	_	V/μs	Tj = 125°C

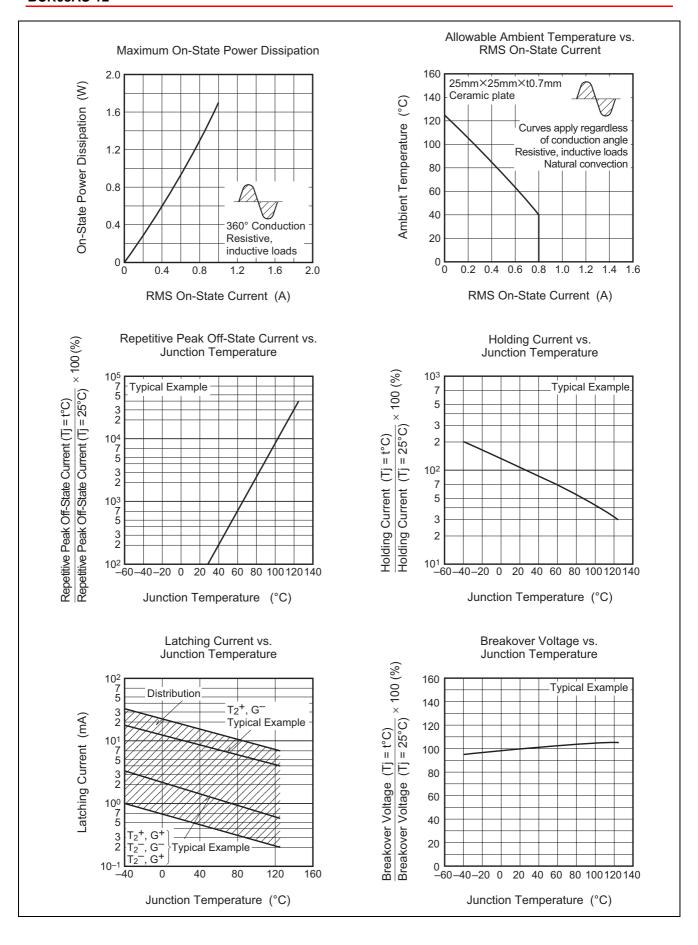
Notes: 2. Measurement using the gate trigger characteristics measurement circuit.

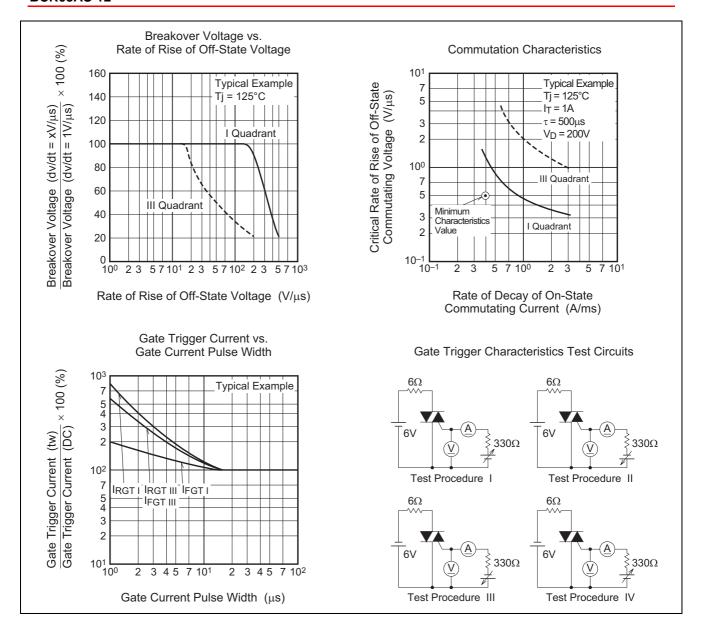
- 3. Soldering with ceramic plate (25 mm \times 25 mm \times t0.7 mm).
- 4. Test conditions of the critical-rate of rise of off-state commutating voltage is shown in the table below.

Test conditions	Commutating voltage and current waveforms (inductive load)
1. Junction temperature Tj = 125°C	Supply Voltage →Time
2. Rate of decay of on-state commutating current (di/dt)c = - 0.4 A/ms	Main Current → (di/dt)c → Time
3. Peak off-state voltage V _D = 400 V	Main Voltage Time (dv/dt)c

Performance Curves

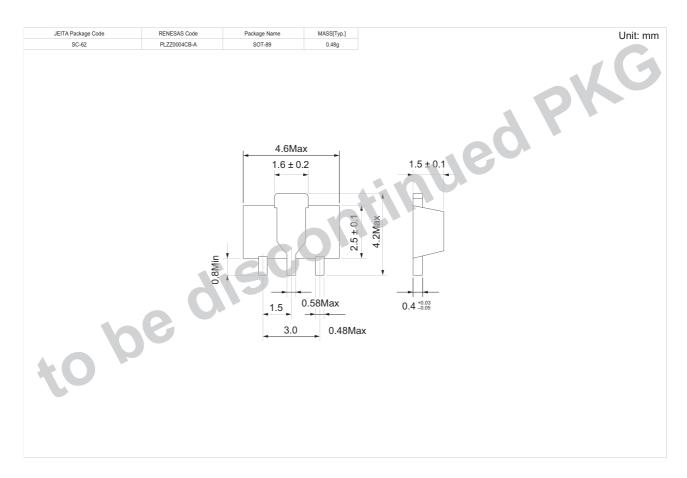






Package Dimensions

Package Name UPAK	JEITA Package Code SC-62	RENESAS Code PLZZ0004CA-A	Previous Code UPAK / UPAKV	MASS[Typ.] 0.050g		Unit: mm
	0.53 Max 0.48 Max	4.5 ± 0.1 1.8 Max 4.5 ± 0.1 1.8 Max 4.5 ± 0.1 1.5 1.5	1.5 ± 0.1 0.44 N	(2.5)	(1.5)	



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Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Surface-mounted type	Taping	4000	Type name +A -T +Direction (1 or 2)+4	BCR08AS-12A-T14

Note: Please confirm the specification about the shipping in detail.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

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