


MITSUBISHI SEMICONDUCTOR (TRIAC)

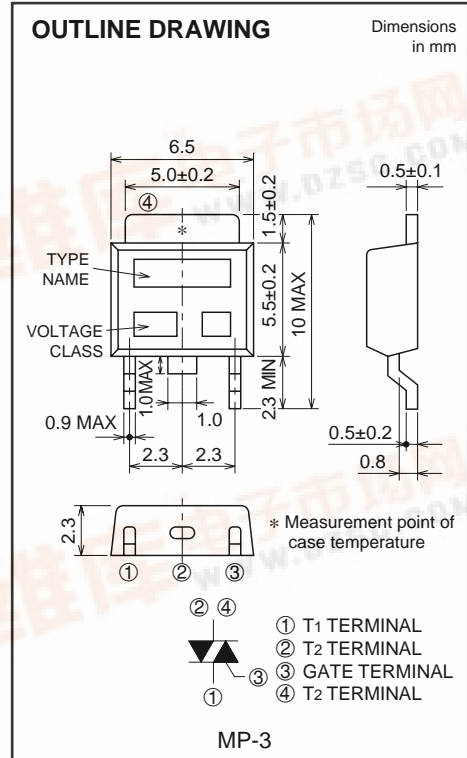
# BCR5AS

MEDIUM POWER USE  
NON-INSULATED TYPE, PLANAR PASSIVATION TYPE

**BCR5AS**



- $I_T$  (RMS) ..... 5A
- $V_{DRM}$  ..... 400V/600V
- $I_{FGT I}$ ,  $I_{RGT I}$ ,  $I_{RGT III}$  ..... 30mA



## APPLICATION

Hybrid IC, solid state relay, switching mode power supply, light dimmer, electric fan, electric blankets, control of household equipment such as washing machine, other general purpose control applications

## MAXIMUM RATINGS

Symbol	Parameter	Voltage class		Unit
		8	12	
$V_{DRM}$	Repetitive peak off-state voltage*1	400	600	V
$V_{DSM}$	Non-repetitive peak off-state voltage*1	500	720	V

Symbol	Parameter	Conditions	Ratings	Unit
$I_T$ (RMS)	RMS on-state current	Commercial frequency, sine full wave 360° conduction, $T_c=103^\circ\text{C}$	5	A
$I_{TSM}$	Surge on-state current	60Hz sinewave 1 full cycle, peak value, non-repetitive	50	A
$I_t^2$	$I_t^2$ for fusing	Value corresponding to 1 cycle of half wave 60Hz, surge on-state current	10.4	$\text{A}^2\text{s}$
PGM	Peak gate power dissipation		3	W
PG (AV)	Average gate power dissipation		0.3	W
VGM	Peak gate voltage		10	V
IGM	Peak gate current		2	A
$T_j$	Junction temperature		-40 ~ +125	$^\circ\text{C}$
$T_{stg}$	Storage temperature		-40 ~ +125	$^\circ\text{C}$
—	Weight	Typical value	0.26	g

\*1. Gate open.



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## ELECTRICAL CHARACTERISTICS

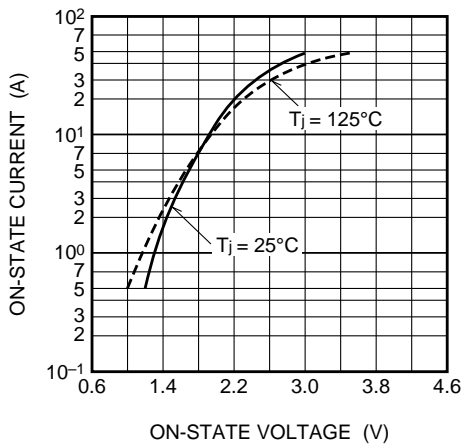
Symbol	Parameter	Test conditions	Limits			Unit	
			Min.	Typ.	Max.		
IDRM	Repetitive peak off-state current	$T_j=125^\circ\text{C}$ , $V_{\text{DRM}}$ applied	—	—	2.0	mA	
VTM	On-state voltage	$T_c=25^\circ\text{C}$ , $I_{\text{TM}}=7\text{A}$ , Instantaneous measurement	—	—	1.8	V	
VFGT I	Gate trigger voltage *2	$T_j=25^\circ\text{C}$ , $V_D=6\text{V}$ , $R_L=6\Omega$ , $R_G=330\Omega$	I	—	—	1.5	V
VRGT I			II	—	—	1.5	V
VRGT III			III	—	—	1.5	V
IFGT I	Gate trigger current *2	$T_j=25^\circ\text{C}$ , $V_D=6\text{V}$ , $R_L=6\Omega$ , $R_G=330\Omega$	I	—	—	30	mA
IRGT I			II	—	—	30	mA
IRGT III			III	—	—	30	mA
VGD	Gate non-trigger voltage	$T_j=125^\circ\text{C}$ , $V_D=1/2V_{\text{DRM}}$	0.2	—	—	V	
Rth (j-c)	Thermal resistance	Junction to case *4	—	—	3	$^\circ\text{C/W}$	
(dv/dt) <sub>c</sub>	Critical-rate of rise of off-state commutating voltage		*3	—	—	V/ $\mu\text{s}$	

\*2. Measurement using the gate trigger characteristics measurement circuit.  
 \*3. The critical-rate of rise of the off-state commutating voltage is shown in the table below.  
 \*4. Case temperature is measured on the T2 terminal.

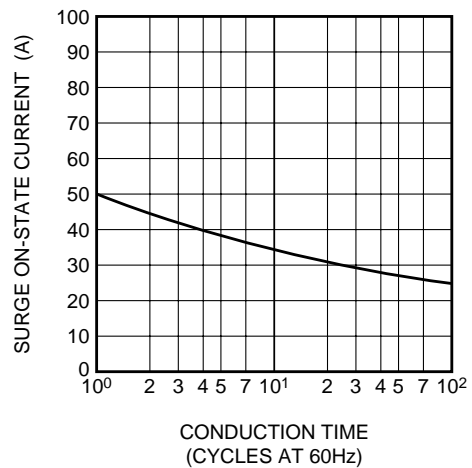
Voltage class	$V_{\text{DRM}}$ (V)	$(dv/dt)_c$		Test conditions	Commutating voltage and current waveforms (inductive load)
		Min.	Unit		
8	400	5	V/ $\mu\text{s}$	1. Junction temperature $T_j=125^\circ\text{C}$ 2. Rate of decay of on-state commutating current $(di/dt)_c=-2.5\text{A/ms}$ 3. Peak off-state voltage $V_D=400\text{V}$	
12	600				

## PERFORMANCE CURVES

MAXIMUM ON-STATE CHARACTERISTICS



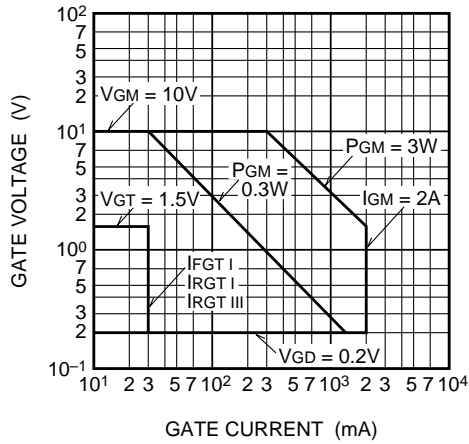
RATED SURGE ON-STATE CURRENT



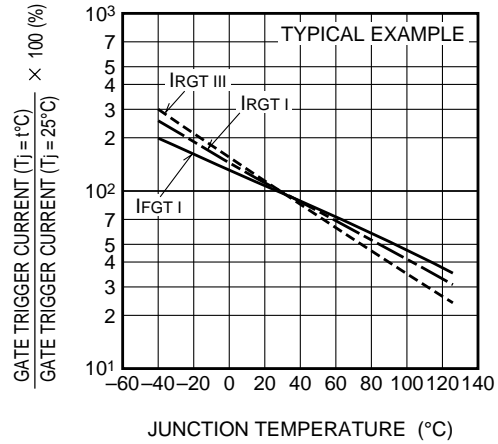
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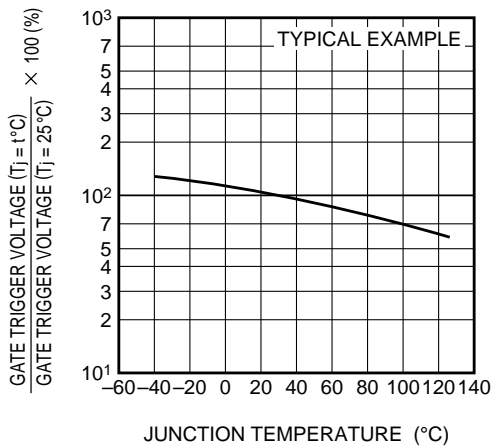
**GATE CHARACTERISTICS**



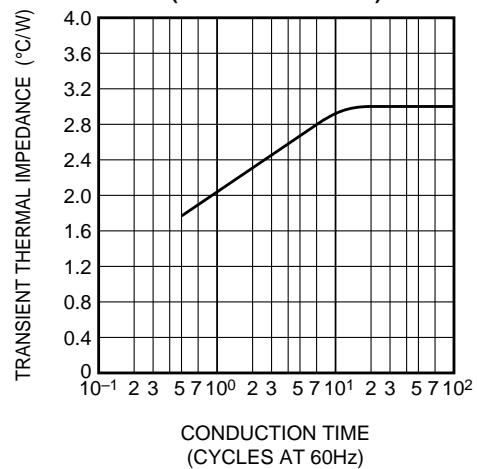
**GATE TRIGGER CURRENT VS. JUNCTION TEMPERATURE**



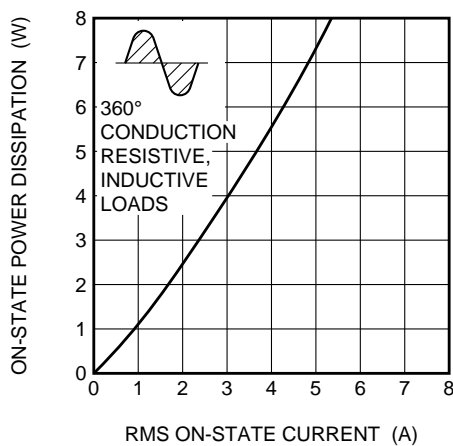
**GATE TRIGGER VOLTAGE VS. JUNCTION TEMPERATURE**



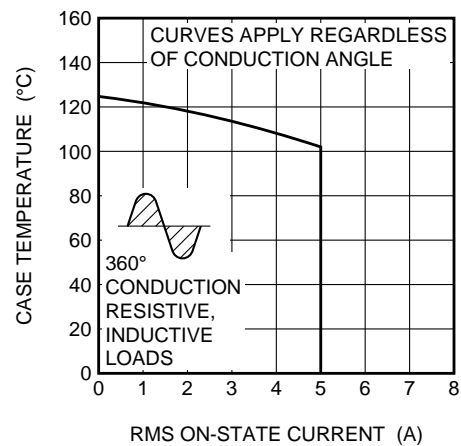
**MAXIMUM TRANSIENT THERMAL IMPEDANCE CHARACTERISTICS (JUNCTION TO CASE)**



**MAXIMUM ON-STATE POWER DISSIPATION**



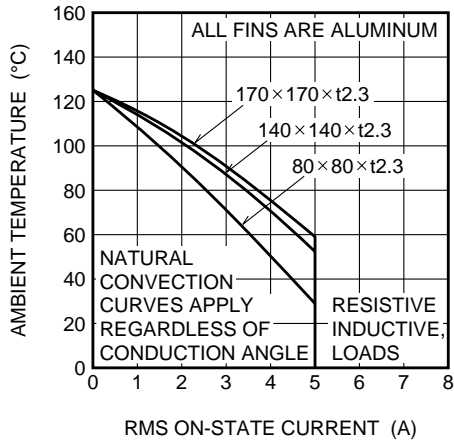
**ALLOWABLE CASE TEMPERATURE VS. RMS ON-STATE CURRENT**



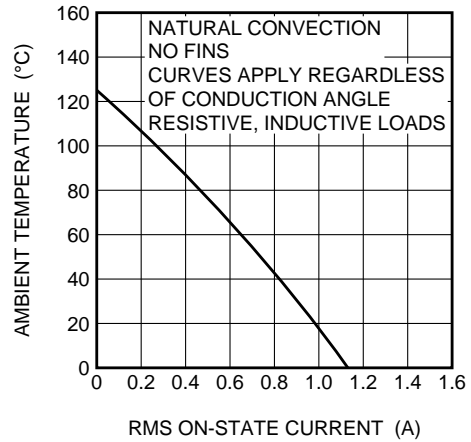
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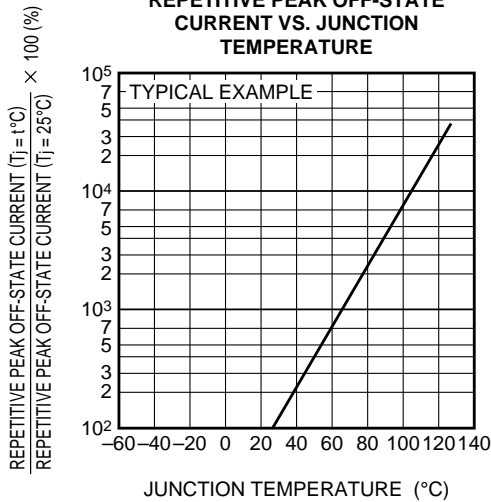
**ALLOWABLE AMBIENT TEMPERATURE VS. RMS ON-STATE CURRENT**



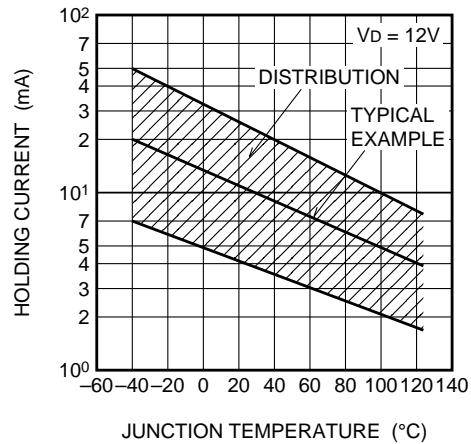
**ALLOWABLE AMBIENT TEMPERATURE VS. RMS ON-STATE CURRENT**



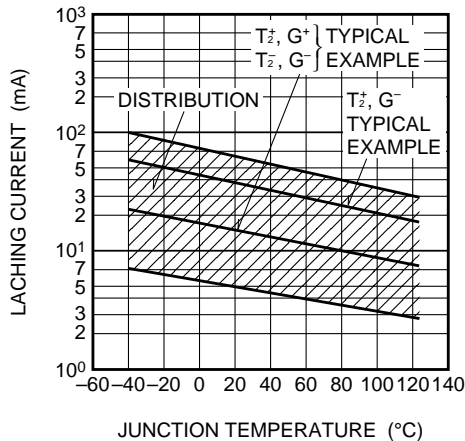
**REPETITIVE PEAK OFF-STATE CURRENT VS. JUNCTION TEMPERATURE**



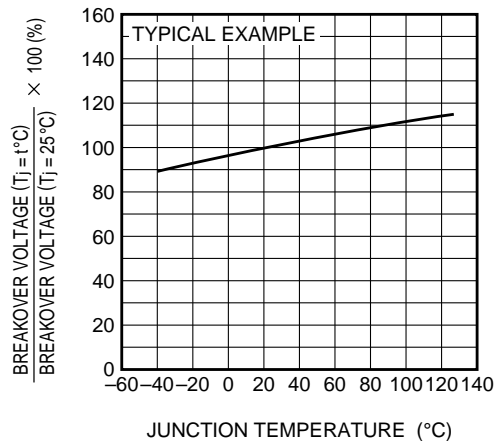
**HOLDING CURRENT VS. JUNCTION TEMPERATURE**



**LATCHING CURRENT VS. JUNCTION TEMPERATURE**

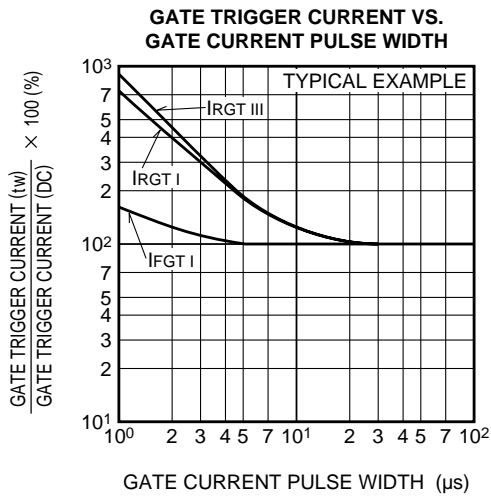
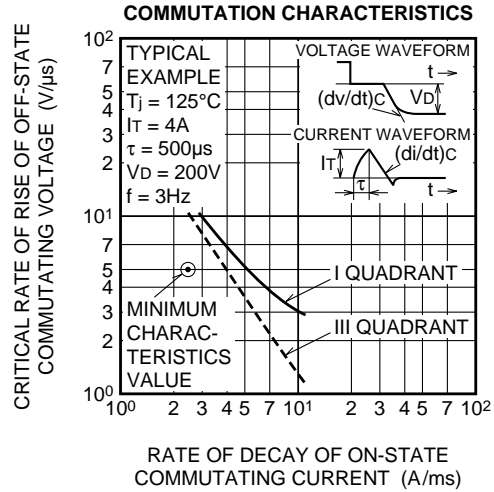
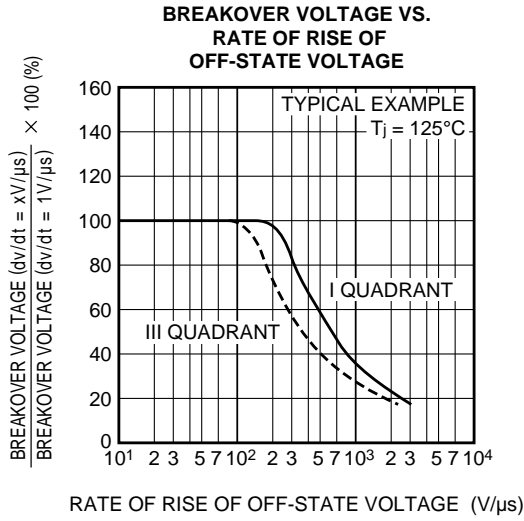


**BREAKOVER VOLTAGE VS. JUNCTION TEMPERATURE**



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**GATE TRIGGER CHARACTERISTICS TEST CIRCUITS**

