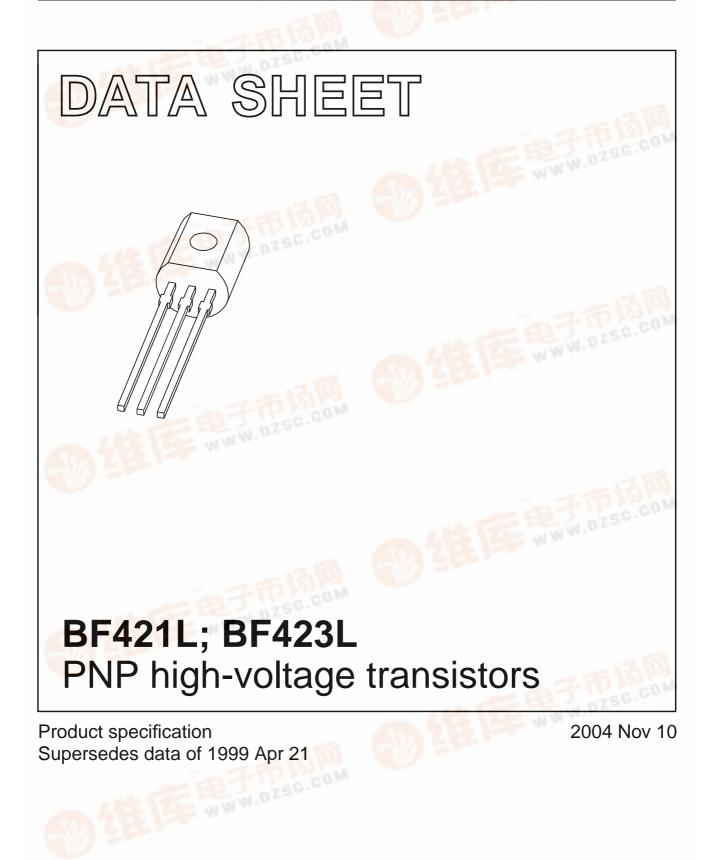
DISCRETE SEMICONDUCTORS









PNP high-voltage transistors

BF421L; BF423L

FEATURES

- Low current (max. 50 mA)
- High voltage (max. 300 V)
- Available with a higher power rating (830 mW) under type number: BF423.

APPLICATIONS

• Primarily intended for telephony applications.

DESCRIPTION

PNP transistor in a TO-92; SOT54 plastic package. NPN complement: BF422L.

PINNING

| PIN | DESCRIPTION | |
|-----|-------------|--|
| 1 | base | |
| 2 | collector | |
| 3 | emitter | |

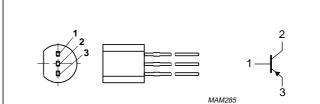


Fig.1 Simplified outline (TO-92; SOT54) and symbol.

ORDERING INFORMATION

| TYPE NUMBER | | PACKAGE | | | |
|-------------|--------|---|---------|--|--|
| ITFE NUMBER | NAME | DESCRIPTION | VERSION | | |
| BF421L | SC-43A | plastic single-ended leaded (through hole) package; 3 leads | SOT54 | | |
| BF423L | | | | | |

LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|------------------|---------------------------|------------------------------|------|------|------|
| V _{CBO} | collector-base voltage | open emitter | | | |
| | BF421L | | _ | -300 | V |
| | BF423L | | _ | -250 | V |
| V _{CEO} | collector-emitter voltage | open base | | | |
| | BF421L | | _ | -300 | V |
| | BF423L | | _ | -250 | V |
| V _{EBO} | emitter-base voltage | open collector | _ | -5 | V |
| I _C | collector current (DC) | | _ | -50 | mA |
| I _{CM} | peak collector current | | - | -100 | mA |
| I _{BM} | peak base current | | _ | -100 | mA |
| P _{tot} | total power dissipation | $T_{amb} \le 25 \ ^{\circ}C$ | - | 625 | mW |
| T _{stg} | storage temperature | | -65 | +150 | °C |
| Tj | junction temperature | | _ | 150 | °C |
| T _{amb} | ambient temperature | | -65 | +150 | °C |

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THERMAL CHARACTERISTICS

| SYMBOL | PARAMETER | CONDITIONS | VALUE | UNIT |
|----------------------|---|------------|-------|------|
| R _{th(j-a)} | thermal resistance from junction to ambient | note 1 | 200 | K/W |

Note

1. Transistor mounted on an FR4 printed-circuit board.

CHARACTERISTICS

 T_{amb} = 25 $^\circ C$ unless otherwise specified.

| SYMBOL | PARAMETER | CONDITIONS | MIN. | MAX. | UNIT |
|--------------------|--------------------------------------|--|------|------|------|
| I _{CBO} | collector-base cut-off current | $V_{CB} = -200 \text{ V}; I_E = 0 \text{ A}$ | _ | -10 | nA |
| | | $V_{CB} = -200 \text{ V}; \text{ I}_{E} = 0 \text{ A}; \text{ T}_{j} = 150 ^{\circ}\text{C}$ | _ | -10 | μA |
| I _{EBO} | emitter-base cut-off current | $V_{EB} = -5 \text{ V}; \text{ I}_{C} = 0 \text{ A}$ | - | -10 | μA |
| h _{FE} | DC current gain | $V_{CE} = -20 \text{ V}; \text{ I}_{C} = -25 \text{ mA}$ | 50 | - | |
| V _{CEsat} | collector-emitter saturation voltage | $I_{C} = -30 \text{ mA}; I_{B} = -5 \text{ mA}; \text{ note } 1$ | _ | -600 | mV |
| C _{re} | feedback capacitance | $V_{CE} = -30 \text{ V}; I_C = i_c = 0 \text{ A}; f = 1 \text{ MHz}$ | - | 1.6 | pF |
| f _T | transition frequency | $V_{CE} = -10 \text{ V}; I_C = -10 \text{ mA}; f = 100 \text{ MHz}$ | 60 | - | MHz |

Note

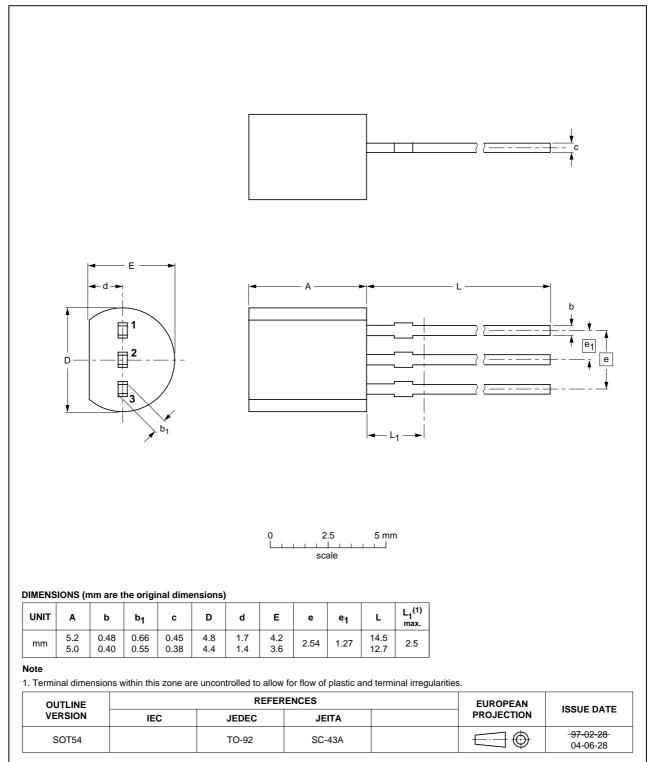
1. Pulse test: $t_p \le 300 \ \mu s$; $\delta \le 0.02$.

BF421L; BF423L

PNP high-voltage transistors

PACKAGE OUTLINE

Plastic single-ended leaded (through hole) package; 3 leads



SOT54

PNP high-voltage transistors

BF421L; BF423L

DATA SHEET STATUS

| LEVEL | DATA SHEET STATUS ⁽¹⁾ | PRODUCT STATUS ⁽²⁾⁽³⁾ | DEFINITION |
|-------|-------------------------------------|-------------------------------------|--|
| 1 | Objective data | Development | This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice. |
| 11 | Preliminary data | Qualification | This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product. |
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- 2. The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL http://www.semiconductors.philips.com.
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Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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