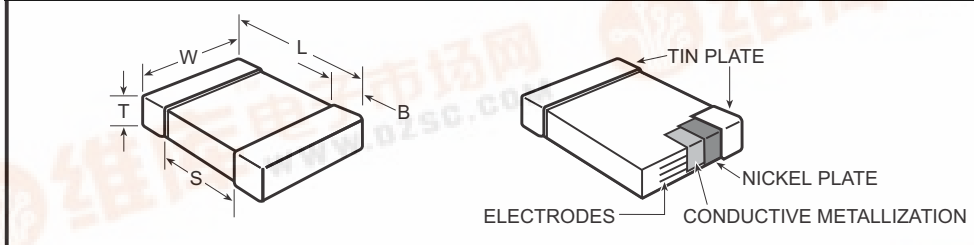




Product Bulletin

Surface Mount Ceramic Chip Capacitors – C0201 Series - X5R Dielectric

Outline Drawing



Dimensions – Millimeters (Inches)

EIA Size Code	Metric Size Code	L Length	W Width	B Bandwidth	S Separation
0201	0603	0.6 (.024) ± 0.03 (.001)	0.3 (.012) ± 0.03 (.001)	0.15 (.006) ± 0.05 (.002)	-

See Capacitance Value Table below for thickness dimension.

Capacitance Value

Capacitance Value (uF)	KEMET Part Number	Voltage	Capacitance Tolerance	Thickness	Qty 7" Reel
0.01	C0201C103K4PAC	16	K,M	0.3 (.012) ± 0.03 (.001)	15,000
0.10	C0201C104K9PAC	6.3	K,M	0.3 (.012) ± 0.03 (.001)	15,000

Capacitor Ordering Information

Style	Size Code	Specification	Capacitance Code, pF	Capacitance Tolerance	End Metallization	Failure Rate Level	Temperature Characteristic	Voltage
C - Ceramic	0201	C - Standard	104	K	C = Standard (Tin-plate nickel barrier)	A = Not Applicable	P = X5R (±15%) (-55°C +85°C)	9 = 6.3V 4 = 16V

RoHS Compliant



Electrical Parameters

As detailed in the KEMET Surface Mount Catalog F3102 for X5R, with following specific requirements based on room temperature (25°C) parameters:

- Operating Range: -55°C to +85°C, with no-bias capacitance shift limited to $\pm 15\%$ over that range.
- Insulation Resistance (IR) measured after 2 minutes at rated voltage @ 25°C: Limit is 500 megohm microfarads or 10000M Ω , whichever of the two is smaller.
- Capacitance and Dissipation Factor (DF) measured at 1 kHz and 1 Vrms. DF Limit is 10% max.**

** Please refer to individual specifications.

Soldering Process

These components are suitable for reflow only. All parts incorporate the standard KEMET barrier layer of pure nickel, with an overplate of pure tin to provide excellent solderability as well as resistance to leaching.

Marking

These chips will be supplied unmarked.

In general, the information in the KEMET Surface Mount catalog F3102 applies to these capacitors. The information in this bulletin supplements that in the catalog.