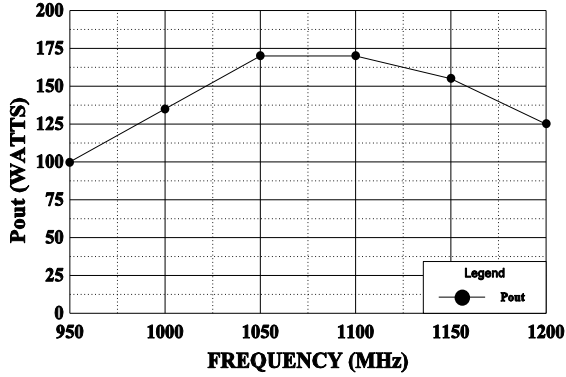




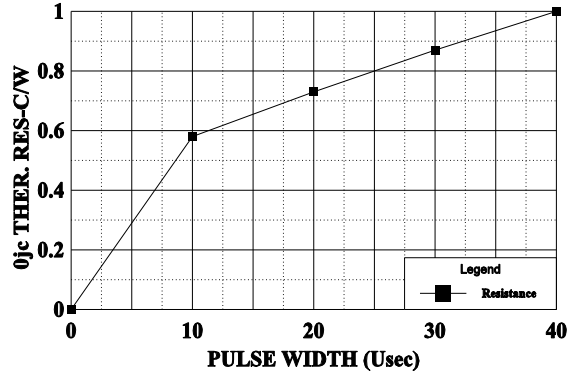
**POWER OUTPUT**

Vcc = 50 V, Pin = 25 W



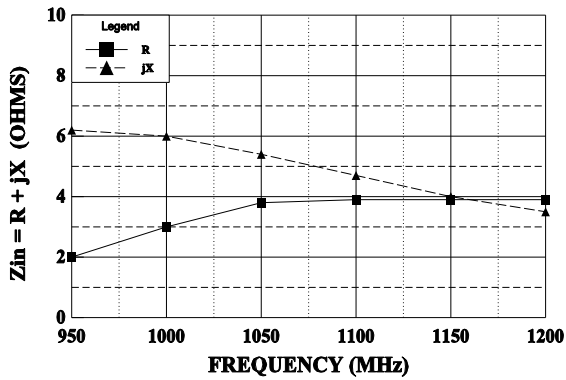
**THERMAL RESISTANCE vs PULSE WIDTH**

Vcc=50V, DF=1%, Tf=30C



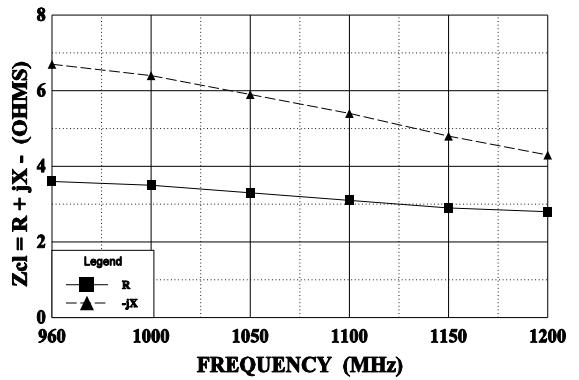
**SERIES INPUT IMPEDANCE vs FREQUENCY**

Vcc = 50 V, Po = 150 W



**SERIES LOAD IMPEDANCE vs FREQUENCY**

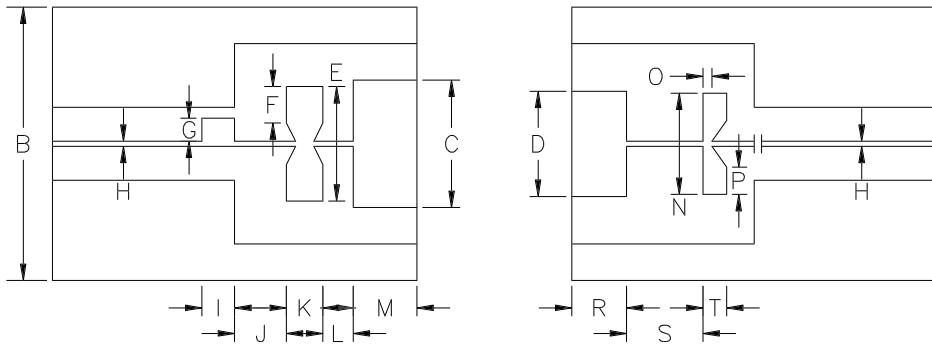
Vcc = 50 V, Po = 150 W



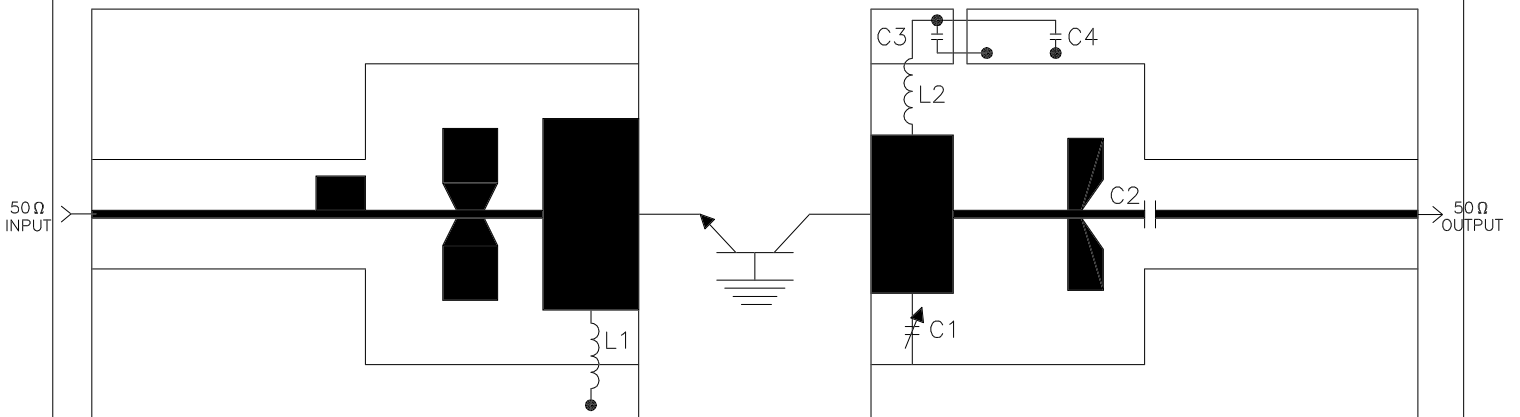
REVISIONS

ZONE	REV	DESCRIPTION	DATE	APPROVED
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DIM	INCHES
A	2.000
B	1.500
C	.700
D	.550
E	.628
F	.200
G	.140
H	.028
I	.180
J	.284
K	.200
L	.166
M	.350
N	.528
O	.050
P	.150
Q	.028
R	.300
S	.419
T	.130



1025/1150 MHz TEST AMPLIFIER



Material=.010" Duroid Er=2.3

- L1= 1" No. 20 wire
- L2= 7 turns No. 20 wire, closewound 1/8" dia.
- C1= Johanson No. 5701, 6-6PF
- C2= A.T.C. Chip cap. 82PF
- C3= A.T.C. Chip cap. 92PF
- C4= 200 MFD @ 50V