



DMP3030SN

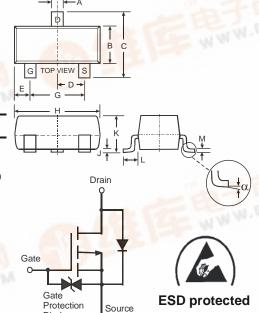
P-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Low Gate Threshold Voltage
- Low Input Capacitance
- Fast Switching Speed
- Lead Free By Design/RoHS Compliant (Note 2)
- **ESD Protected Gate**
- "Green" Device (Note 4)
- Qualified to AEC-Q101 standards for High Reliability

Mechanical Data

- Case: SC-59
- Case Material: Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 4
- Ordering & Date Code Information: See Page 4
- Weight: 0.008 grams (approximate)



SC-59									
Dim	Min	Max							
Α	0.30	0.50							
В	1.40	1.80							
С	2.50	3.00							
D	0.85	1.05							
Е	0.30	0.70							
G	1.70	2.10							
Н	2.70	3.10							
Jo	COM	0.10							
K	1.00	1.40							
L	0.55	0.70							
М	0.10	0.35							
α	0°	8°							
All Dimensions in mm									

EQUIVALENT CIRCUIT

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Unit	
Drain-Source Voltage	V _{DSS}	-30	V	
Gate-Source Voltage	V _{GSS}	±20	V	
Drain Current (Note 1) Steady State	ID	-0.7	А	
Pulsed Drain Current (Note 3)	I _{DM}	-2.8	Α	
Total Power Dissipation (Note 1)	P _d	500	mW	
Thermal Resistance, Junction to Ambient	$R_{ heta JA}$	250	°C/W	
Operating and Storage Temperature Range	T _i , T _{STG}	-65 to +150	°C	

Diode

Notes:

- Device mounted on FR-4 PCB.
- No purposefully added lead.
- Pulse width ${\leq}10\mu S,$ Duty Cycle ${\leq}1\%.$
- WWW.DZSC.COM Diodes Inc.'s "Green" policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

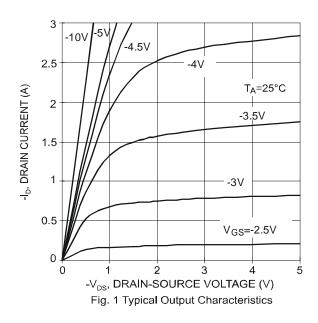


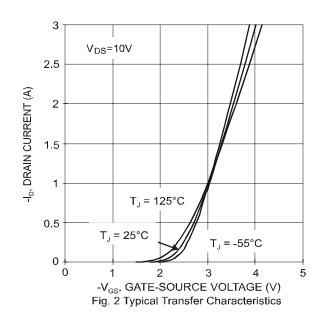


Electrical Characteristics @T_A = 25°C unless otherwise specified

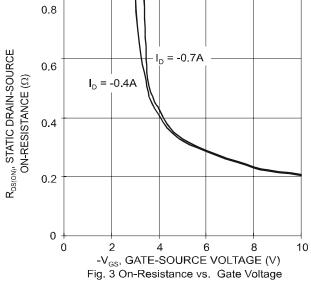
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition			
OFF CHARACTERISTICS (Note 5)									
Drain-Source Breakdown Voltage	BV _{DSS}	-30	_	_	V	$V_{GS} = 0V, I_D = -250mA$			
Zero Gate Voltage Drain Current	I _{DSS}	_	_	-10	μΑ	V _{DS} = -30V, V _{GS} = 0V			
Gate-Body Leakage	I _{GSS}	_	_	±10	μА	$V_{GS} = \pm 20V, V_{DS} = 0V$			
ON CHARACTERISTICS (Note 5)	•								
Gate Threshold Voltage	V _{GS(th)}	-1.0	_	-3.0	V	V _{DS} = -10V, I _D = -1.0mA			
Static Drain-Source On-Resistance	R _{DS (ON)}	_	0.20 0.35	0.25 0.45	Ω	V _{GS} = -10V, I _D = -0.4A V _{GS} = -4.5V, I _D = -0.4A			
Forward Transfer Admittance	Y _{fs}	_	1	_	S	V _{DS} = -10V, I _D = 0.4A			
Diode Forward Voltage (Note 5)	V _{SD}	_	-0.8	-1.1	V	V _{GS} = 0V, I _S = -0.7A			
DYNAMIC CHARACTERISTICS									
Input Capacitance	C _{iss}	_	160	_	pF				
Output Capacitance	C _{oss}	_	120	_	pF	$V_{DS} = -10V, V_{GS} = 0V$ f = 1.0MHz			
Reverse Transfer Capacitance	C _{rss}	_	50	_	pF	1 - 1.000112			
SWITCHING CHARACTERISTICS	•								
Turn-On Delay Time	t _{D(ON)}	_	10		ns				
Turn-Off Delay Time	t _{D(OFF)}	_	25		ns	V _{DD} = -10V, I _D = -0.4A,			
Turn-On Rise Time	t _r	_	25		ns	$V_{GS} = -5.0V$, $R_{GEN} = 50\Omega$			
Turn-Off Fall Time	t _f	_	40	_	ns				

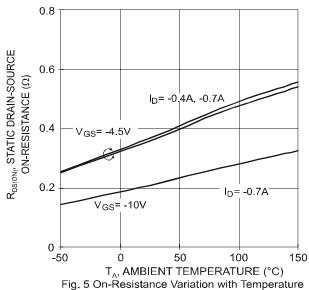
Notes: 5. Short duration test pulse used to minimize self-heating effect.

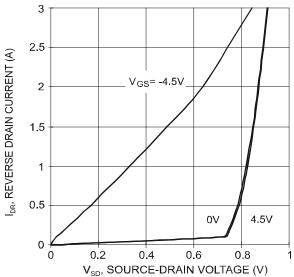


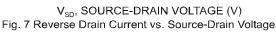


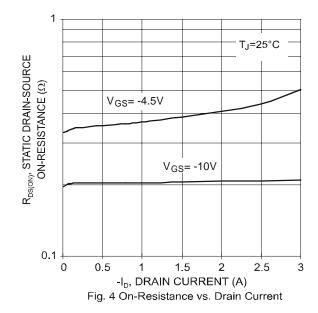


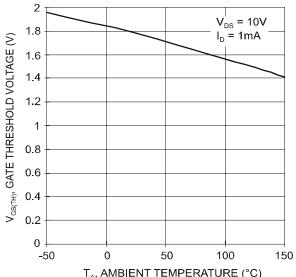




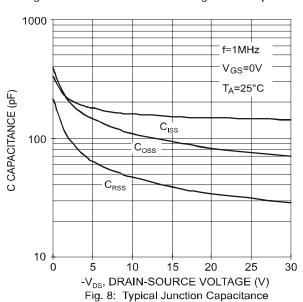








 $\rm T_A$, AMBIENT TEMPERATURE (°C) Fig. 6 Gate-Source Threshold Voltage with Temperature



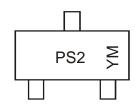


Ordering Information (Note 6)

Device	Packaging	Shipping			
DMP3030SN-7	SC-59	3000/Tape & Reel			

Notes: 6. For Packaging Details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information



PS2 = Product Type Marking Code YM = Date Code Marking Y = Year ex: T = 2006

M = Month ex: 9 = September

Date Code Key

Year	2006 200		2007		2008		2009			2011	2012	
Code	Т		U		V W		V	Х		Υ	Z	
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

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