DTD513ZE / DTD513ZM

Transistors

500mA / 12V Low VCE (sat) Digital transistors (with built-in resistors) DTD513ZE / DTD513ZM

Applications

Inverter, Interface, Driver

Feature

- 1) VCE (sat) is lower than conventional products.
- 2) Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors (see equivalent circuit).
- 3) The bias resistors consist of thin-film resistors with complete isolation to allow negative biasing of the input. They also have the advantage of almost completely eliminating parasitic effects.
- 4) Only the on / off conditions need to be set for operation, making the device design easy. SC.COM

Structure

NPN epitaxial plannar silicon transistor (Resistor built-in type)

Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit
Parameter	Symbol	DTD513ZE DTD513ZM	
Supply voltage	Vcc	12	V
Input voltage	Vin	-5 to +10	V
Collector current *1	IC (max)	500	mA
Power dissipation *2	PD	150	mW
Junction temperature	Tj	150	ΰ
Storage temperature	Tstg	-55 to +150	ĉ

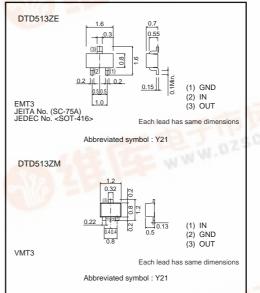
*1 Characteristics of built-in transistor.
*2 Each terminal mounted on a recommended land.

Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
land on the set	VI(off)	-	-	0.3	V	Vcc=5V, Io=100µA	
Input voltage	VI(on)	2.5	-	-		Vo=0.3V, Io=20mA	
Output voltage	VO(on)	-	60	300	mV	lo/l=100mA / 5mA	
Input current	h		-	6.4	mA	Vi= 5V	
Output current	IO(off)	100	1.2	0.5	μA	Vcc=12V, Vi=0V	
DC current gain	Gi	140	-	-	-	Vo=2V, Io=100mA	
Transition frequency *	fτ	-	260	-	MHz	Vce=10V, Ie= -5mA, f=100MHz	
Input resistance	R1	0.7	1.0	1.3	kΩ	-	
Resistance ratio	R2/R1	8.0	10	12	_	_	



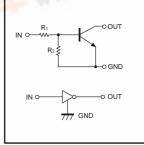
External dimensions (Unit : mm)



Packaging specifications

1.0	Package	EMT3	VMT3
	Packaging type	Taping	Taping
	Code	TL	T2L
Part No.	Basic ordering unit (pieces)	3000	8000
DTD513ZE		0	-
DTD513ZM		-	0

Equivalent circuit



R1=1.0kΩ / R2=10kΩ

Appendix

Notes

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