



Single line IPAD™, EMI filter including ESD protection

- High density capacitor
- 1 line low-pass-filter
- Lead-free package
- High efficiency in EMI filtering
- Very low PCB space consumption
- Very thin package: 0.65 mm
- High efficiency in ESD suppression (IEC 61000-4-2 level 4)
- High reliability offered by monolithic integration
- High density capacitor technology

- IEC 61000-4-2 level 4, on output pins
 - 15 kV (air discharge)
 - 8 kV (contact discharge)
- IEC 61000-4-2 Level 1, on input pins
 - 2 kV (air and contact discharge)

- Single ended microphone in mobile phones and portable devices

The EMIF01-SMIC01F2 is a highly integrated device designed to suppress EMI/RFI noise for microphone line filtering.

Additionally, this filter includes ESD protection circuitry which prevents damage to the application when subjected to ESD surges up to 15 kV.

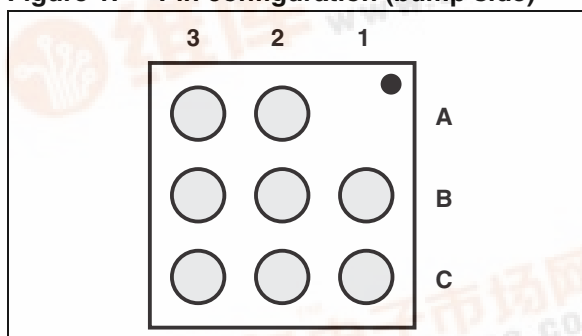
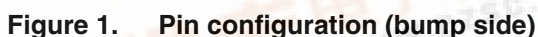
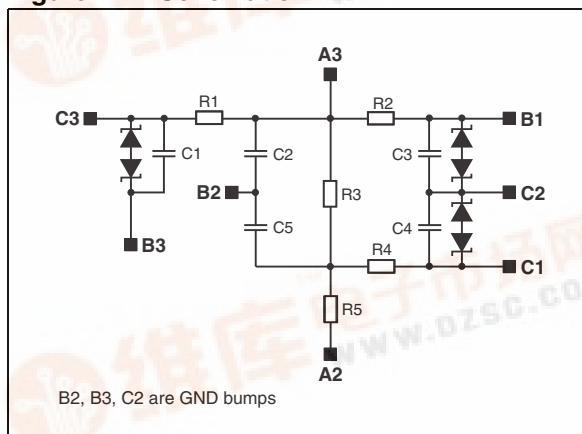


Figure 2. Schematic



TM: IPAD is a trademark of STMicroelectronics



1 Characteristics

Table 1. Absolute maximum ratings ($T_{amb} = 25\text{ °C}$)

Symbol	Parameter and test conditions	Value	Unit
V_{PP}	Output lines (C3)		
	ESD discharge IEC61000-4-2, air discharge	15	
	ESD discharge IEC61000-4-2, contact discharge	8	
	Input lines (A3, A2, B1, C1)		
	ESD discharge IEC61000-4-2, air discharge	2	
	ESD discharge IEC61000-4-2, contact discharge	2	
T_j	Maximum junction temperature	125	°C
T_{op}	Operating temperature range	- 40 to + 85	°C
T_{stg}	Storage temperature range	- 55 to + 150	°C

Table 2. Electrical characteristics ($T_{amb} = 25\text{ °C}$)

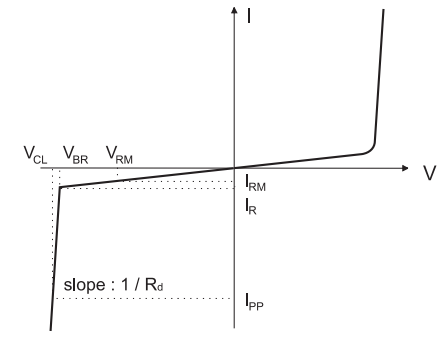
Symbol	Parameter				
V _{BR}	Breakdown voltage				
I _{RM}	Leakage current @ V _{RM}				
V _{RM}	Stand-off voltage				
V _{CL}	Clamping voltage				
R _d	Dynamic impedance				
I _{PP}	Peak pulse current				
Symbol	Test conditions	Min.	Typ.	Max.	Unit
V _{BR}	I _R = 1 mA	14			V
I _{RM}	V _{RM} = 3 V per line			0.5	μA
C ₁ , C ₂ , C ₃ , C ₄ ,	V _{LINE} = 0 V, V _{OSC} = 30 mV, F = 1 MHz, Tolerance ± 20%		1		nF
C ₅	V _{LINE} = 0 V, V _{OSC} = 30 mV, F = 1 MHz, Tolerance ± 20%		150		pF
R ₁ , R ₅	Tolerance ± 5%		50		Ω
R ₂ , R ₃ , R ₄	Tolerance ± 5%		2.2		kΩ

Figure 3. Filtering measurements

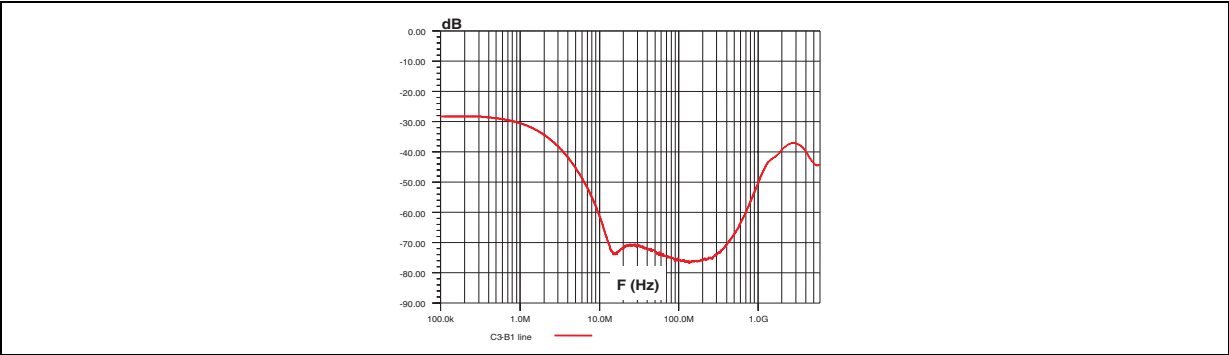


Figure 4. ESD response to IEC 61000-4-2 (+15 kV air discharge) on one output line

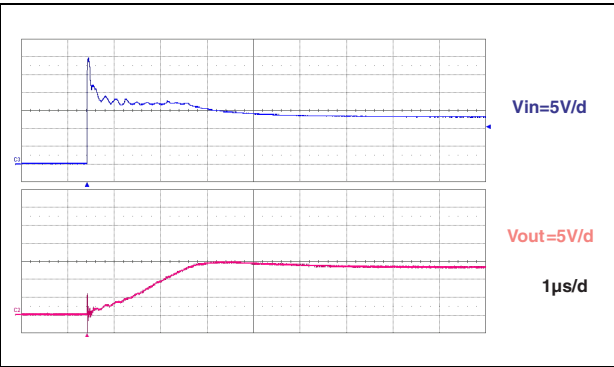
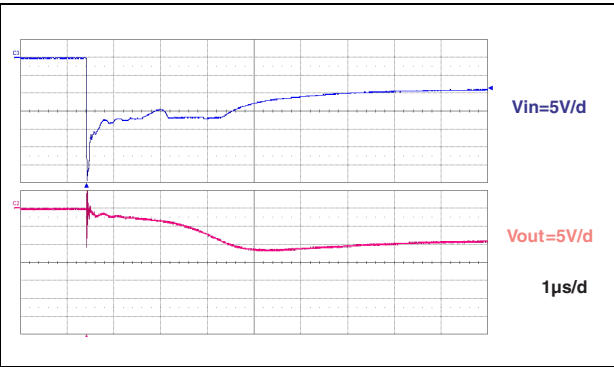
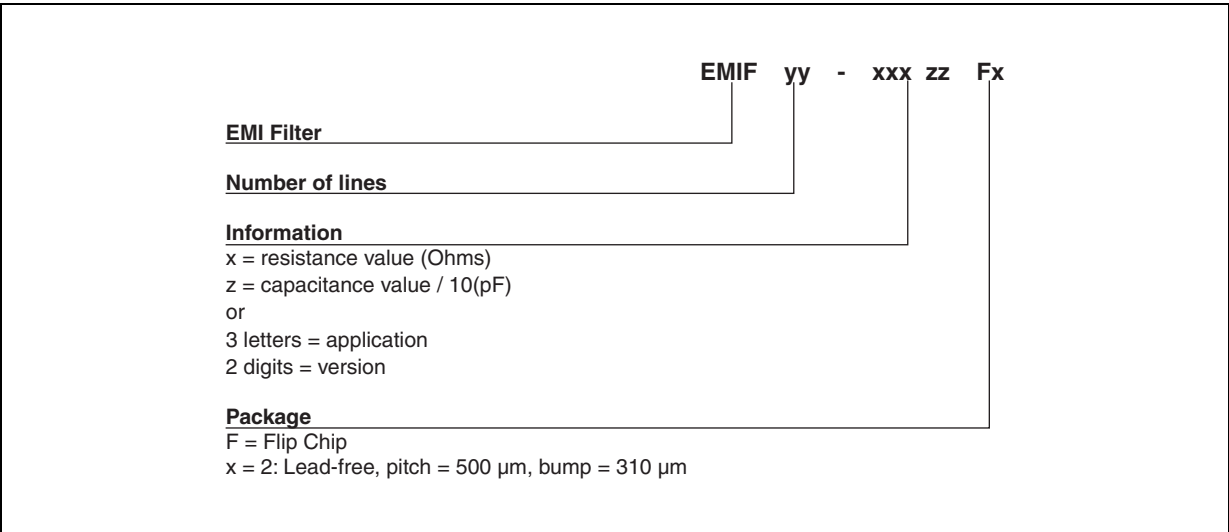


Figure 5. ESD response to IEC 61000-4-2 (-15 kV air discharge) on one output line



2 Ordering information scheme

Figure 6. Ordering information scheme



3 Package information

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a lead-free second level interconnect. The category of second level interconnect is marked on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at www.st.com.

Figure 7. Flip Chip dimensions

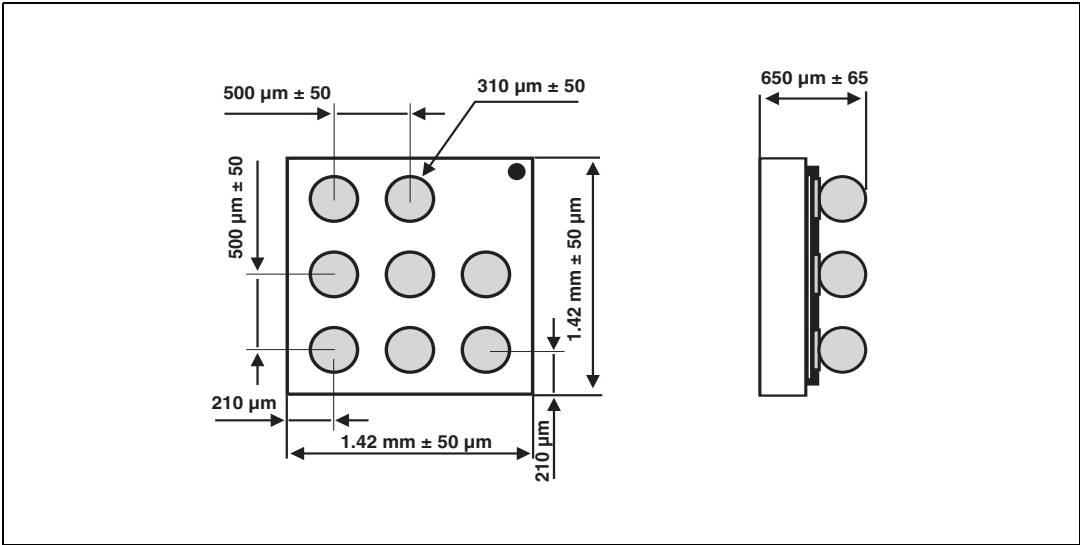


Figure 8. Flip Chip footprint

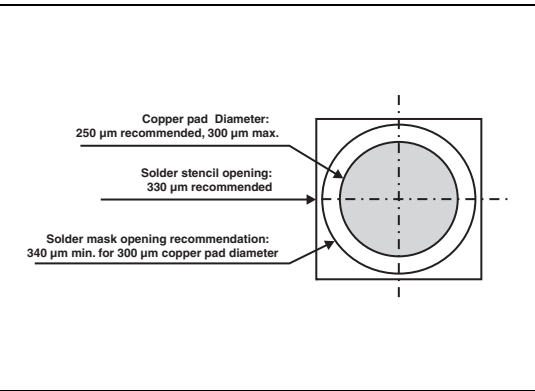


Figure 9. Marking

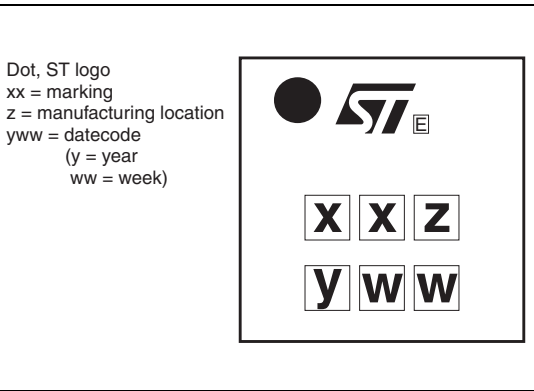
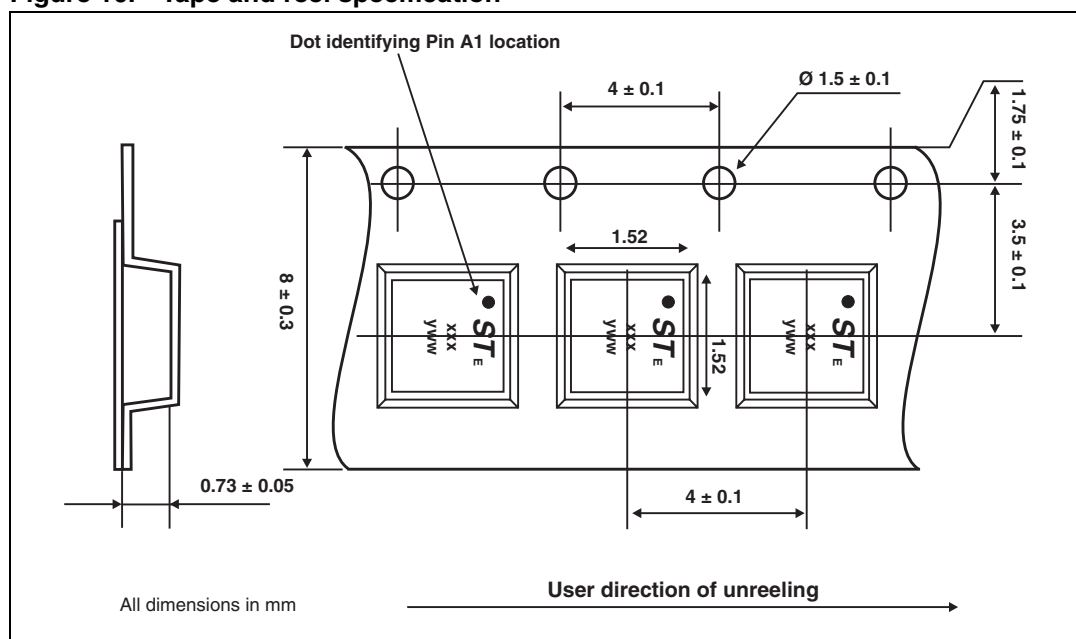


Figure 10. Tape and reel specification



Note:

More packing information is available in the application notes:

AN1235: "Flip Chip: Package description and recommendations for use"

AN1751: "EMI Filters: Recommendations and measurements"

4 Ordering information

Table 3. Ordering information

Order code	Marking	Package	Weight	Base qty	Delivery mode
EMIF01-SMIC01F2	GA	Flip Chip	2.8 mg	5000	Tape and reel 7"

5 Revision history

Table 4. Document revision history

Date	Revision	Changes
03-Oct-2006	1	Initial release
24-Apr-2008	2	Updated values of capacitors in Table 2. C ₁ , C ₂ , C ₃ , C ₄ from 0.85 nF to 1 nF, and C ₅ from 140 pF to 150 pF. Updated ECOPACK statement. Updated Figure 7 , and Figure 10 . Reformatted to current standards.

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