

Ordering number : ENN7506



NPN Epitaxial Planar Silicon Transistor

15C01S

**Low-Frequency
General-Purpose Amplifier Applications**

Applications

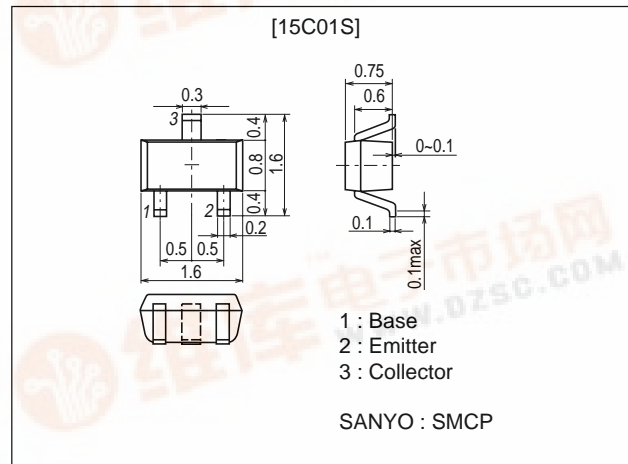
- Low-frequency Amplifier, muting circuit.

Features

- Large current capacitance.
- Low collector-to-emitter saturation voltage (resistance).
RCE (sat) typ.=0.58Ω [IC=0.7A, IB=35mA].
- Ultrasmall package facilitates miniaturization in end products.
- Small ON-resistance (Ron).

Package Dimensions

unit : mm
2106A



Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CB0}		20	V
Collector-to-Emitter Voltage	V _{CEO}		15	V
Emitter-to-Base Voltage	V _{EB0}		5	V
Collector Current	I _C		600	mA
Collector Current (Pulse)	I _{CP}		1.2	A
Collector Dissipation	P _C	Mounted on a glass epoxy board (20X30X1.6mm)	200	mW
Junction Temperature	T _J		150	°C
Storage Temperature	T _{stg}		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector Cutoff Current	I _{CB0}	V _{CB} =15V, I _E =0			0.1	μA
Emitter Cutoff Current	I _{EB0}	V _{EB} =4V, I _C =0			0.1	μA
DC Current Gain	h _{FE}	V _{CE} =2V, I _C =10mA	300		800	
Gain-Bandwidth Product	f _T	V _{CE} =2V, I _C =50mA		330		MHz
Output Capacitance	C _{ob}	V _{CB} =10V, f=1MHz		3.2		pF
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =200mA, I _B =10mA		150	300	mV
Base-to-Emitter Saturation Voltage	V _{BE(sat)}	I _C =200mA, I _B =10mA		0.9	1.2	V

Marking : YP

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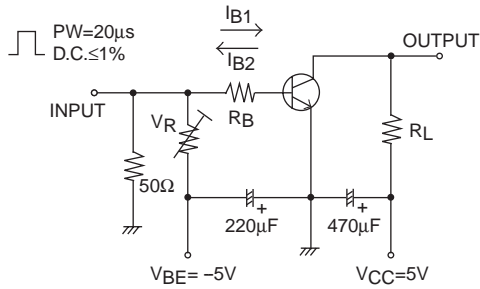


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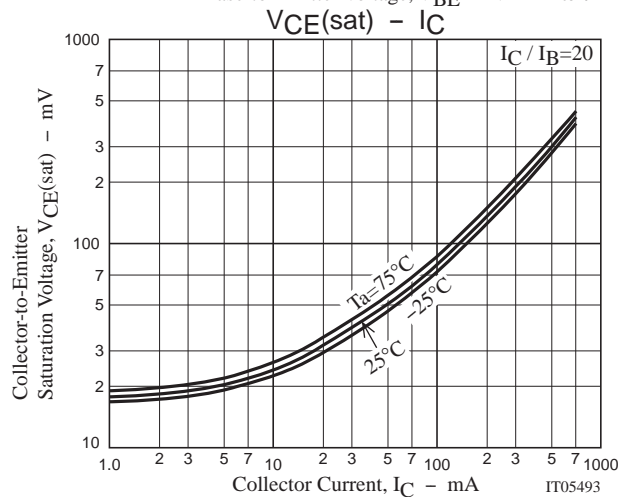
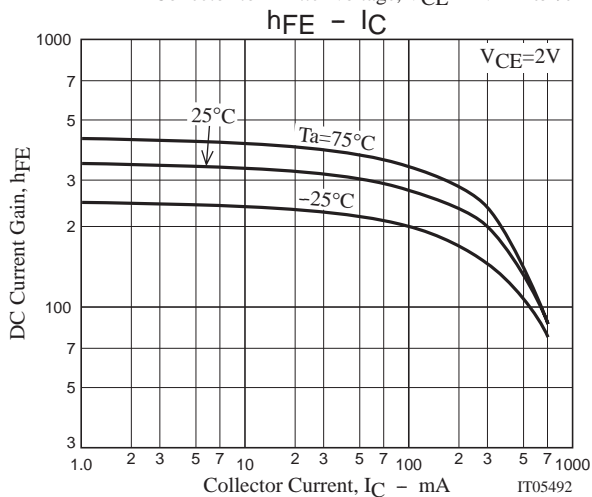
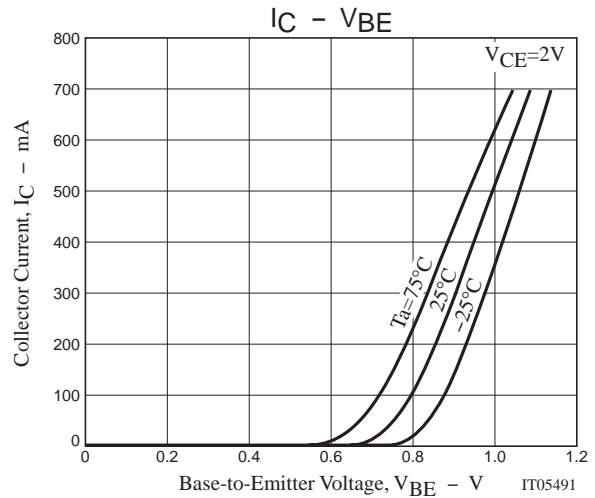
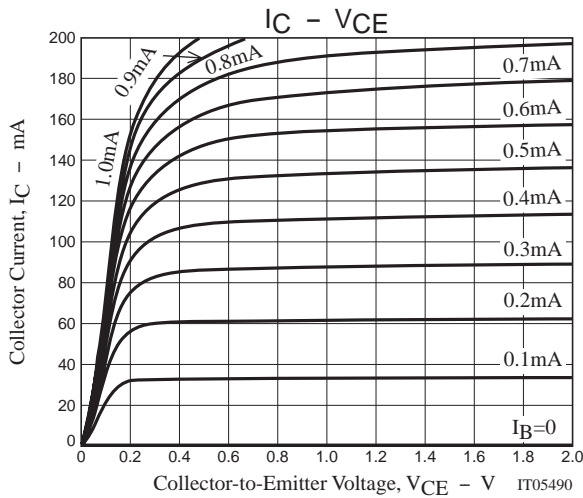
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Collector-to-Base Breakdown Voltage	$V_{(BR)CBO}$	$I_C=10\mu A, I_E=0$	20			V
Collector-to-Emitter Breakdown Voltage	$V_{(BR)CEO}$	$I_C=1mA, R_{BE}=\infty$	15			V
Emitter-to-Base Breakdown Voltage	$V_{(BR)EBO}$	$I_E=10\mu A, I_C=0$	5			V
Turn-ON Time	t_{on}	See specified Test Circuit.		30		ns
Storage Time	t_{stg}	See specified Test Circuit.		77		ns
Fall Time	t_f	See specified Test Circuit.		40		ns

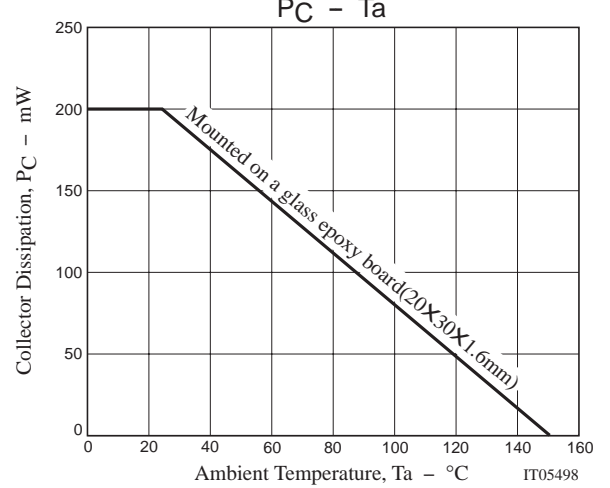
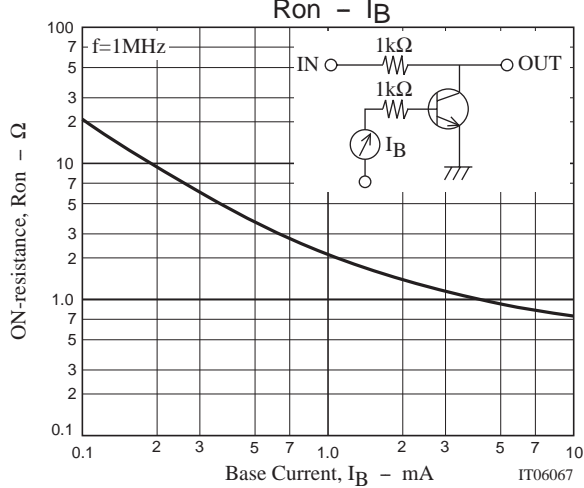
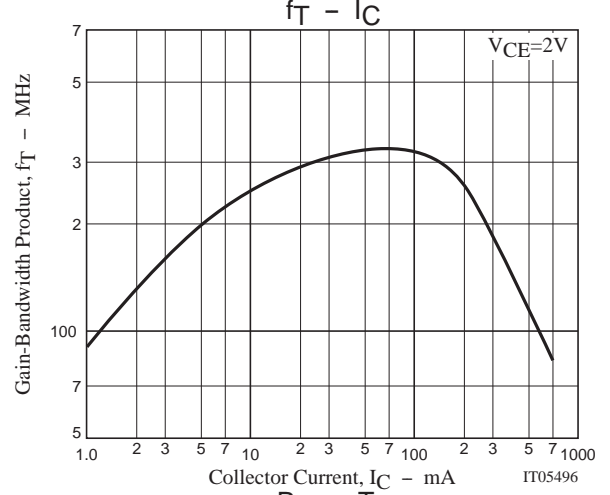
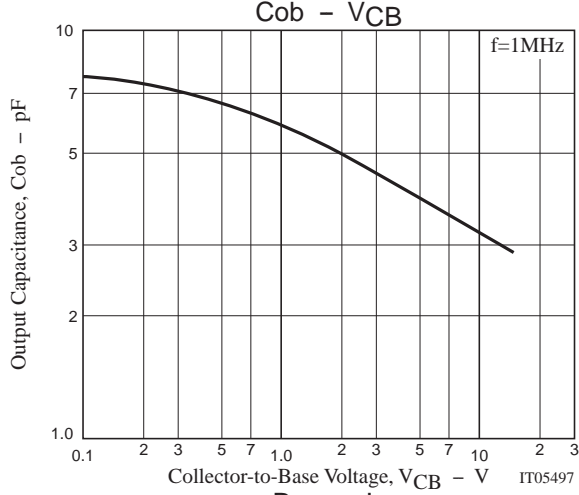
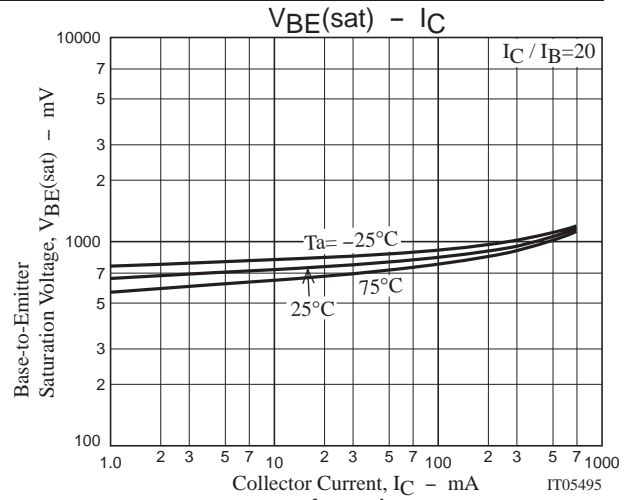
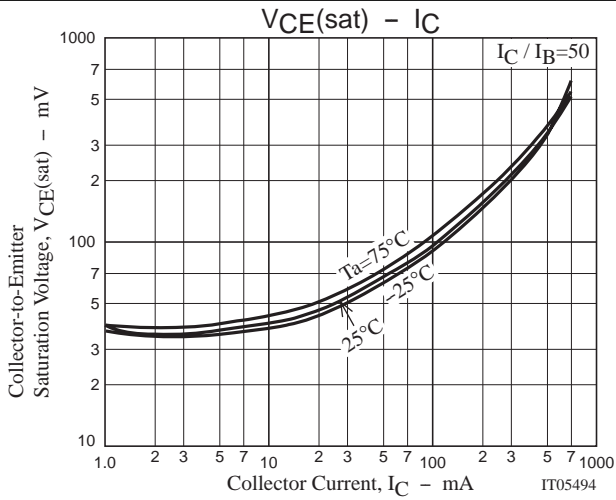
Switching Time Test Circuit



$$I_C = 20I_{B1} = -20I_{B2} = 500mA$$



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