



10 Base-T Interface Module for Multiport Applications

EPE6104



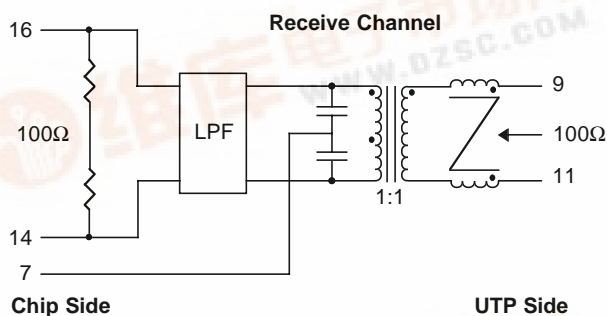
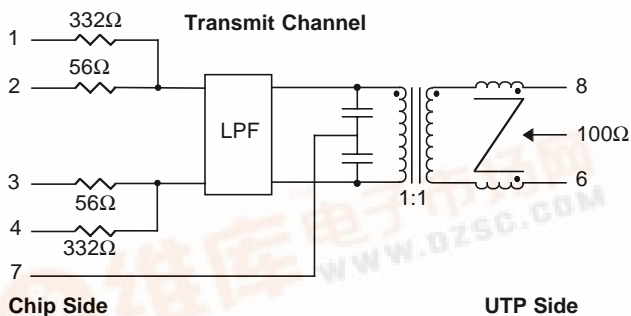
- Optimized for National DP83950B Repeater Chip •
- Robust design allows for the toughest soldering processes •
- Complies with or exceeds IEEE 802.3, 10 Base-T Requirements •

Electrical Parameters @ 25° C

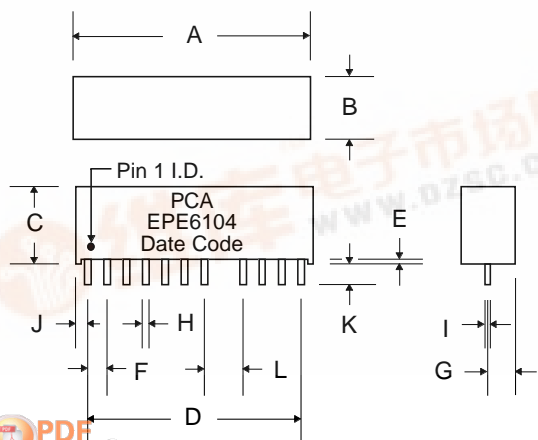
Cut-off Frequency (MHz)		Insertion Loss (dB Max.)		Return Loss (dB Min.)		Attenuation (dB Min.)								Common Mode Rejection (dB Min.)						Crosstalk (dB Min.)	
± 1.0 MHz		1-10 MHz		5-10 MHz		@ 20 MHz		@ 25 MHz		@ 30 MHz		@ 50-70 MHz		@ 50 MHz		@ 100 MHz		@ 200 MHz		@ 1-10 MHz	
Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv
17	17	-1	-1	-15	-15	-7	-5	-18	-11	-37	-15	-40	-35	-50	-50	-50	-50	-25	-25	-45	-45

- **Isolation** : meets or exceeds 802.3 IEEE Requirements • **Characteristic Filter Impedance** : 100 Ω •
- Referenced @ 5 MHz • All Resistors are matched within $\pm 1\%$ •

Schematic



Package



Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.		Min.	Max.	
A	1.220	1.240		30.99	31.50	
B	.310	.330		7.87	8.38	
C	.400	.430		10.16	10.92	
D	1.100	Typ.		27.94	Typ.	
E	.020	.023		.508	.584	
F	.100	Typ.	9 Plcs	2.54	Typ.	9 Plcs
G	.165	Typ.		4.19	Typ.	
H	.018	.022		.457	.559	
I	.010°	Typ.		.254	Typ.	
J	.065	Typ.		1.65	Typ.	
K	.120	.140		3.05	3.56	
L	.200	Typ.		5.08	Typ.	

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The circuit below is a guideline for interconnecting PCA's EPE6104 with NSC DP83950B Repeater/10 Base-T PHY chip over UTP cable. Further details of system design, such as chip pin-out, etc. can be obtained from the specific chip manufacturer.

Typical insertion loss of the isolation transformer/filter is 0.7dB. This parameter covers the entire spectrum of the encoded signals in 10 Base-T protocols. However, the predistortion resistor network introduces some loss which has to be taken into account in determining how well your design meets the Standard Template requirements.

Additionally, note that NSC suggests using a set of resistor networks with lower values suitable for more optimum voltage level control. Users can obtain modules with either values from PCA (EPE6092). But please check with NSC to find which set suits most for your specific application. EPE6104 has the lower values. Effective termination impedance offered by the outputs of the transmit channel is $2 \times (62 // 303) // 800 \Omega$ or approximately 92Ω . Thus, bench testing of return loss needs to add approx. 7.5Ω on each leg of the transmit inputs to take into account the 74ACTXXXX output impedance.

Note that some systems have auto polarity detection and some do not. If not, be certain to follow the proper polarity.

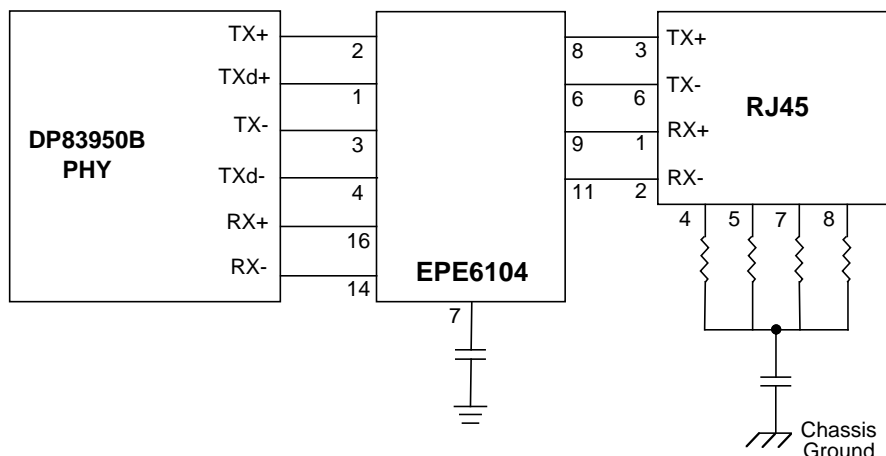
It is recommended that system designers ground the chip side center taps via a low voltage capacitor. Taking the cable side center taps to chassis via capacitors, is not recommended, as this will add cost without containing EMI. This may worsen EMI, specifically if the primary "common mode termination" is pulled to ground as shown.

The pull down resistors used around the RJ45 connector have been known to suppress unwanted radiation that unused wires pick up from the immediate environment. Their placement and use are to be considered carefully before a design is finalized.

It is recommended that there be a neat separation of ground planes in the layout. It is generally accepted practice to limit the plane off at least 0.08 inches away from the chip side pins of EPE6104. There need not be any ground plane beyond this point.

For best results, PCB designer should design the outgoing traces preferably to be 50Ω , balanced and well coupled to achieve minimum radiation from these traces.

Typical Application Circuit (for one port only)



Notes : * Pin-outs shown are for Hub/Repeater Applications only.