

# Token Ring Transceiver Module

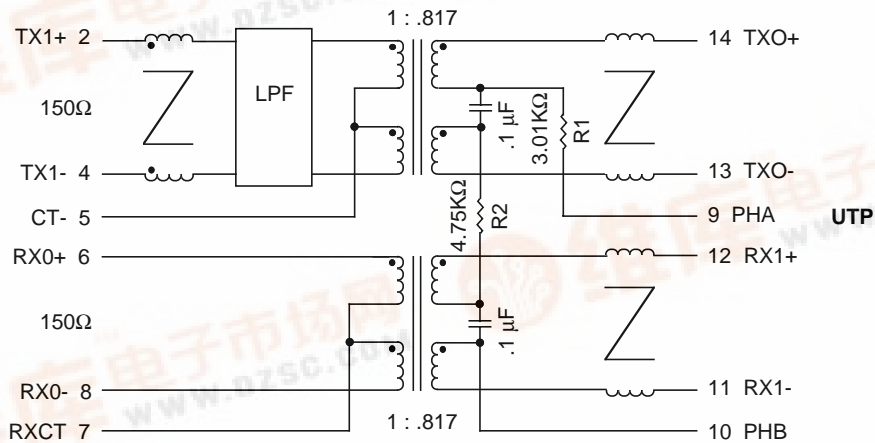
## EPT7055AM

- Optimized for voltage source driving into 150Ω •
- Enhanced Common Mode performance over UTP •
- Robust construction allows for solder reflow processes •
- Complies with or exceeds IEEE 802.5 Requirements •

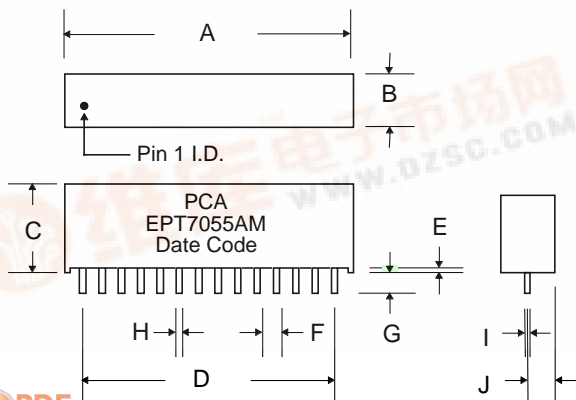
### Electrical Parameters @ 25° C

Impedance (Ω) [Xmit/Rcv]	Insertion Loss (dB Max.)								Return Loss (dB Min.)								Common Mode Rejection (dB Min.)								Crosstalk (dB Min.)
	1-16 MHz		32 MHz		36 MHz		44 MHz		1-8 MHz		8-17 MHz		17-24 MHz		1-30 MHz		30-100 MHz		100-200 MHz		200-300 MHz		10 MHz		
<b>Chip Side</b> 150																									
<b>Cable Side</b>	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	Xmit	Rcv	
100	-6	-5	-7	-1	-25	-3	-3.5	-3	-18	-23	-12	-20	-8	-10	-57	-40	-35	-35	-30	-29	-20	-17	-50		

### Schematic



### Package



### Dimensions

Dim.	(Inches)			(Millimeters)		
	Min.	Max.	Nom.	Min.	Max.	Nom.
A	1.480	1.500		37.85	38.10	
B	.265	.275		6.73	6.99	
C	.450	.460		11.43	11.68	
D	1.300	Typ.		33.02	Typ.	
E	.010	.015		.254	.381	
F	.100	.140		3.18	3.56	
G	.100	.120		2.54	Typ.	
H	.020	.024		.508	.610	
I	.010°	Typ.		.254	Typ.	
J	.135	.140		3.43	.356	

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## EPT7055AM

The circuit below is a guideline for interconnecting PCA's EPT7055AM with a typical Token Ring PHY chip for 4 Mb/16Mb applications over UTP cable. Further details of system design should be obtained from the specific chip manufacturer. Note that this module is optimized for a "voltage source" driver such as TI380C60.

Typical insertion loss of the isolation transformer is 0.5dB up to 16 MHz. Under terminated conditions, to transmit a 2V pk-pk signal across the cable, you must adjust the specific chip preset template control resistors to get at least 2.12V pk-pk across the transmit input pins.

Note that there is no need for the receiver side filtering. So consider this a cost effective solution for almost all Token Ring applications using this or similar chips.

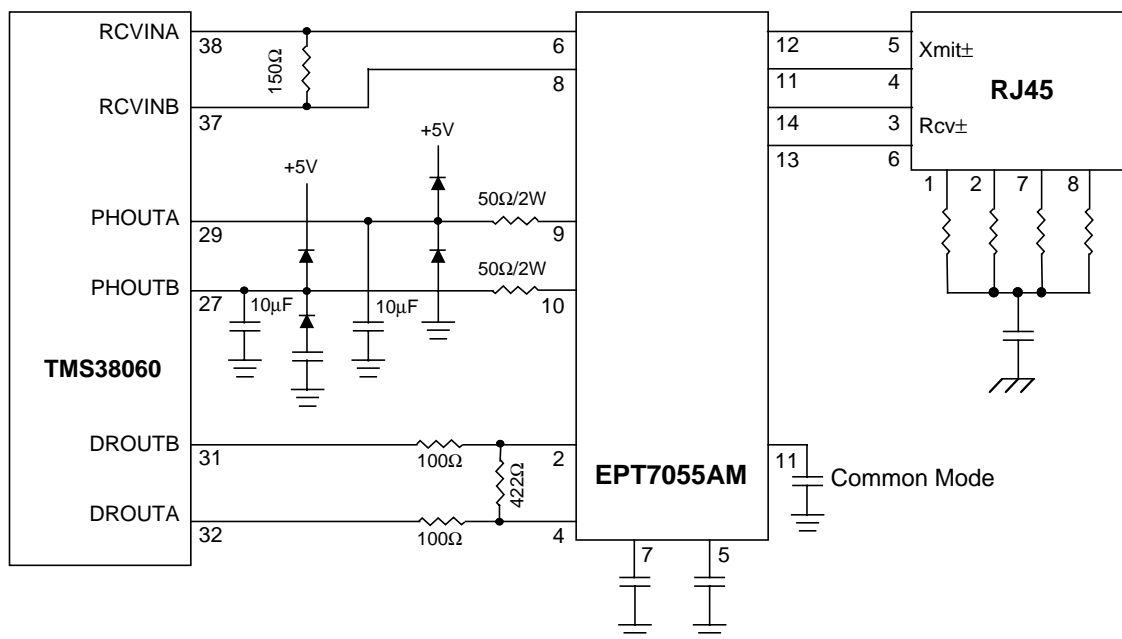
The pull down resistors to chassis via a cap shown around the RJ45 connector have been known to suppress unwanted radiation that unused wires pick up from the immediate environment. This is specially true if driving UTP cable. Their placement and use are to be considered carefully before a design is finalized.

No specific recommendation is made here for phantom return circuitry: implementation varies. Please note that additional emission control has been observed if both nodes of the phantom bypass capacitor on the transmit channel are pulled to the chassis ground via suitable capacitors.

It is recommended that there be a neat separation of ground planes in the layout. It is generally accepted practice to limit the plane off at least 0.05 inches away from the chip side of EPT7055AM. There need not be any ground plane beyond this point.

For best results, PCB designer should design the outgoing traces preferably to be 50Ω, balanced and well coupled to achieve minimum radiation from these traces.

### Typical Application Circuit Connection to TI380C60 (or Equivalent)



Notes : 1. Current limiting resistors R1 & R2 are included in this package.