



# ET721

## STEERING DIODE ARRAY

### APPLICATIONS

- ✓ Ethernet - 10/100/1000 Base T
- ✓ Set Top Box Interfac
- ✓ Handheld Electronics
- ✓ FireWire
- ✓ USB Interface
- ✓ Sensor Interface

### IEC COMPATIBILITY (EN61000-4)

- ✓ 61000-4-2 (ESD): Air - 15kV, Contact - 8kV
- ✓ 61000-4-4 (EFT): 40A - 5/50ns
- ✓ 61000-4-5 (Surge): 12A, 8/20 $\mu$ s - Level 1(Line-Gnd) & Level 2(Line-Line)

### FEATURES

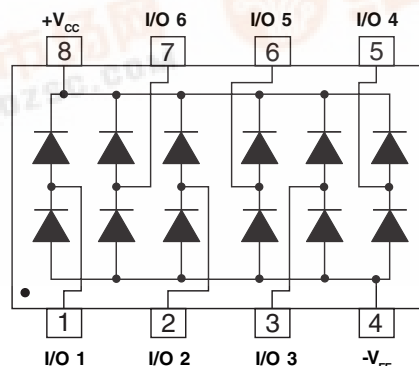
- ✓ ESD Protection > 40 kilovolts
- ✓ Low Clamping Voltage
- ✓ Provides Six (6) Lines of Protection
- ✓ Low Leakage Current < 200nA
- ✓ Low Capacitance: 3pF Typical
- ✓ RoHS Compliant

### MECHANICAL CHARACTERISTICS

- ✓ Molded JEDEC SO-8
- ✓ Weight 70 milligrams (Approximate)
- ✓ Available in Lead-Free Pure-Tin Plating(Annealed)
- ✓ Solder Reflow Temperature:  
Pure-Tin - Sn, 100: 260-270°C
- ✓ Consult Factory for Leaded Device Availability
- ✓ Flammability Rating UL 94V-0
- ✓ 12mm Tape and Reel Per EIA Standard 481
- ✓ Marking: Marking Code, Logo, Date Code & Pin One Defined By Dot on Top of Package



### PIN CONFIGURATION



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## DEVICE CHARACTERISTICS

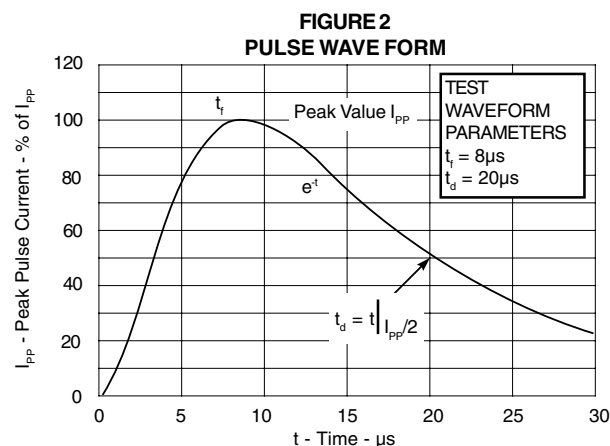
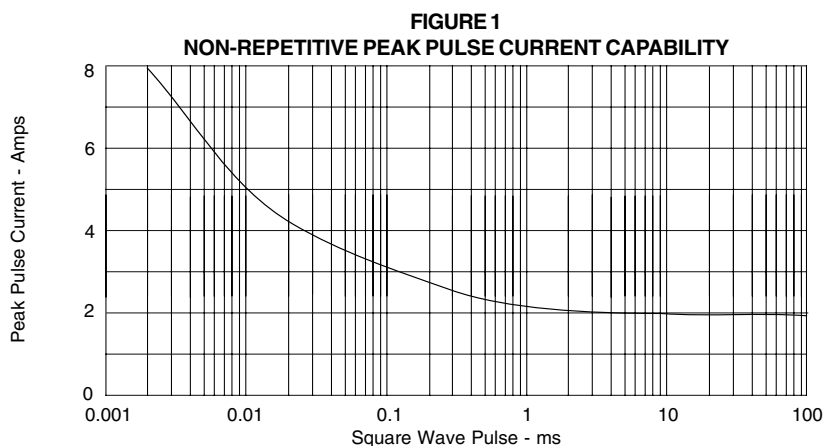
| MAXIMUM RATINGS @ 25°C Unless Otherwise Specified |           |            |       |
|---------------------------------------------------|-----------|------------|-------|
| PARAMETER                                         | SYMBOL    | VALUE      | UNITS |
| Operating Temperature                             | $T_A$     | -55 to 150 | °C    |
| Storage Temperature                               | $T_{STG}$ | -55 to 150 | °C    |
| Continuous Power Dissipation                      | $P_{PC}$  | 145        | mW    |

| ELECTRICAL CHARACTERISTICS PER LINE @ 25°C Unless Otherwise Specified |                |                                                                       |                                                                   |                                                                        |                                                                               |                                                                             |                                                  |
|-----------------------------------------------------------------------|----------------|-----------------------------------------------------------------------|-------------------------------------------------------------------|------------------------------------------------------------------------|-------------------------------------------------------------------------------|-----------------------------------------------------------------------------|--------------------------------------------------|
| PART NUMBER                                                           | DEVICE MARKING | REPETITIVE PEAK REVERSE VOLTAGE<br>(See Note 1)<br>$V_{RRM}$<br>VOLTS | TYPICAL FORWARD VOLTAGE<br>8/20 $\mu$ s<br>@ 1A<br>$V_F$<br>VOLTS | MAXIMUM PEAK PULSE FORWARD CURRENT<br>8/20 $\mu$ s<br>$I_{FM}$<br>AMPS | MAXIMUM REVERSE LEAKAGE CURRENT<br>(See Note 2)<br>@ $V_{RRM}$<br>$I_R$<br>nA | MAXIMUM QUIESCENT SUPPLY CURRENT<br>(See Note 3)<br>@ 20V<br>$I_{RQ}$<br>nA | TYPICAL CAPACITANCE<br>@ 0V, 1MHz<br>$C_j$<br>pF |
| ET721                                                                 | STA            | 50                                                                    | 2                                                                 | 12                                                                     | 20                                                                            | 200                                                                         | 3                                                |

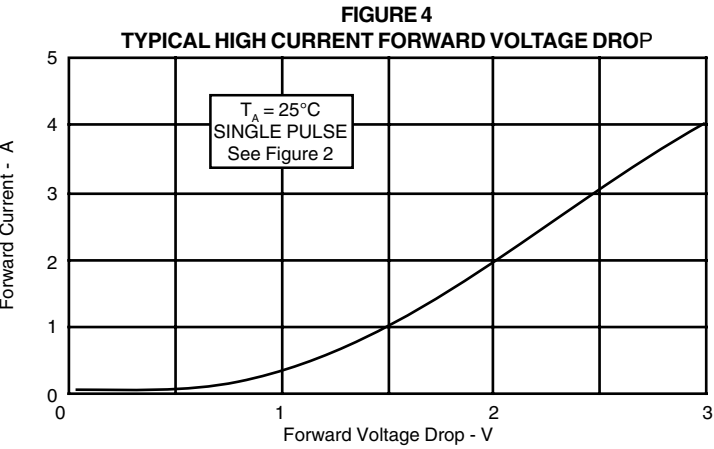
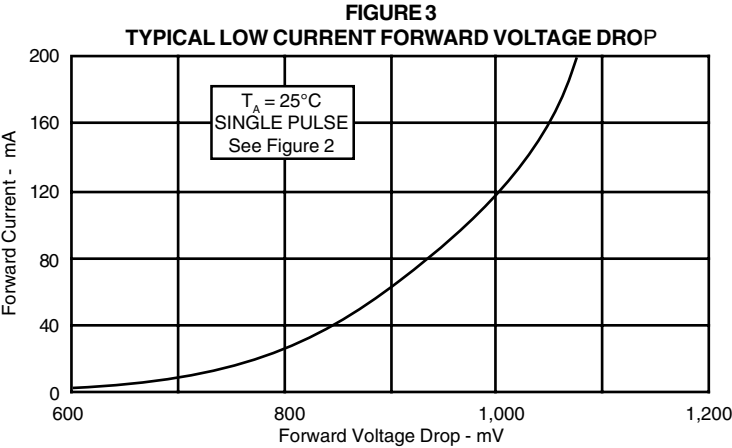
**Note 1:**  $V_{RRM}$  is  $+V_{CC}$  for pin 8,  $-V_{EE}$  for pin 4.

**Note 2:** +20V from pin 8 to 1, 2, 3, 5, 6, and 7. -20V from pin 4 to 1, 2, 3, 5, 6 and 7.

**Note 3:** +20V from pin 8 to 4.



GRAPHS



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## SO-8 PACKAGE OUTLINE & DIMENSIONS

