

Absolute Maximum Ratings * Ta = 25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CBO}	Collector-Base Voltage	40	V
V _{CEO}	Collector-Emitter Voltage	40	V
V _{EBO}	Emitter-Base Voltage	5	V
lc	Collector Current	100	mA
T _{STG}	Storage Temperature Range	-55~150	°C
TJ	Junction Temperature	150	℃
P _C	Collector Power Dissipation, by R _{0JA}	200	mW

* These ratings are limiting values above which the serviceability of any semiconductor device may by impaired.

Thermal Characteristics* Ta=25°C unless otherwise noted

Symbol	Parameter Parameter	Max	Units
R _{0JA}	Thermal Resistance, Junction to Ambient	600	°C/W
* Minimum land pac	size.		

Electrical Characteristics*

T _C = 25°C unless otherwise noted	

Symbol	Parameter	Test Condition	MIN	Тур	MAX	Units
V(BR)CBO	Collector-Emitter Breakdown Voltage	Ic = 100 uA, IE = 0	40	52	L OL	V
V(BR)CEO	Collector-Base Breakdown Voltage	Ic = 1mA, Iв = 0	40	W W	10000	V
Ісво	Collector-Cutoff Current	$V_{CB} = 30 \text{ V}, \text{ I}_{E} = 0$			0.1	uA
hfe	DC Current Gain	$V_{CE} = 5 V$, Ic = 1 mA	100		600	
Vce(sat)	Collector-Emitter Saturation Voltage	Ic = 10 mA, Iв = 1 mA			0.3	V
fт	Current Gain - Bandwidth Product	Vce = 10V, Ic = 5 mA		250		MHz
Ccb	Output Capacitance	Vcb = 10 V, IE = 0, f = 1.0 MHz		3.7		pF
R	Input Resistor		7	10	13	KΩ

<mark>* Pulse Test:</mark> PW≤300μs, Duty Cycle≤2%



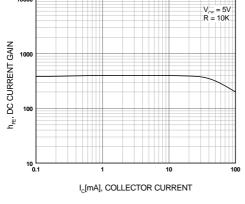


Figure 3. Power Derating

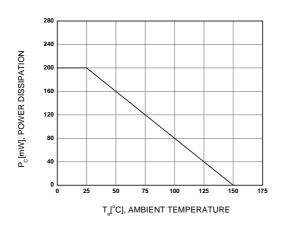
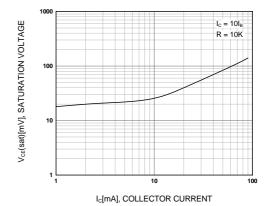
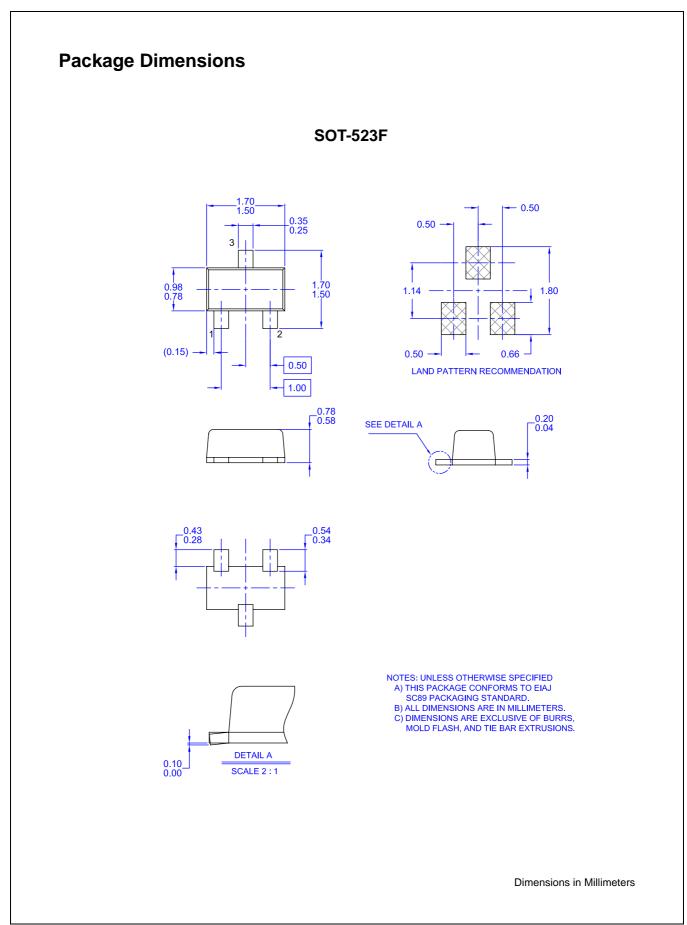


Figure 2. Collector-Emitter Saturation Voltage





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