



January 2006

FRFET™

FQP44N10F

100V N-Channel MOSFET

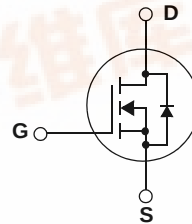
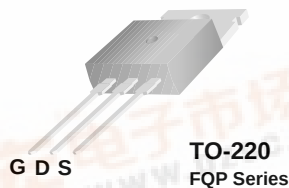
Features

- 43.5A, 100V, $R_{DS(on)} = 0.039\Omega$ @ $V_{GS} = 10V$
- Low gate charge (typical 48 nC)
- Low C_{rss} (typical 85 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability
- 175°C maximum junction temperature rating

Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for low voltage applications such as audio amplifier, high efficiency switching DC/DC converters, and DC motor control.



Absolute Maximum Ratings

Symbol	Parameter	FQP44N10F	Units
V_{DSS}	Drain-Source Voltage	100	V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	43.5	A
	- Continuous ($T_C = 100^\circ\text{C}$)	30.8	A
I_{DM}	Drain Current - Pulsed (Note 1)	174	A
V_{GSS}	Gate-Source Voltage	± 25	V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	530	mJ
I_{AR}	Avalanche Current (Note 1)	43.5	A
E_{AR}	Repetitive Avalanche Energy (Note 1)	14.6	mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	15	V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	146	W
	- Derate above 25°C	0.97	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +175	°C
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300	°C

Thermal Characteristics

Symbol	Parameter	FQP44N10F	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.03	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink	0.5	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	°C/W

Package Marking and Ordering Information

Device Marking	Device	Package	Reel Size	Tape Width	Quantity
FQP44N10F	FQP44N10F	TO-220	--	--	50

Electrical Characteristics T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	100	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.1	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 100 V, V _{GS} = 0 V	--	--	10	μA
		V _{DS} = 80 V, T _C = 150°C	--	--	100	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 25 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -25 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 21.75 A	--	0.03	0.039	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 21.75 A (Note 4)	--	30	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	1400	1800	pF
C _{oss}	Output Capacitance		--	425	550	pF
C _{rss}	Reverse Transfer Capacitance		--	85	110	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 50 V, I _D = 43.5A, R _G = 25 Ω	--	19	45	ns
t _r	Turn-On Rise Time		--	190	390	ns
t _{d(off)}	Turn-Off Delay Time		--	90	190	ns
t _f	Turn-Off Fall Time		(Note 4, 5)	--	100	210
Q _g	Total Gate Charge	V _{DS} = 80 V, I _D = 43.5A, V _{GS} = 10 V	--	48	62	nC
Q _{gs}	Gate-Source Charge		--	9.0	--	nC
Q _{gd}	Gate-Drain Charge		(Note 4, 5)	--	24	--
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	43.5	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	174	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 43.5 A	--	--	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 43.5 A, dI _F / dt = 100 A/μs (Note 4)	--	70	--	ns
Q _{rr}	Reverse Recovery Charge		--	175	--	nC

NOTES:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. L = 0.42mH, I_{AS} = 43.5A, V_{DD} = 25V, R_G = 25 Ω, Starting T_J = 25°C
3. I_{SD} ≤ 43.5A, di/dt ≤ 3200A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

Typical Performance Characteristics

Figure 1. On-Region Characteristics

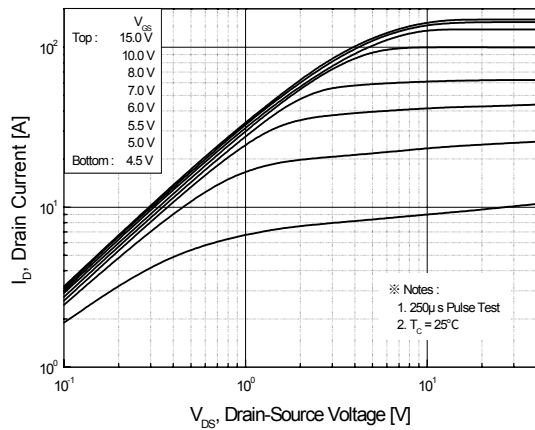


Figure 2. Transfer Characteristics

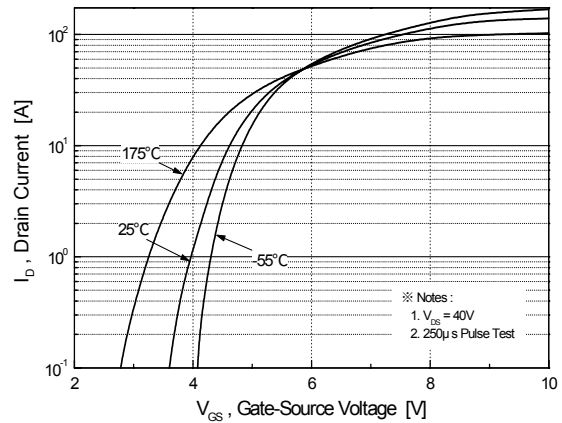


Figure 3. On-Resistance Variation vs. Drain Current and Gate Voltage

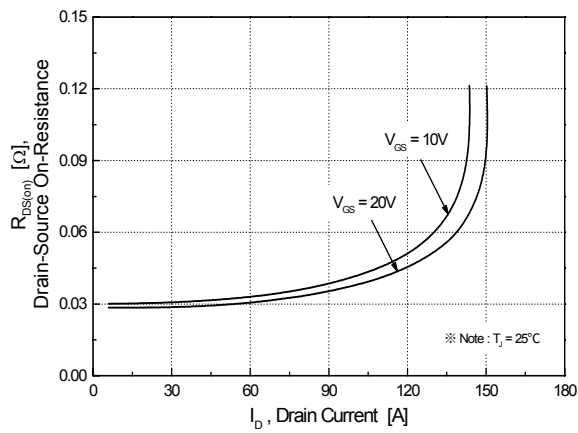


Figure 4. Body Diode Forward Voltage Variation vs. Source Current and Temperature

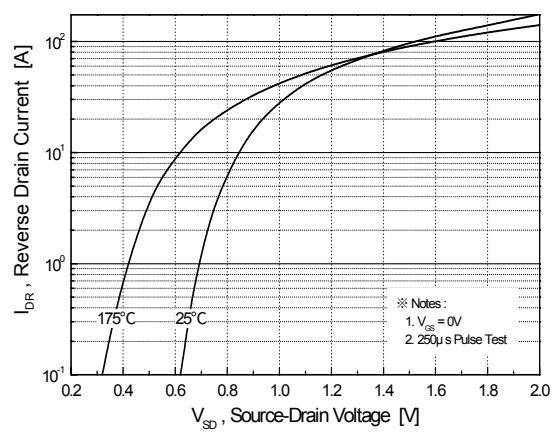


Figure 5. Capacitance Characteristics

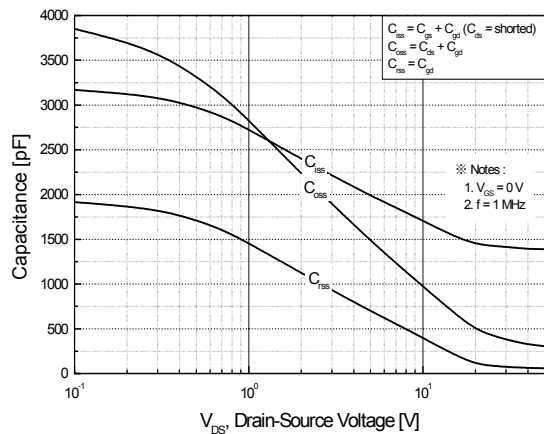
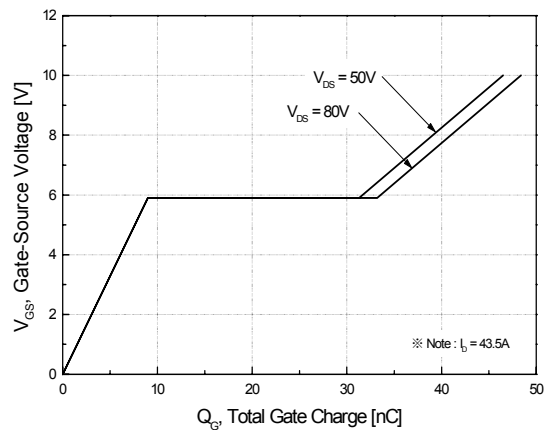


Figure 6. Gate Charge Characteristics



Typical Performance Characteristics (Continued)

Figure 7. Breakdown Voltage Variation vs. Temperature

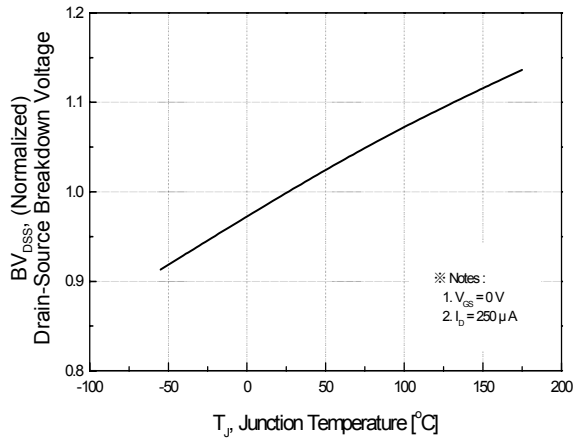


Figure 8. On-Resistance Variation vs. Temperature

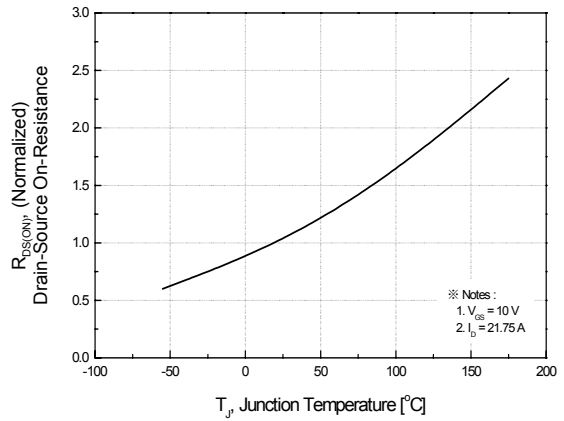


Figure 9. Maximum Safe Operating Area

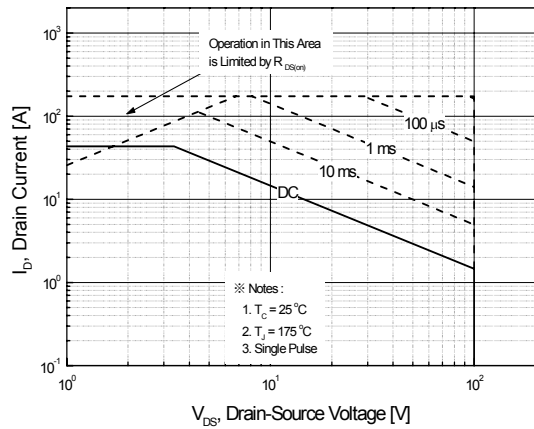


Figure 10. Maximum Drain Current vs. Case Temperature

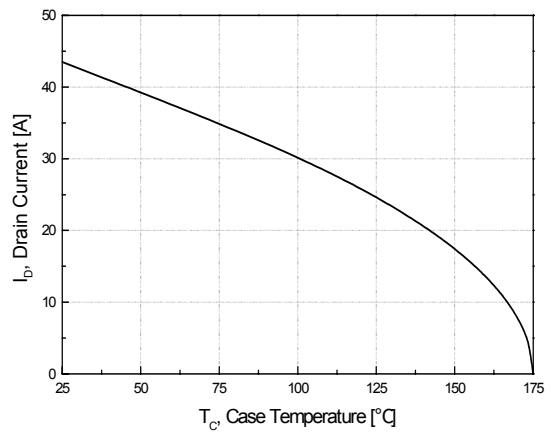
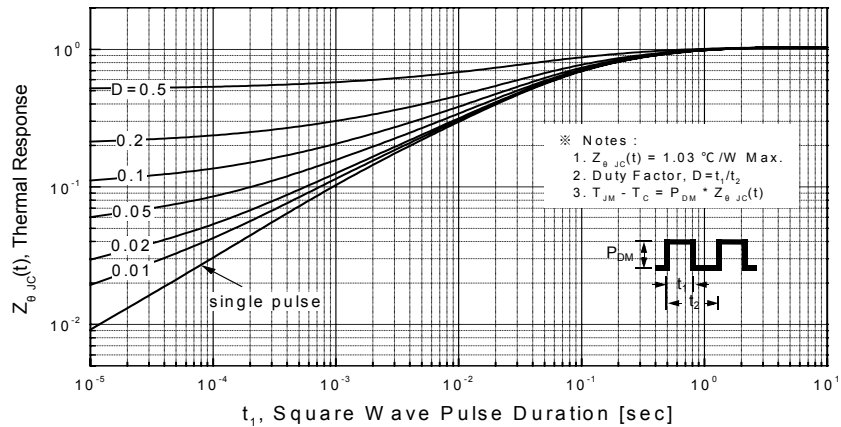
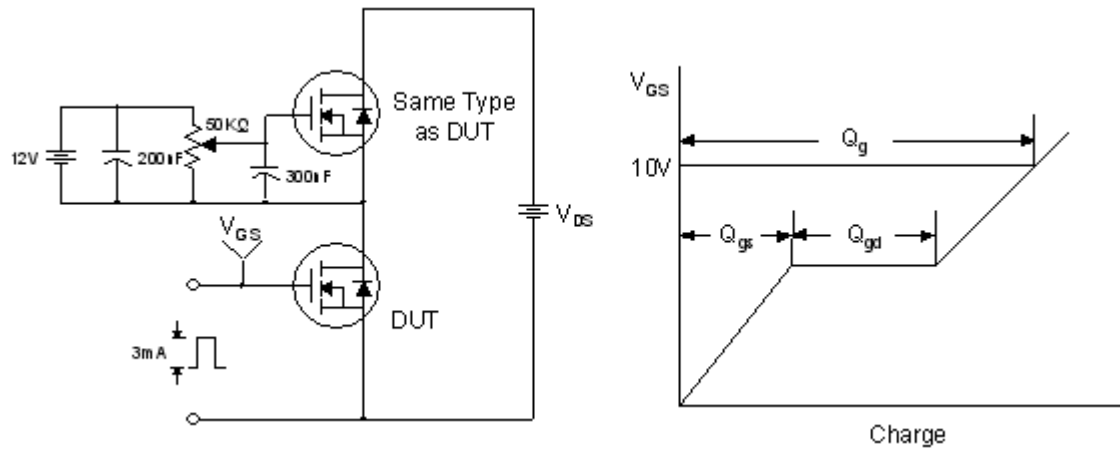


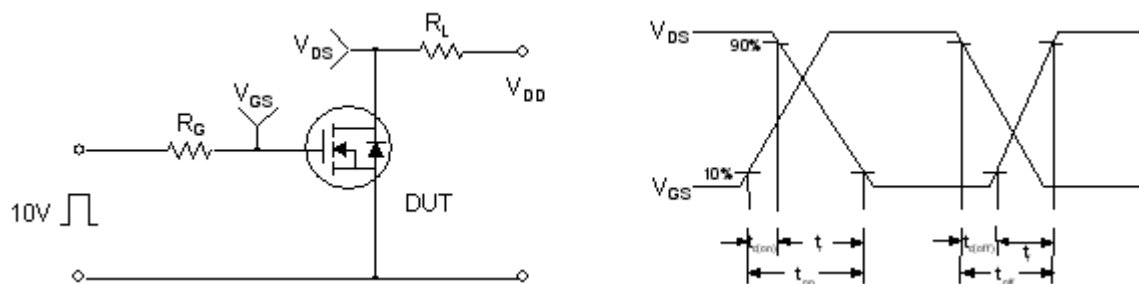
Figure 11. Transient Thermal Response Curve



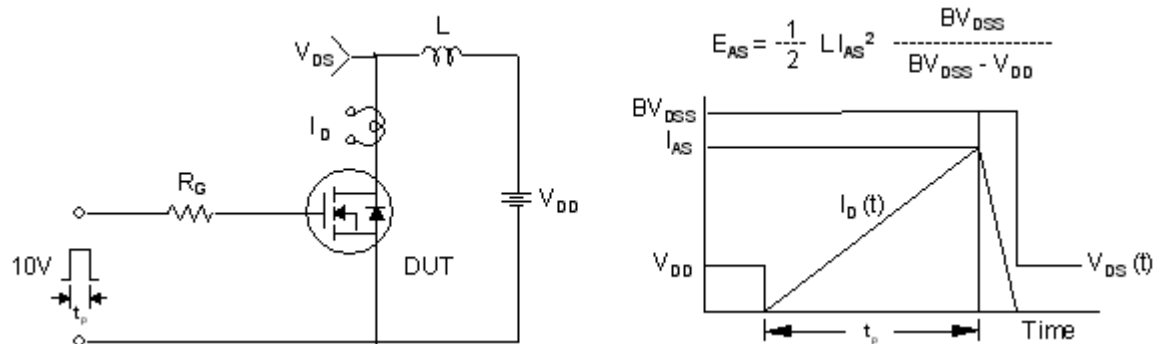
Gate Charge Test Circuit & Waveform



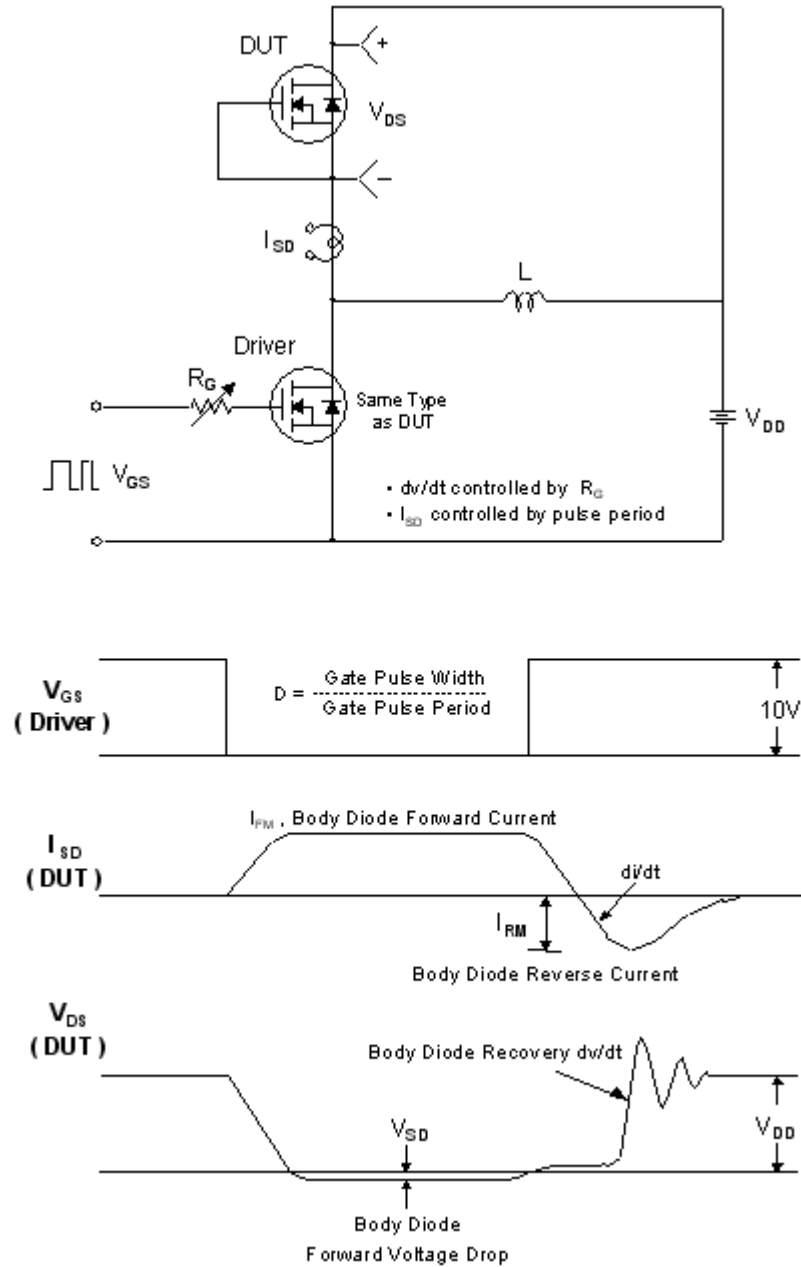
Resistive Switching Test Circuit & Waveforms



Unclamped Inductive Switching Test Circuit & Waveforms



Peak Diode Recovery dv/dt Test Circuit & Waveforms



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Rev. I17