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June 2008

FSA1208 Low-Power, Eight-Port, High-Speed Isolation Switch

Features

Low On Capacitance: 6pF Typical
 Low On Resistance: 15Ω Typical

Low Power Consumption: 1µA Maximum

 10µA Maximum I_{CCT} over an Expanded Voltage Range (V_{IN}=2.3V, V_{CC}=4.3V)

Wide -3db Bandwidth: > 400MHz

 Packaged in Space-Saving 20-Lead MLP (2.5x4.5mm)

8kV ESD Rating; >16kV Power/GND ESD Rating

Low C_{OFF} Capacitance: 2.5pF Typical

Applications

DIMM DDR Memory

IMPORTANT NOTE:

For additional performance information, please contact analogswitch@fairchildsemi.com.

Description

The FSA1208 is a low-power, eight-port, high-speed switch. This part is configured as a single-pole, single-throw switch and is optimized for isolating a high-speed source, such as a DDR memory bus. The FSA1208 features an extremely low on capacitance (CoN) of 6pF. Superior channel-to-channel crosstalk minimizes interference.

The FSA1208 contains special circuitry on the A & B pins that allows the device to withstand an over-voltage condition. This device is also designed to minimize current consumption even when the control voltage applied to the /OE pin is lower than the supply voltage (Vcc). Applications include port isolation and switching in DDR memory modules, portable cell phones, PDAs, digital cameras, printers, and notebook computers.

Ordering Information

Part Number	Top Mark	Operating Temperature Range	Package	© Eco Status
FSA1208BQX	F1208	-40 to +85°C	20-Lead, Quad, Molded Leadless Package (MLP), 2.5x4.5mm	Green

For Fairchild's definition of "green" Eco Status, please visit: http://www.fairchildsemi.com/company/green/rohs_green.html.

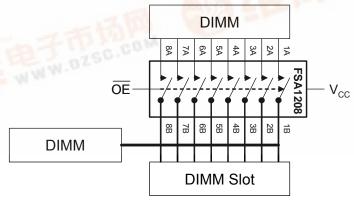


Figure 1. Analog Symbol





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PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative / In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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