RENESAS

FS50SM-3

High-Speed Switching Use
Nch Power MOS FET

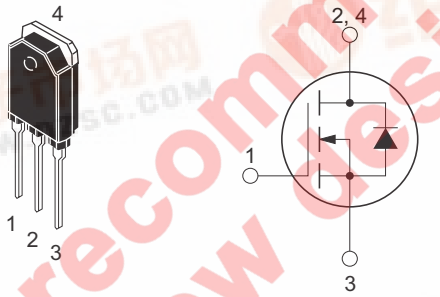
REJ03G1422-0200
(Previous: MEJ02G0120-0101)
Rev.2.00
Aug 07, 2006

Features

- Drive voltage : 10 V
- V_{DSS} : 150 V
- $r_{DS(ON)\ (max)}$: 31 mΩ
- I_D : 50 A
- Integrated Fast Recovery Diode (TYP.) : 130 ns

Outline

RENESAS Package code: PRSS0004ZB-A
(Package name: TO-3P)



1. Gate
2. Drain
3. Source
4. Drain

Applications

Motor control, Lamp control, Solenoid control, DC-DC converters, etc.

Maximum Ratings

(Tc = 25°C)

Parameter	Symbol	Ratings	Unit	Conditions
Drain-source voltage	V_{DSS}	150	V	V_{GS} = 0 V
Gate-source voltage	V_{GSS}	±20	V	V_{DS} = 0 V
Drain current	I_D	50	A	
Drain current (Pulsed)	I_{DM}	200	A	
Avalanche drain current (Pulsed)	I_{DA}	50	A	L = 100 µH
Source current	I_S	50	A	
Source current (Pulsed)	I_{SM}	200	A	
Maximum power dissipation	P_D	150	W	
Channel temperature	Tch	– 55 to +150	°C	
Storage temperature	Tstg	– 55 to +150	°C	
Mass	—	4.8	g	Typical value

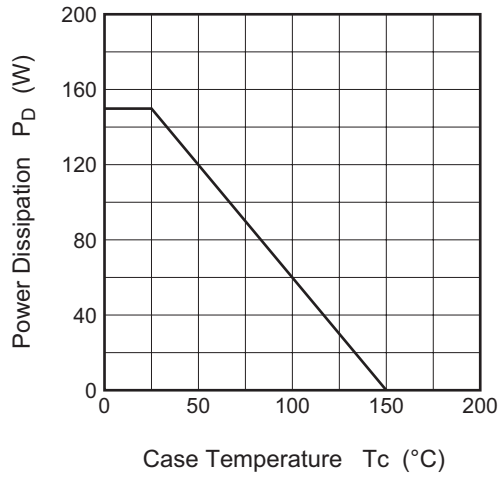
Electrical Characteristics

(Tch = 25°C)

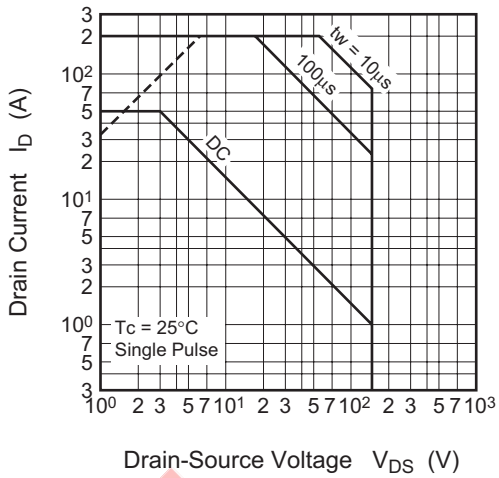
Parameter	Symbol	Min	Typ	Max	Unit	Test Conditions
Drain-source breakdown voltage	$V_{(BR)DSS}$	150	—	—	V	I_D = 1 mA, V_{GS} = 0 V
Gate-source leakage current	I_{GSS}	—	—	±0.1	µA	V_{GS} = ±20 V, V_{DS} = 0 V
Drain-source leakage current	I_{DSS}	—	—	0.1	mA	V_{DS} = 150 V, V_{GS} = 0 V
Gate-source threshold voltage	$V_{GS(th)}$	2.0	3.0	4.0	V	I_D = 1 mA, V_{DS} = 10 V
Drain-source on-state resistance	$r_{DS(ON)}$	—	24	31	mΩ	I_D = 25 A, V_{GS} = 10 V
Drain-source on-state voltage	$V_{DS(ON)}$	—	0.600	0.775	V	I_D = 25 A, V_{GS} = 10 V
Forward transfer admittance	$\lvert y_{fs} \rvert$	—	55	—	S	I_D = 25 A, V_{DS} = 10 V
Input capacitance	Ciss	—	6540	—	pF	V_{DS} = 10 V, V_{GS} = 0 V, f = 1MHz
Output capacitance	Coss	—	860	—	pF	
Reverse transfer capacitance	Crss	—	360	—	pF	
Turn-on delay time	$t_{d(on)}$	—	95	—	ns	V_{DD} = 80 V, I_D = 25 A, V_{GS} = 10 V, R_{GEN} = R_{GS} = 50 Ω
Rise time	t_r	—	155	—	ns	
Turn-off delay time	$t_{d(off)}$	—	380	—	ns	
Fall time	t_f	—	180	—	ns	
Source-drain voltage	V_{SD}	—	1.0	1.5	V	I_S = 25 A, V_{GS} = 0 V
Thermal resistance	$R_{th(ch-c)}$	—	—	0.83	°C/W	Channel to case
Reverse recovery time	t_{rr}	—	130	—	ns	I_S = 50 A, d_{is}/d_t = – 100 A/µs

Performance Curves

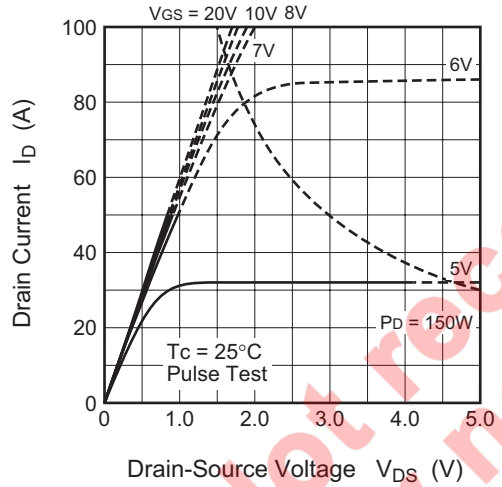
Power Dissipation Derating Curve



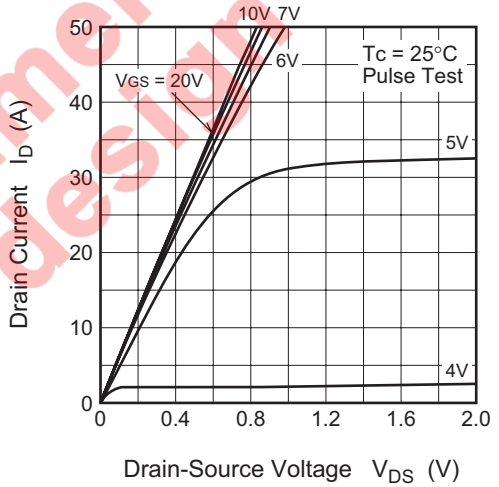
Maximum Safe Operating Area



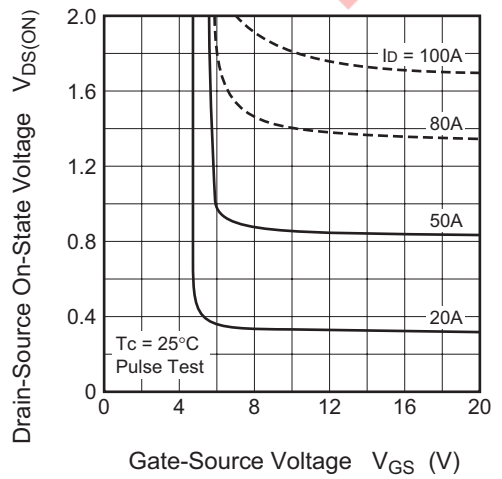
Output Characteristics (Typical)



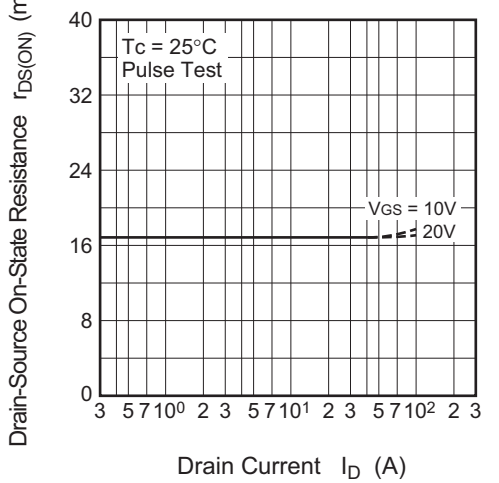
Output Characteristics (Typical)



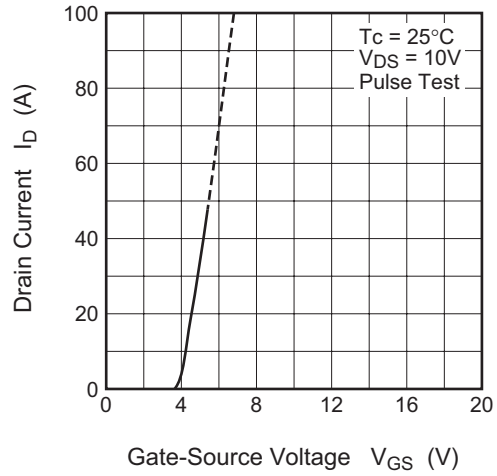
On-State Voltage vs.
Gate-Source Voltage (Typical)



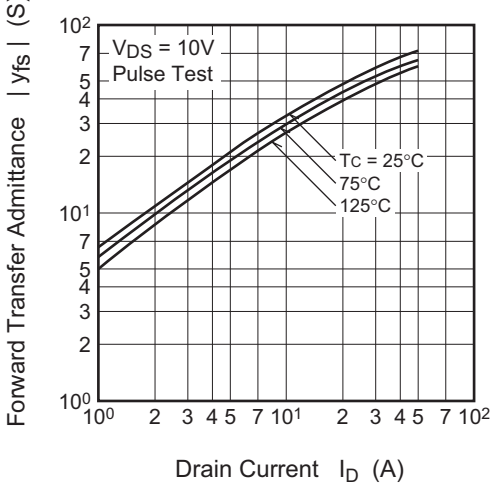
On-State Resistance vs.
Drain Current (Typical)



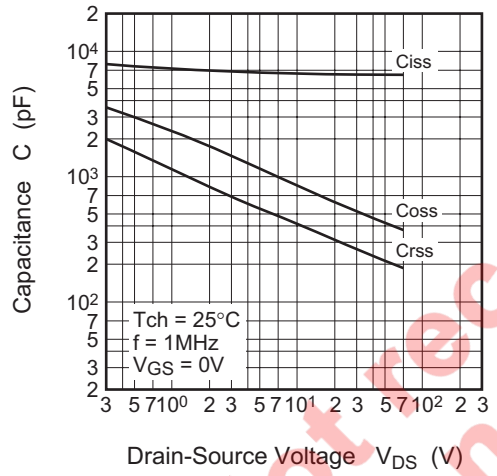
Transfer Characteristics (Typical)



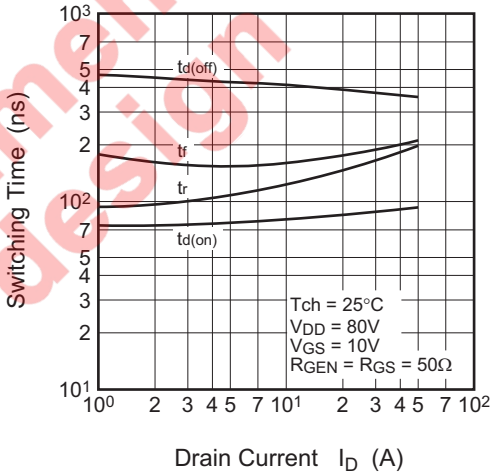
Forward Transfer Admittance vs. Drain Current (Typical)



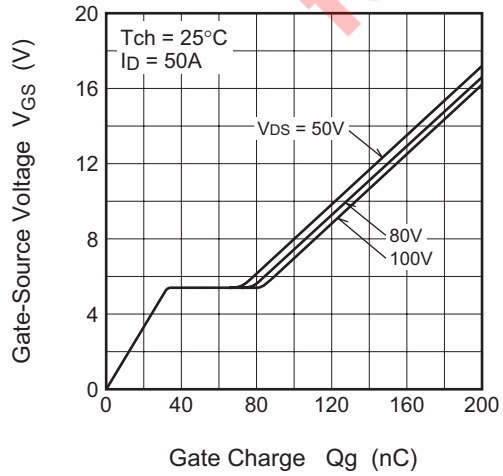
Capacitance vs. Drain-Source Voltage (Typical)



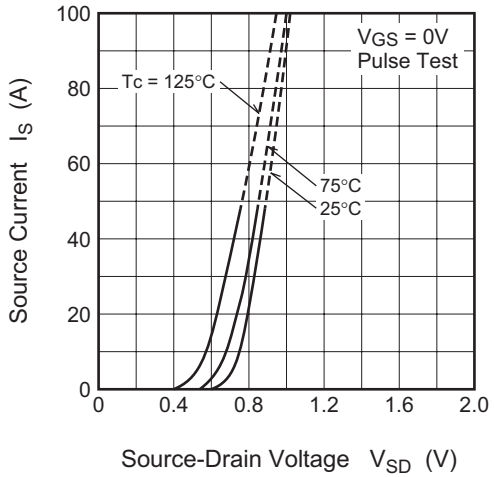
Switching Characteristics (Typical)



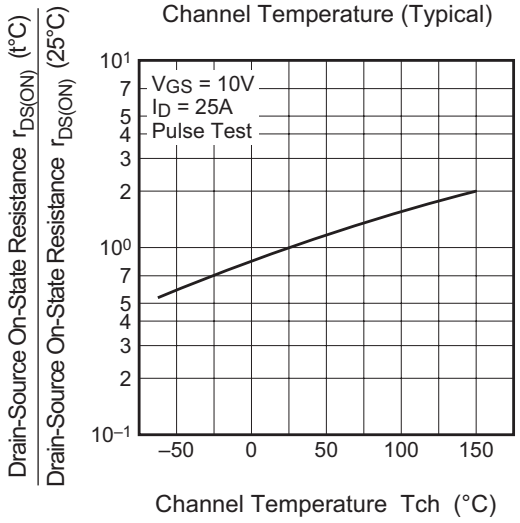
Gate-Source Voltage vs. Gate Charge (Typical)



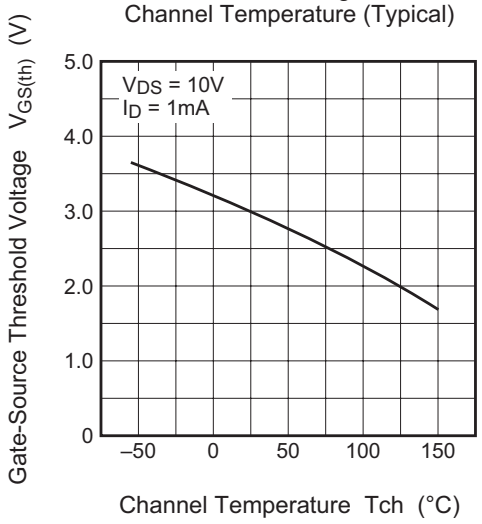
Source-Drain Diode Forward Characteristics (Typical)



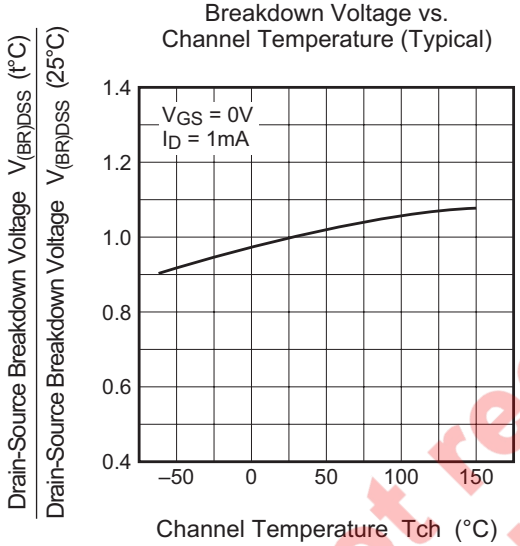
On-State Resistance vs.
Channel Temperature (Typical)



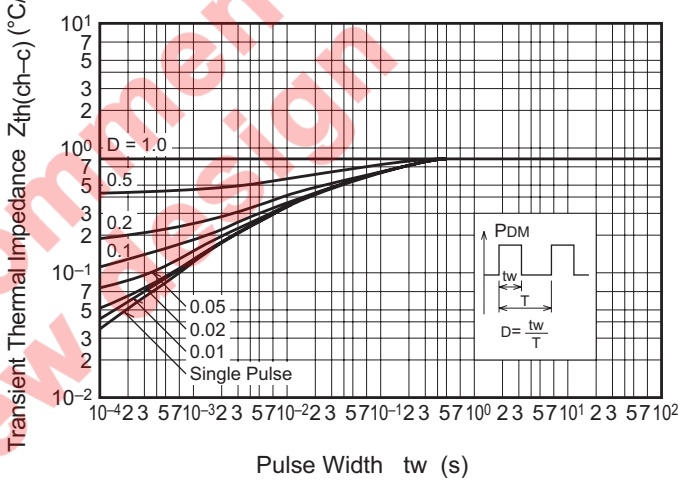
Threshold Voltage vs.
Channel Temperature (Typical)



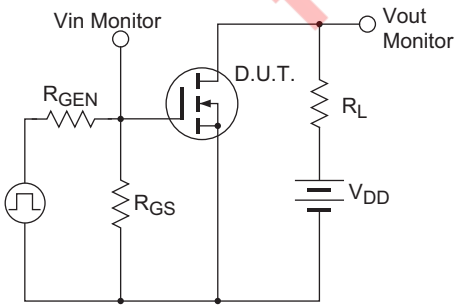
Breakdown Voltage vs.
Channel Temperature (Typical)



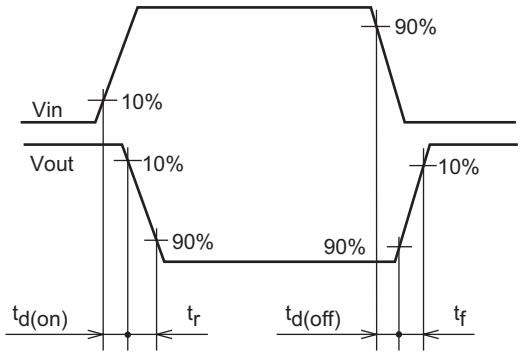
Transient Thermal Impedance Characteristics



Switching Time Measurement Circuit



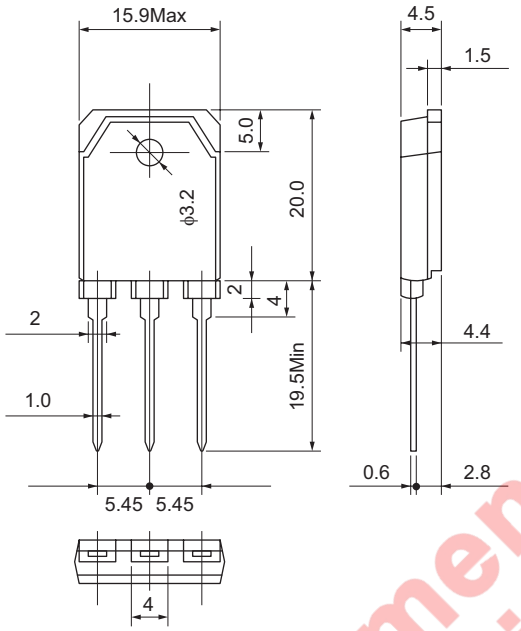
Switching Waveform



Package Dimensions

Package Name	JEITA Package Code	RENESAS Code	Previous Code	MASS[Typ.]
TO-3P*	SC-65	PRSS0004ZB-A	—	4.8g

Unit: mm



Order Code

Lead form	Standard packing	Quantity	Standard order code	Standard order code example
Straight type	Static electricity prevention bag	20	Type name	FS50SM-3
Lead form	Plastic Magazine (Tube)	30	Type name – Lead forming code	FS50SM-3-A8

Note : Please confirm the specification about the shipping in detail.

Renesas Technology Corp. Sales Strategic Planning Div. Nippon Bldg., 2-6-2, Ohte-machi, Chiyoda-ku, Tokyo 100-0004, Japan

RENESAS SALES OFFICES

http://www.renesas.com

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Renesas Technology America, Inc.
450 Holger Way, San Jose, CA 95134-1368, U.S.A
Tel: <1> (408) 382-7500, Fax: <1> (408) 382-7501

Renesas Technology Europe Limited
Dukes Meadow, Millboard Road, Bourne End, Buckinghamshire, SL8 5FH, U.K.
Tel: <44> (1628) 585-100, Fax: <44> (1628) 585-900

Renesas Technology (Shanghai) Co., Ltd.
Unit 204, 205, AZIACenter, No.1233 Lujiazui Ring Rd, Pudong District, Shanghai, China 200120
Tel: <86> (21) 5877-1818, Fax: <86> (21) 6887-7898

Renesas Technology Hong Kong Ltd.
7th Floor, North Tower, World Finance Centre, Harbour City, 1 Canton Road, Tsimshatsui, Kowloon, Hong Kong
Tel: <852> 2265-6688, Fax: <852> 2730-6071

Renesas Technology Taiwan Co., Ltd.
10th Floor, No.99, Fushing North Road, Taipei, Taiwan
Tel: <886> (2) 2715-2888, Fax: <886> (2) 2713-2999

Renesas Technology Singapore Pte. Ltd.
1 Harbour Front Avenue, #06-10, Keppel Bay Tower, Singapore 098632
Tel: <65> 6213-0200, Fax: <65> 6278-8001

Renesas Technology Korea Co., Ltd.
Kukje Center Bldg. 18th Fl., 191, 2-ka, Hangang-ro, Yongsan-ku, Seoul 140-702, Korea
Tel: <82> (2) 796-3115, Fax: <82> (2) 796-2145

Renesas Technology Malaysia Sdn. Bhd
Unit 906, Block B, Menara Amcorp, Amcorp Trade Centre, No.18, Jalan Persiaran Barat, 46050 Petaling Jaya, Selangor Darul Ehsan, Malaysia
Tel: <603> 7955-9390, Fax: <603> 7955-9510

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