

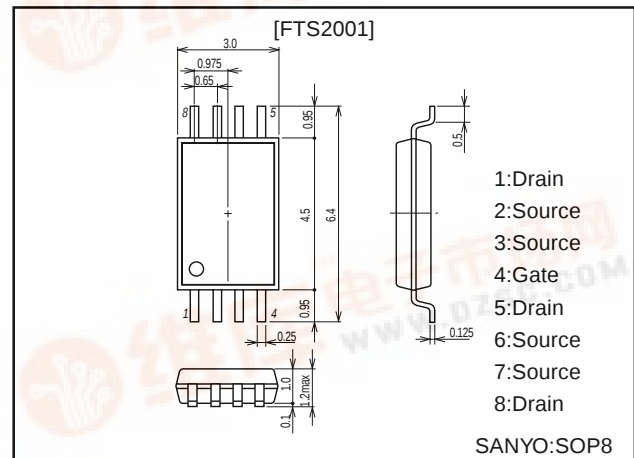
SANYO**FTS2001****DC-DC Converter Applications****Features**

- Low ON resistance.
- 2.5V drive.
- Mount height 1.1mm.

Package Dimensions

unit:mm

2147

**Specifications****Absolute Maximum Ratings** at $T_a = 25^\circ\text{C}$

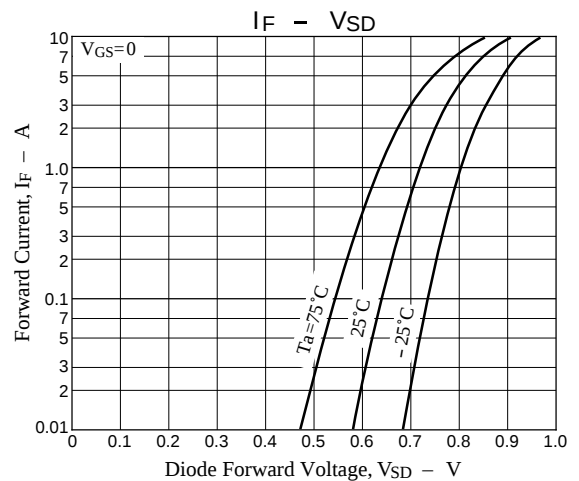
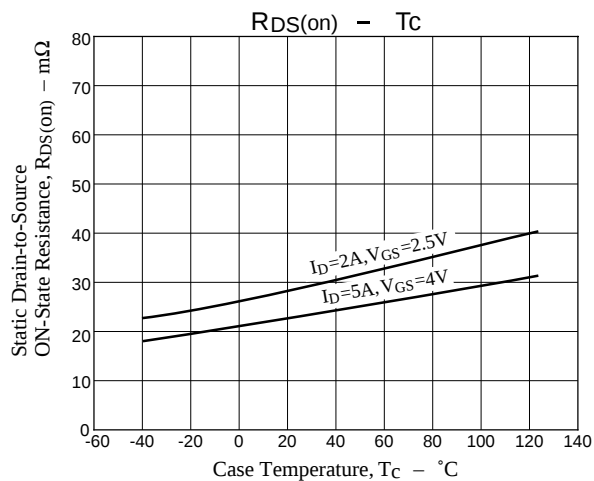
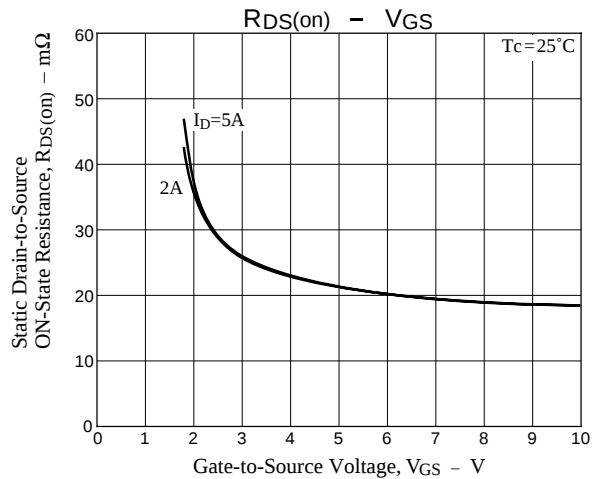
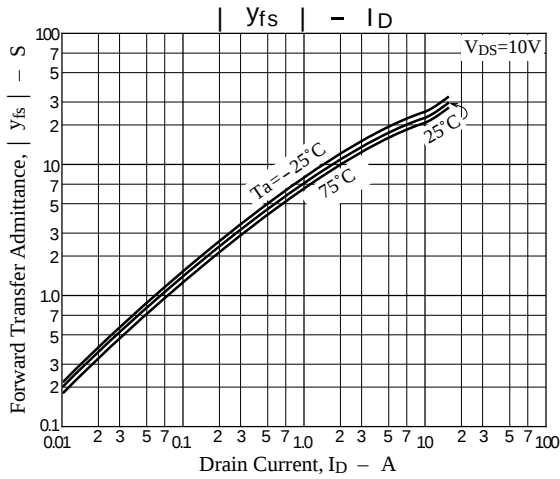
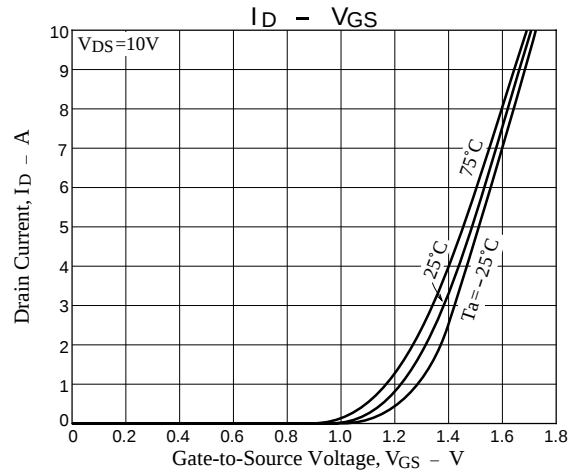
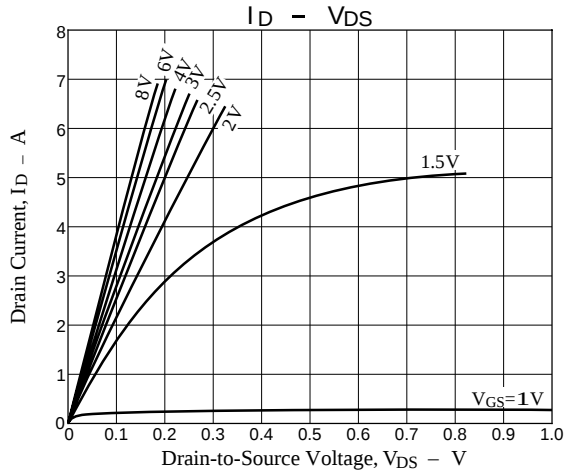
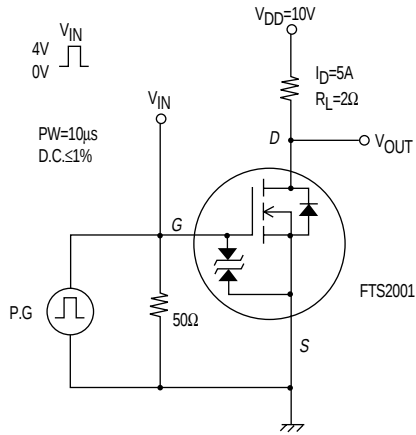
Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	V_{DSS}		20	V
Gate-to-Source Voltage	V_{GSS}		± 10	V
Drain Current (DC)	I_D		5	A
Drain Current (pulse)	I_{DP}	$PW \leq 10\mu\text{s}$, duty cycle $\leq 1\%$	30	A
Allowable Power Dissipation	P_D	Mounted on a ceramic board ($1000\text{mm}^2 \times 0.8\text{mm}$)	1.5	W
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to +150	$^\circ\text{C}$

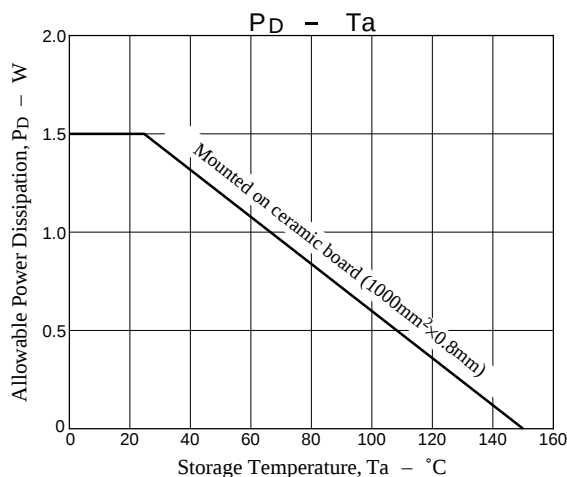
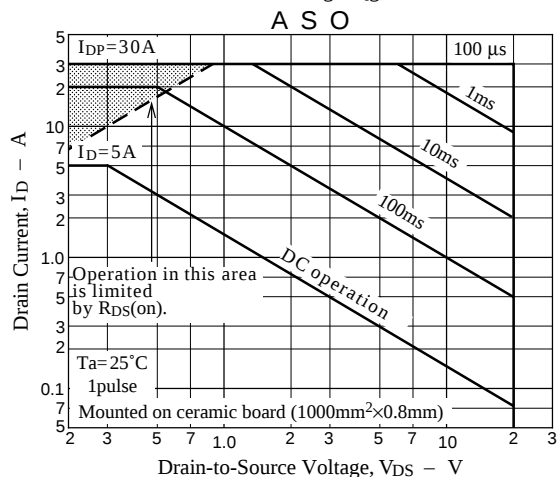
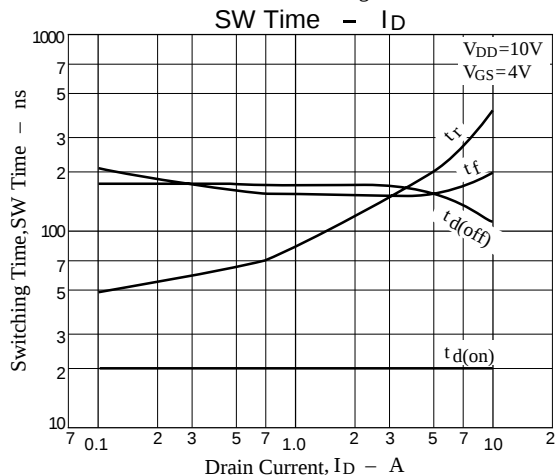
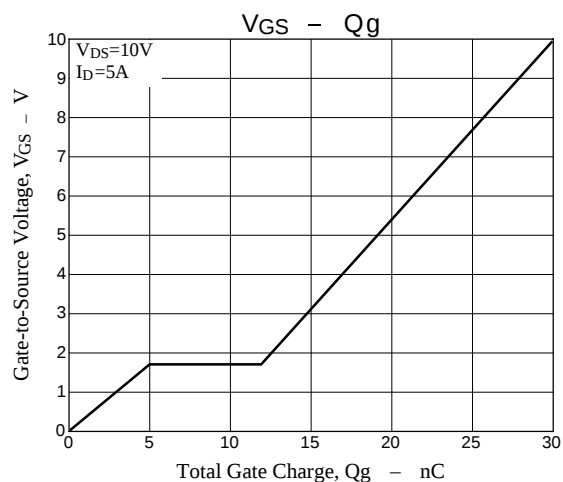
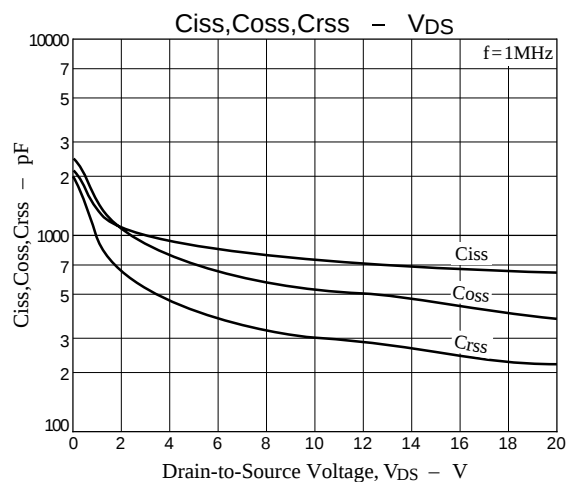
Electrical Characteristics at $T_a = 25^\circ\text{C}$

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1\text{mA}$, $V_{GS} = 0$	20			V
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 20\text{V}$, $V_{GS} = 0$			100	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{GS} = \pm 8\text{V}$, $V_{DS} = 0$			± 10	μA
Cutoff Voltage	$V_{GS(off)}$	$V_{DS} = 10\text{V}$, $I_D = 1\text{mA}$	0.4		1.3	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 10\text{V}$, $I_D = 5\text{A}$	9	15		S
Static Drain-to-Source ON-State Resistance	$R_{DS(on)1}$	$I_D = 5\text{A}$, $V_{GS} = 4\text{V}$		23	30	$\text{m}\Omega$
	$R_{DS(on)2}$	$I_D = 2\text{A}$, $V_{GS} = 2.5\text{V}$		32	46	$\text{m}\Omega$
Input Capacitance	C_{iss}	$V_{DS} = 10\text{V}$, $f = 1\text{MHz}$		750		pF
Output Capacitance	C_{oss}	$V_{DS} = 10\text{V}$, $f = 1\text{MHz}$		520		pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = 10\text{V}$, $f = 1\text{MHz}$		300		pF
Turn-ON Delay Time	$t_{d(on)}$	See Specified Test Circuit		20		ns
Rise Time	t_r	See Specified Test Circuit		200		ns
Turn-OFF Delay Time	$t_{d(off)}$	See Specified Test Circuit		150		ns
Fall Time	t_f	See Specified Test Circuit		150		ns
Total Gate Charge	Q_g	$V_{DS} = 10\text{V}$, $V_{GS} = 10\text{V}$, $I_D = 5\text{A}$		30		nC
Gate-to-Source "Miller" Charge	Q_{gs}			5		nC
Gate-to-Drain Charge	Q_{gd}			7		nC
Diode Forward Voltage	V_{SD}	$I_S = 5\text{A}$, $V_{GS} = 0$	1.0	1.2		V

FTS2001

Switching Time Test Circuit





■ No products described or contained herein are intended for use in surgical implants, life-support systems, aerospace equipment, nuclear power control systems, vehicles, disaster/crime-prevention equipment and the like, the failure of which may directly or indirectly cause injury, death or property loss.

■ Anyone purchasing any products described or contained herein for an above-mentioned use shall:

- ① Accept full responsibility and indemnify and defend SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors and all their officers and employees, jointly and severally, against any and all claims and litigation and all damages, cost and expenses associated with such use:
- ② Not impose any responsibility for any fault or negligence which may be cited in any such claim or litigation on SANYO ELECTRIC CO., LTD., its affiliates, subsidiaries and distributors or any of their officers and employees jointly or severally.

■ Information (including circuit diagrams and circuit parameters) herein is for example only; it is not guaranteed for volume production. SANYO believes information herein is accurate and reliable, but no guarantees are made or implied regarding its use or any infringements of intellectual property rights or other rights of third parties.