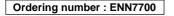
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SANYO Semiconductors DATA SHEET

N-Channel Silicon MOSFET FTS2022-- DC / DC Converter Applications

Features

- Ultrahigh-speed switching.
 Mounting height 1 1:

Specifications

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS	2.00	30	V
Gate-to-Source Voltage	VGSS	VD	±20	V
Drain Current (DC)	ID	±C.CU.	7	А
Drain Current (Pulse)	IDP	PW≤10µs, duty cycle≤1%	28	А
Allowable Power Dissipation	PD	Mounted on a ceramic board (1000mm ² X0.8mm) 1unit	1.3	W
Channel Temperature	Tch		150	°C
Storage Temperature	Tstg		-55 to +150	°C

Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	The second	Ratings		
Parameter			min	typ	max	Unit
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	30			V
Zero-Gate Voltage Drain Current	IDSS	VDS=30V, VGS=0			1	μΑ
Gate-to-Source Leakage Current	IGSS	V _{GS} =±16V, V _{DS} =0			±10	μA
Cutoff Voltage	VGS(off)	V _{DS} =10V, I _D =1mA	1.0		2.4	V
Forward Transfer Admittance	yfs	V _{DS} =10V, I _D =7A	7.7	11		S
Static Drain-to-Source On-State Resistance	R _{DS} (on) 1	ID=7A, VGS=10V		19	25	mΩ
	R _{DS} (on) 2	ID=4A, VGS=4.5V		27	38	mΩ
Input Capacitance	Ciss	V _{DS} =10V, f=1MHz		750	- 15	pF
Output Capacitance	Coss	V _{DS} =10V, f=1MHz	100	300		pF
Reverse Transfer Capacitance	Crss	V _{DS} =10V, f=1MHz	C	120	Le C.	pF

Marking : S2022

Continued on next page.

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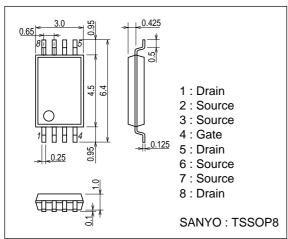
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Turn-ON Delay Time	t _d (on)	See specified Test Circuit.		10		ns
Rise Time	tr	See specified Test Circuit.		147		ns
Turn-OFF Delay Time	t _d (off)	See specified Test Circuit.		53		ns
Fall Time	tf	See specified Test Circuit.		58		ns
Total Gate Charge	Qg	V _{DS} =10V, V _{GS} =10V, I _D =7A		14		nC
Gate-to-Source Charge	Qgs	V _{DS} =10V, V _{GS} =10V, I _D =7A		2.5		nC
Gate-to-Drain "Miller" Charge	Qgd	V _{DS} =10V, V _{GS} =10V, I _D =7A		1.3		nC
Diode Forward Voltage	V _{SD}	IS=7A, VGS=0			1.2	V

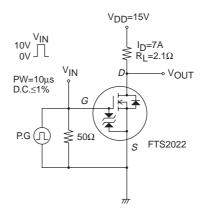
Package Dimensions

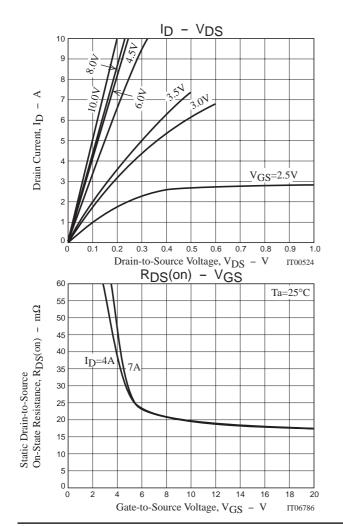
unit : mm

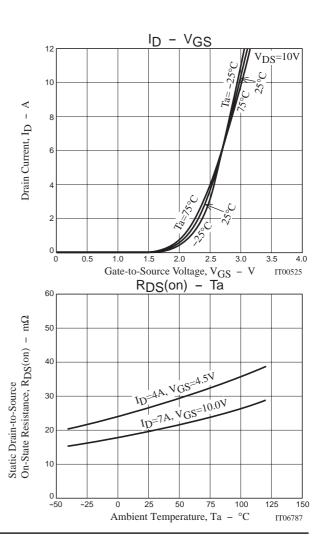
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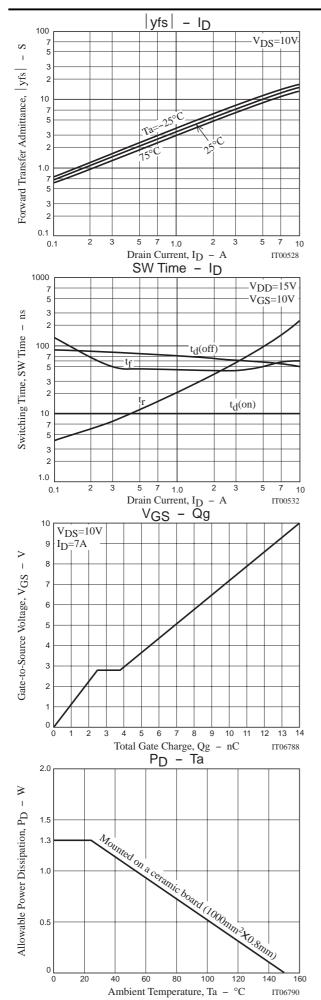
Switching Time Test Circuit

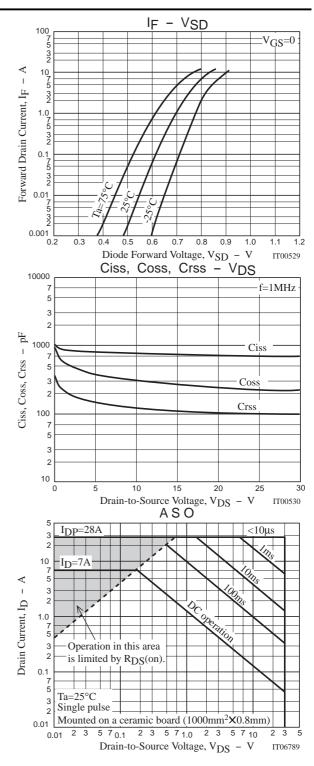






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