

GTM CORPORATION

ISSUED DATE :2005/05/20
REVISD DATE :

GD421SD

SURFACE MOUNT, SCHOTTKY BARRIER DIODE
VOLTAGE 40V, CURRENT 0.1A

Description

The GD421SD is designed for low power rectification.

Package Dimensions

SOD-323

Marking :

Circuit:

REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	0.85	1.05			
A1	0	0.10	L	0.20	0.40
A2	0.80	1.00	b	0.25	0.40
D	1.15	1.45	c	0.10	0.18
E	1.60	1.80			
HE	2.30	2.70	Q1	0.15 BSC.	

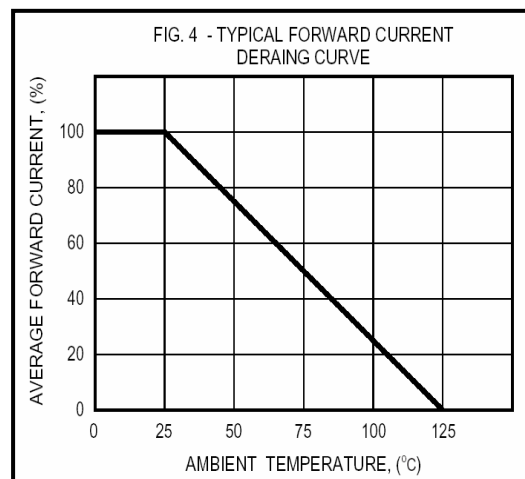
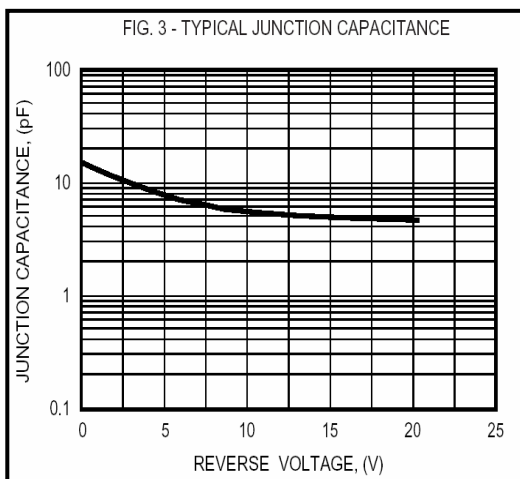
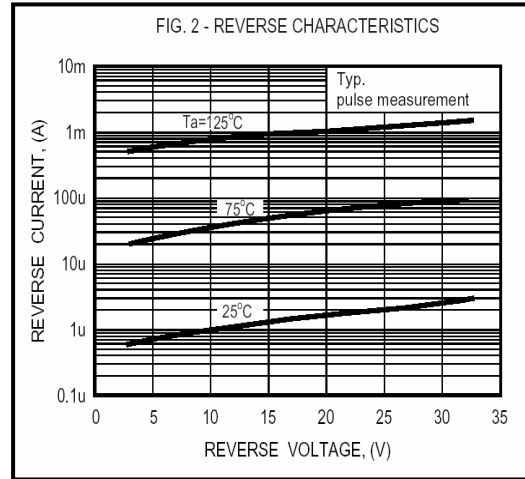
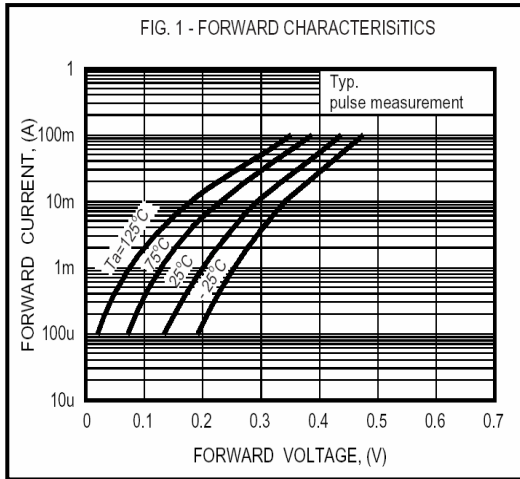
Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Ratings	Unit
Junction Temperature	T _J	+125	°C
Storage Temperature	T _{stg}	-40 ~ +125	°C
Maximum Recurrent Peak Reverse Voltage	V _{RRM}	40	V
Maximum RMS Voltage	V _{RPS}	28	V
Maximum DC Blocking Voltage	V _{DC}	40	V
Peak Forward Surge Current at 8.3mSec single half sine-wave	I _{FSM}	1.0	A
Typical Junction Capacitance between Terminal	C _J	6.0	pF
Maximum Average Forward Rectified Current	I _o	0.1	A
Total Power Dissipation	PD	225	mW

Characteristics at Ta = 25°C

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Reverse Breakdown Voltage	V _{(BR)R}	40	-	-	V	I _R =50μA
Forward Voltage	V _{F(1)}	-	-	340	mV	I _F =10mA
	V _{F(2)}	-	-	550	mV	I _F =100mA
Reverse Leakage Current	I _R	-	-	30	μA	V _R =10V

Characteristics Curve



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