

GTM CORPORATION

ISSUED DATE :2005/08/01
REVISED DATE :2005/09/29B

GSS4957

P-CHANNEL ENHANCEMENT MODE POWER MOSFET

BV _{DSS}	-30V
R _{DS(ON)}	24mΩ
I _D	-7.7A

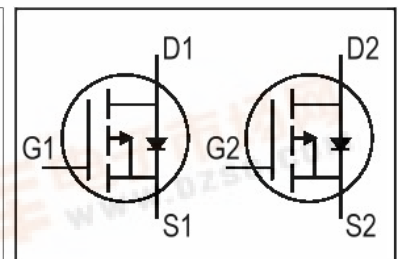
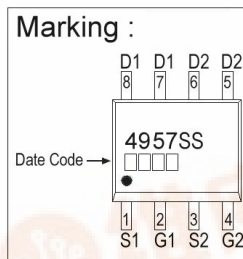
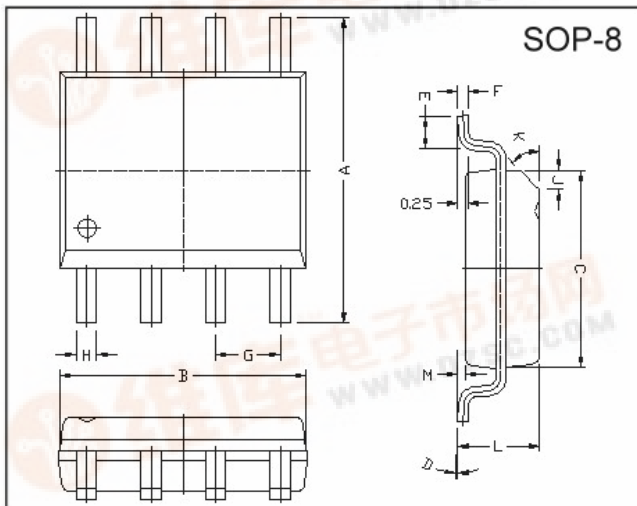
Description

The GSS4957 provide the designer with the best combination of fast switching, ruggedized device design, ultra low on-resistance and cost-effectiveness.

Features

- *Low On-Resistance
- *Simple Drive Requirement
- *Dual P MOSFET Package

Package Dimensions



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	5.80	6.20	M	0.10	0.25
B	4.80	5.00	H	0.35	0.49
C	3.80	4.00	L	1.35	1.75
D	0°	8°	J	0.375 REF.	
E	0.40	0.90	K	45°	
F	0.19	0.25	G	1.27 TYP.	

Absolute Maximum Ratings

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Continuous Drain Current ³	I _D @TA=25°C	-7.7	A
Continuous Drain Current ³	I _D @TA=70°C	-6.1	A
Pulsed Drain Current ¹	I _{DM}	-30	A
Total Power Dissipation	P _D @TA=25°C	2	W
Linear Derating Factor		0.016	W/°C
Operating Junction and Storage Temperature Range	T _j , T _{stg}	-55 ~ +150	°C

Thermal Data

Parameter	Symbol	Value	Unit
Thermal Resistance Junction-ambient ³ Max.	R _{thj-amb}	62.5	°C/W

Electrical Characteristics(Tj = 25°C Unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Drain-Source Breakdown Voltage	BV_{DSS}	-30	-	-	V	$V_{GS}=0, I_D=-250\mu A$
Breakdown Voltage Temperature Coefficient	$\Delta BV_{DSS} / \Delta T_j$	-	-0.02	-	V/°C	Reference to 25°C, $I_D=-1mA$
Gate Threshold Voltage	$V_{GS(th)}$	-1.0	-	-3.0	V	$V_{DS}=V_{GS}, I_D=-250\mu A$
Forward Transconductance	g_{fs}	-	12	-	S	$V_{DS}=-10V, I_D=-7A$
Gate-Source Leakage Current	I_{GSS}	-	-	± 100	nA	$V_{GS}= \pm 20V$
Drain-Source Leakage Current(Tj=25°C)	I_{DSS}	-	-	-1	μA	$V_{DS}=-30V, V_{GS}=0$
Drain-Source Leakage Current(Tj=70°C)		-	-	-25	μA	$V_{DS}=-24V, V_{GS}=0$
Static Drain-Source On-Resistance ²	$R_{DS(ON)}$	-	20	24	m Ω	$V_{GS}=-10V, I_D=-7A$
		-	30	36		$V_{GS}=-4.5V, I_D=-5A$
Total Gate Charge ²	Q_g	-	27	45	nC	$I_D=-7A$ $V_{DS}=-24V$ $V_{GS}=-4.5V$
Gate-Source Charge	Q_{gs}	-	5	-		
Gate-Drain ("Miller") Charge	Q_{gd}	-	18	-		
Turn-on Delay Time ²	$T_{d(on)}$	-	14	-	ns	$V_{DS}=-15V$ $I_D=-1A$ $V_{GS}=-10V$ $R_G=3.3\Omega$ $R_D=15\Omega$
Rise Time	T_r	-	11	-		
Turn-off Delay Time	$T_{d(off)}$	-	38	-		
Fall Time	T_f	-	25	-		
Input Capacitance	C_{iss}	-	1670	2670	pF	$V_{GS}=0V$ $V_{DS}=-25V$ $f=1.0MHz$
Output Capacitance	C_{oss}	-	530	-		
Reverse Transfer Capacitance	C_{rss}	-	435	-		
Gate Resistance	R_g	-	3	4.5	Ω	$f=1.0MHz$

Source-Drain Diode

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Forward On Voltage ²	V_{SD}	-	-	-1.2	V	$I_S=-1.7A, V_{GS}=0V, T_j=25^\circ C$
Reverse Recovery Time ²	T_{rr}	-	35	-	ns	$I_S=-7A, V_{GS}=0V$ $di/dt=100A/\mu s$
Reverse Recovery Charge	Q_{rr}	-	34	-	nC	

Notes: 1. Pulse width limited by Max. junction temperature.

2. Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.

3. Surface mounted on 1 in² copper pad of FR4 board; 135°C/W when mounted on Min. copper pad.

Characteristics Curve

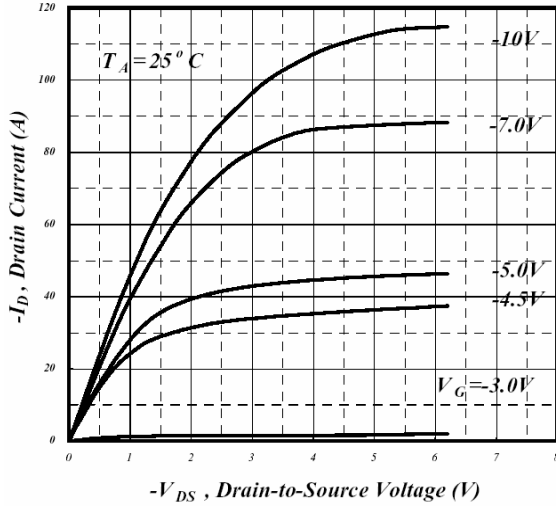


Fig 1. Typical Output Characteristics

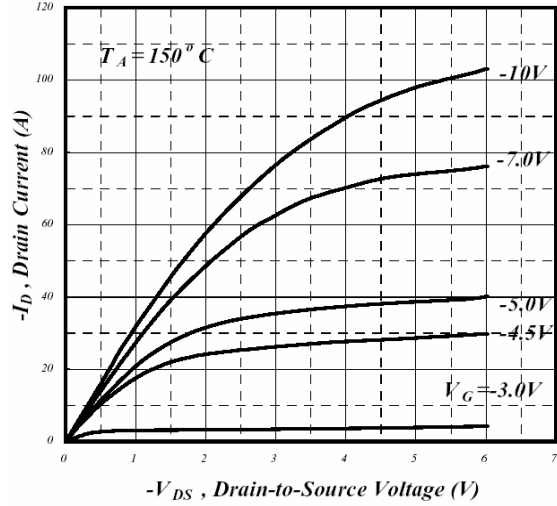


Fig 2. Typical Output Characteristics

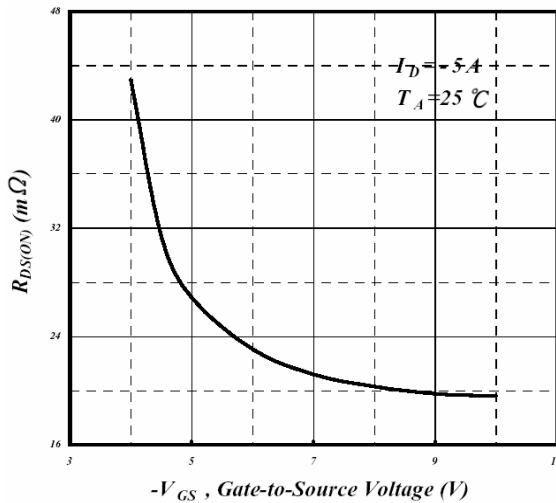


Fig 3. On-Resistance v.s. Gate Voltage

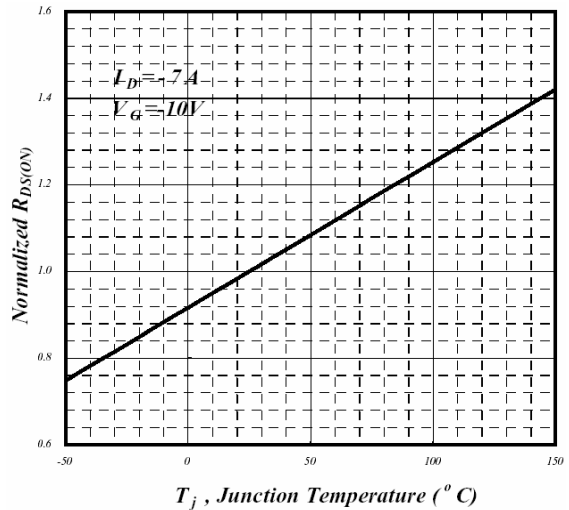


Fig 4. Normalized On-Resistance v.s. Junction Temperature

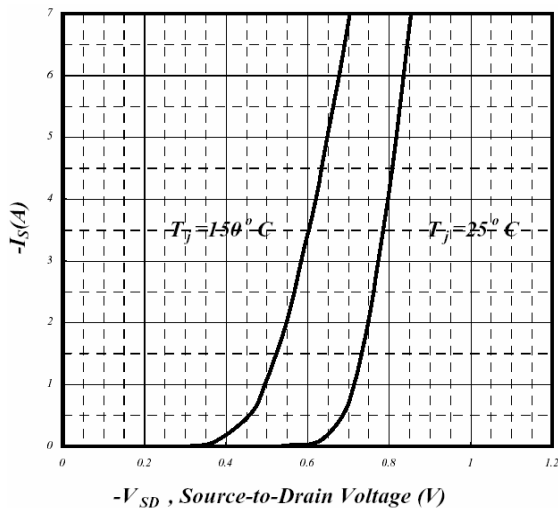


Fig 5. Forward Characteristics of Reverse Diode

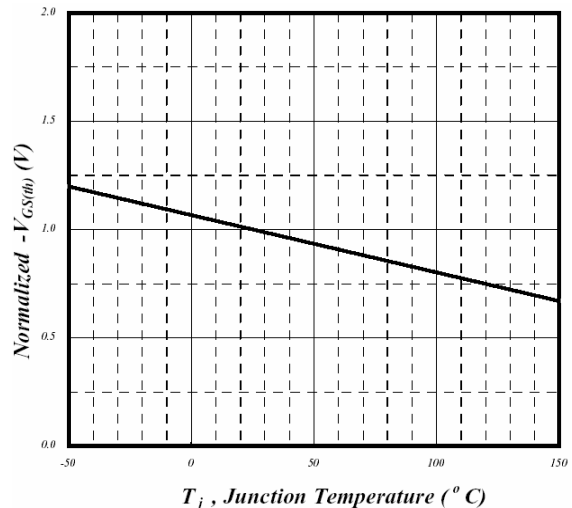


Fig 6. Gate Threshold Voltage v.s. Junction Temperature

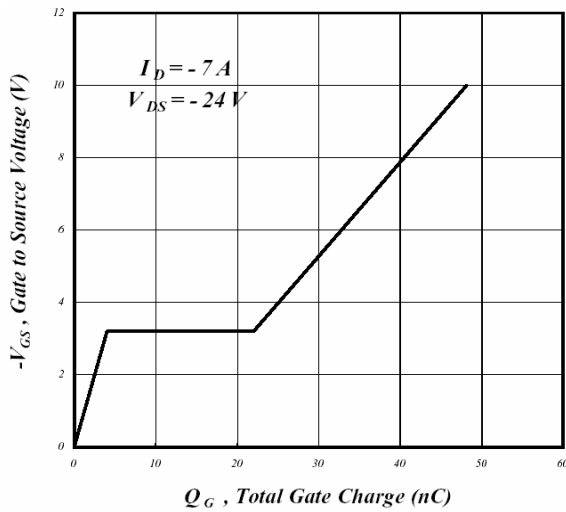


Fig 7. Gate Charge Characteristics

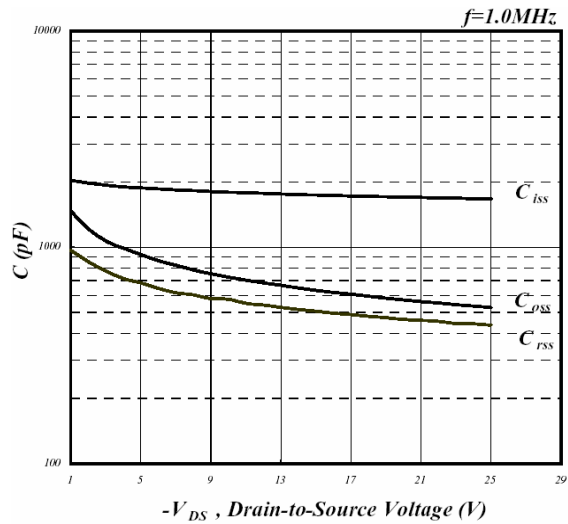


Fig 8. Typical Capacitance Characteristics

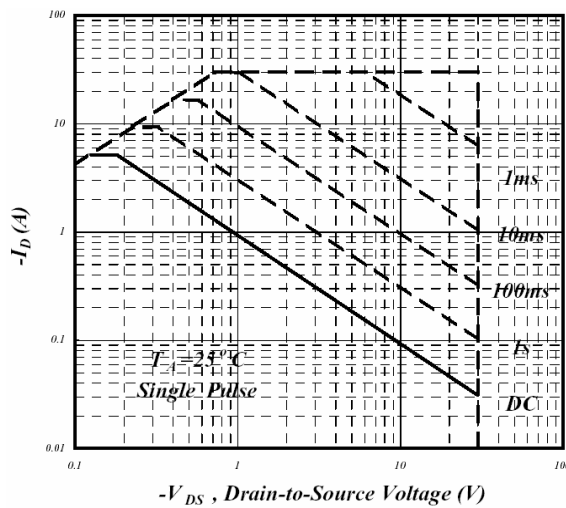


Fig 9. Maximum Safe Operating Area

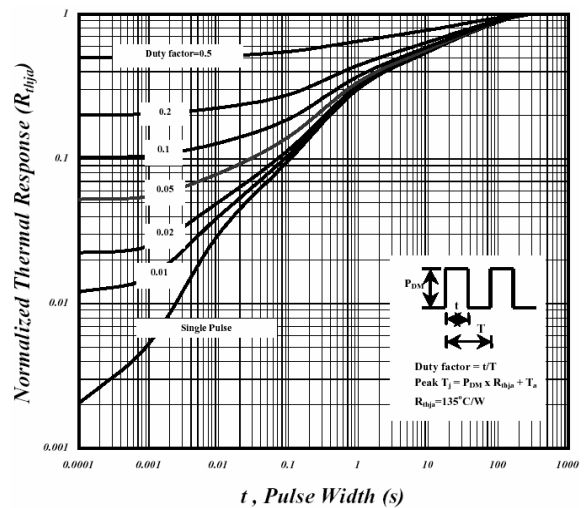


Fig 10. Effective Transient Thermal Impedance

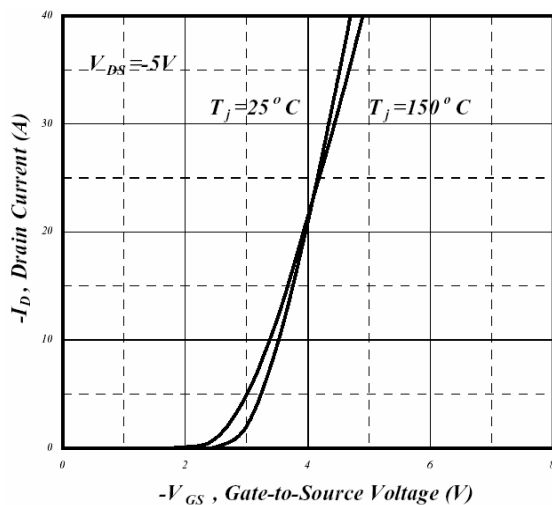


Fig 11. Transfer Characteristics

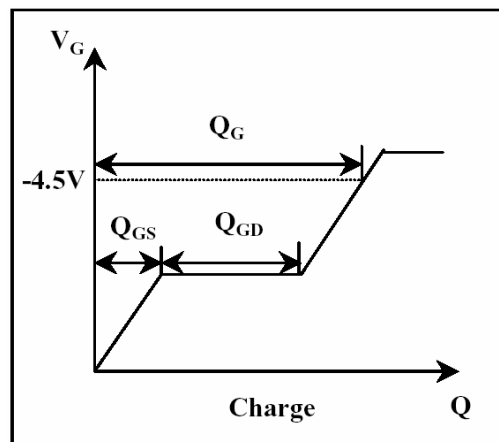


Fig 12. Gate Charge Waveform

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