To all our customers

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Renesas Technology Corp. Customer Support Dept. April 1, 2003





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Silicon P Channel Power MOS FET High Speed Power Switching

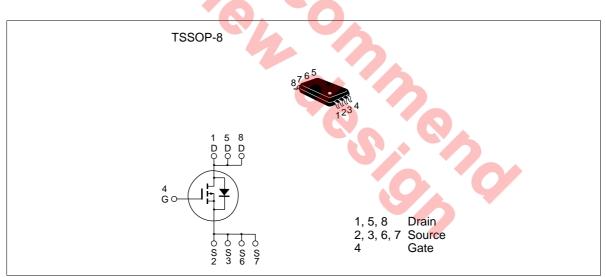


ADE-208-1238F (Z) 7th. Edition Jan. 2001

Features

- Low on-resistance
- Capable of 2.5 V gate drive
- Low drive current
- High density mounting

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	V _{DSS}	-20	V
Gate to source voltage	V _{GSS}	±12	V
Drain current	I _D	-6.0	А
Drain peak current	Note1 D(pulse)	-48	А
Body-drain diode reverse drain current	I _{DR}	-6.0	А
Channel dissipation	Pch Note2	1.3	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

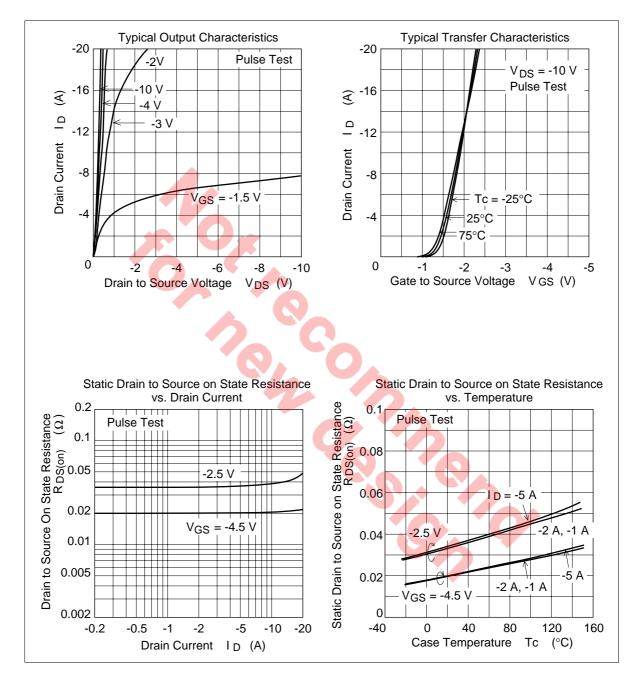
Note: 1. $PW \le 10\mu s$, $duty cycle \le 1 \%$

2. When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW≤10s

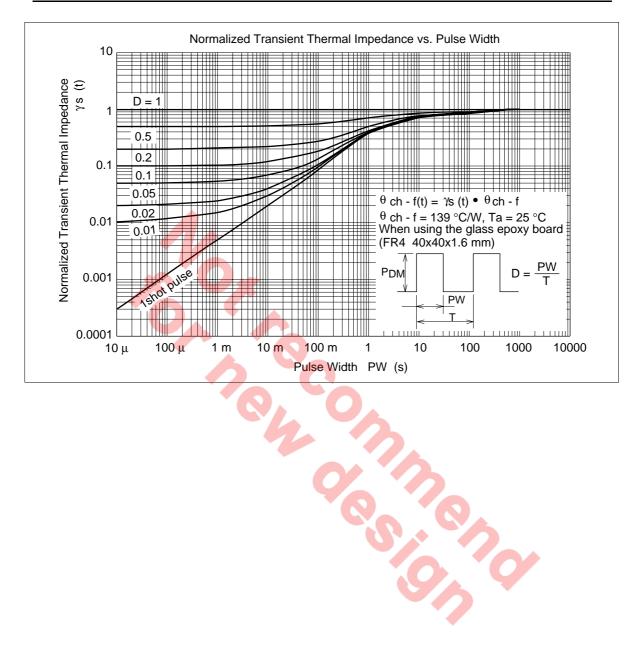
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	V _{(BR)DSS}	-20	0	_	V	$I_{\rm D}$ = -10mA, $V_{\rm GS}$ = 0
Gate to source leak current	I _{GSS}	-	_	±0.1	μA	$V_{GS} = \pm 12V$, $V_{DS} = 0$
Zero gate voltege drain current	I _{DSS}	4	_	1	μA	$V_{\rm DS} = -20 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-0.4		-1.4	V	$V_{DS} = -10V, I_{D} = -1mA$
Static drain to source on state	$R_{DS(on)}$		0.020	0.026	Ω	$I_{\rm D}$ =- 3A, $V_{\rm GS}$ =- 4.5V ^{Note3}
resistance	R _{DS(on)}	_	0.035	0.045	Ω	$I_{\rm D}$ = -3A, $V_{\rm GS}$ = -2.5V ^{Note3}
Forward transfer admittance	y _{fs}	6.5	11		S	$I_{\rm D} = -3A, V_{\rm DS} = -10V^{\rm Note3}$
Input capacitance	Ciss	_	1850	U)	рF	V _{DS} = -10V
Output capacitance	Coss		590	-	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	380	- 6	pF	f = 1MHz
Turn-on delay time	t _{d(on)}	_	30	_	ns	$V_{GS} = -4V, I_{D} = -3A$
Rise time	t,	_	145	_	ns	$V_{DD} \cong -10V$
Turn-off delay time	t _{d(off)}	_	210	_	ns	_
Fall time	t _f	_	170		ns	-
Body–drain diode forward voltage	V_{DF}	—	-0.85	-1.10	V	IF =-6.0A, $V_{GS} = 0^{Note3}$
Body–drain diode reverse recovery time	t _{rr}	_	70	_	ns	IF = -6.0A, V _{GS} = 0 diF/ dt =20A/μs
Nata: 0 Dulas test						

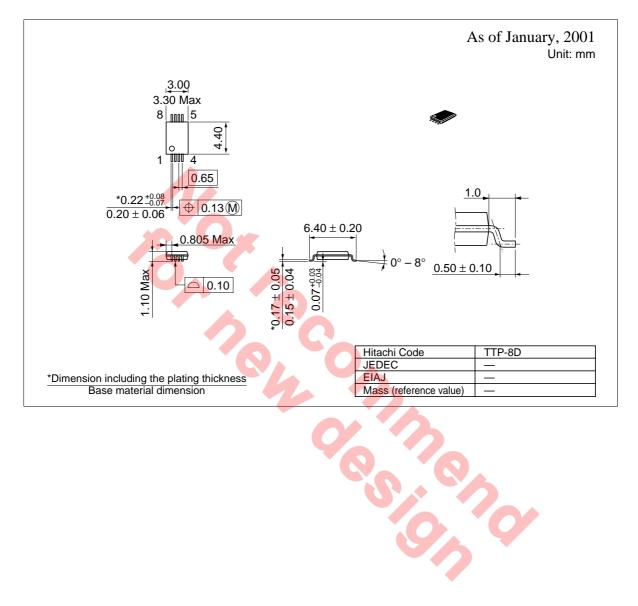
Note: 3. Pulse test



Main Characteristics



Package Dimensions



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