To all our customers

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DENIES

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# Silicon P Channel Power MOS FET Power Switching



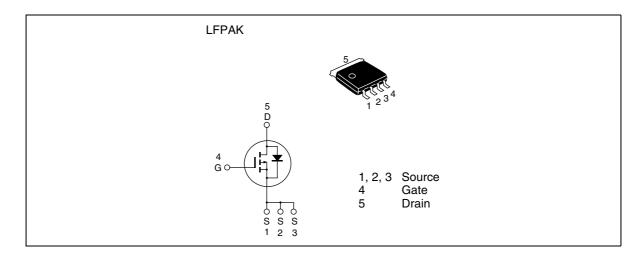
ADE-208-1534E (Z)

6th. Edition May 2002

#### **Features**

- Capable of -4.5 V gate drive
- Low drive current
- High density mounting
- Low on-resistance  $R_{_{DS(on)}} = 3.6 \text{ m}\Omega \text{ typ (at } V_{_{GS}} = -10 \text{ V})$

#### **Outline**



# **Absolute Maximum Ratings**

 $(Ta = 25^{\circ}C)$ 

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	-30	V	
Gate to source voltage	V <sub>GSS</sub>	-20/+10	V	
Drain current	I <sub>D</sub>	-40	А	
Drain peak current	Note1 D(pulse)	-160	A	
Body-drain diode reverse drain current	I <sub>DR</sub>	-40	Α	
Channel dissipation	Pch Note2	30	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	- 55 to + 150	°C	

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Tc=25°C

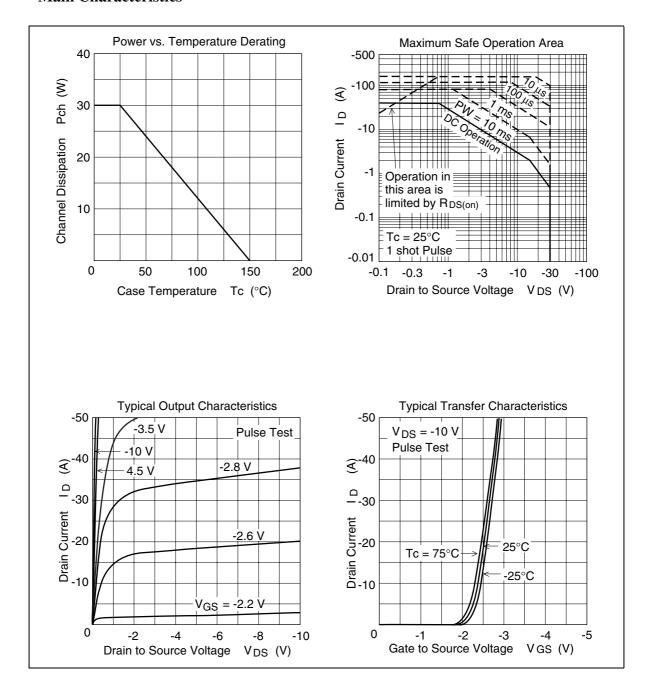
### **Electrical Characteristics**

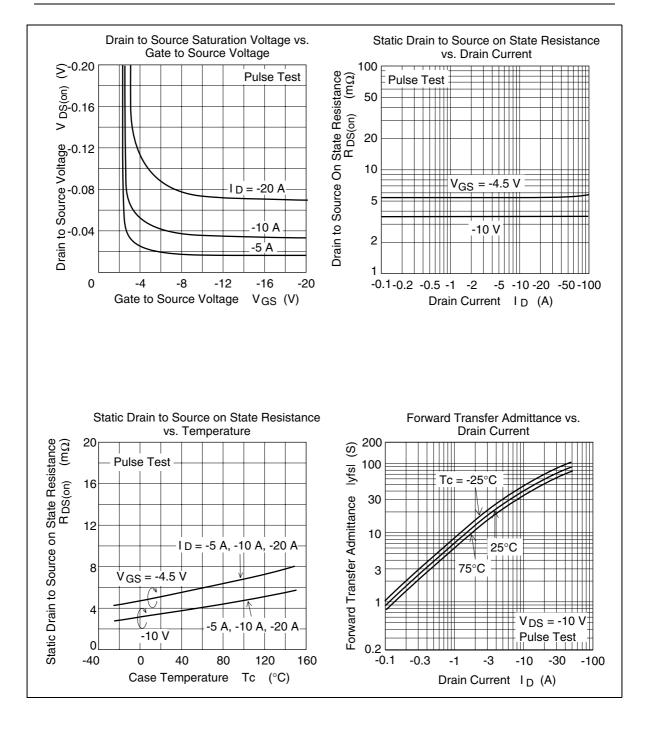
 $(Ta = 25^{\circ}C)$ 

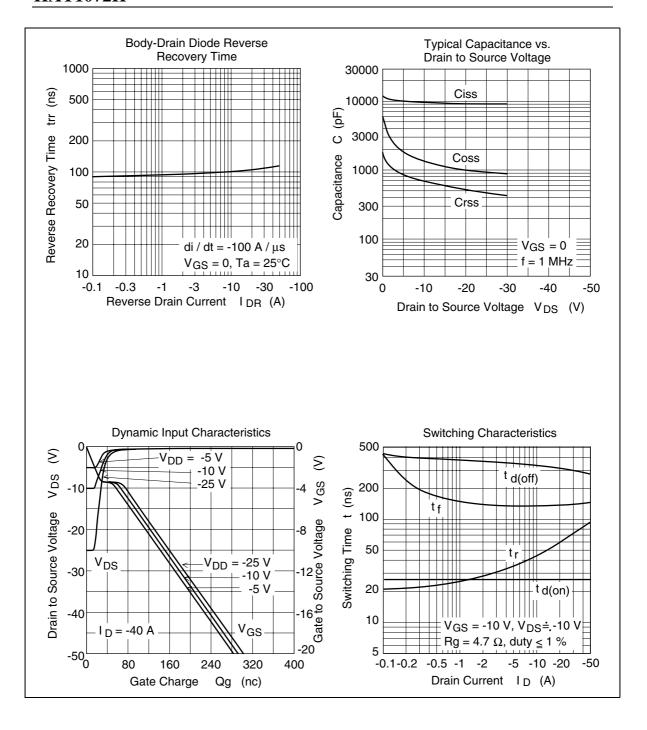
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{_{(BR)DSS}}$	-30	_	_	V	$I_{D} = -10 \text{ mA}, V_{GS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	± 0.1	μΑ	$V_{GS} = -20, +10 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	-0.5	_	-2.0	V	$V_{DS} = -10 \text{ V}, I_{D} = -1 \text{ mA}$
Static drain to source on state	R <sub>DS(on)</sub>	_	3.6	4.5	mΩ	$I_{D} = -20 \text{ A}, V_{GS} = -10 \text{ V}^{\text{Note3}}$
resistance	R <sub>DS(on)</sub>	_	5.3	7.7	mΩ	$I_D = -20 \text{ A}, V_{GS} = -4.5 \text{ V}^{Note3}$
Forward transfer admittance	ly <sub>fs</sub> l	36	60	_	S	$I_{D} = -20 \text{ A}, V_{DS} = -10 \text{ V}^{\text{Note3}}$
Input capacitance	Ciss	_	9500	_	pF	V <sub>DS</sub> = -10 V
Output capacitance	Coss	_	1300	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	700	_	pF	f = 1 MHz
Total gate charge	Qg	_	155	_	nc	V <sub>DD</sub> = -10 V
Gate to source charge	Qgs	_	28	_	nc	$V_{GS} = -10 \text{ V}$
Gate to drain charge	Qgd	_	26	_	nc	I <sub>D</sub> = -40 A
Turn-on delay time	t <sub>d(on)</sub>	_	28	_	ns	$V_{GS} = -10 \text{ V}, I_{D} = -20 \text{ A}$
Rise time	t <sub>r</sub>	_	60	_	ns	$V_{DD} \cong -10 \text{ V}$
Turn-off delay time	$\mathbf{t}_{d(off)}$	_	305	_	ns	$R_L = 0.5 \Omega$
Fall time	t,	_	140	_	ns	$Rg = 4.7 \Omega$
Body-drain diode forward voltage	$V_{DF}$	_	0.87	1.14	V	$IF = -40 \text{ A}, V_{GS} = 0^{\text{Note3}}$
Body-drain diode reverse recovery time	t <sub>rr</sub>	_	110	_	ns	IF = -40 A, $V_{GS} = 0$ diF/ dt = 100 A/ $\mu$ s

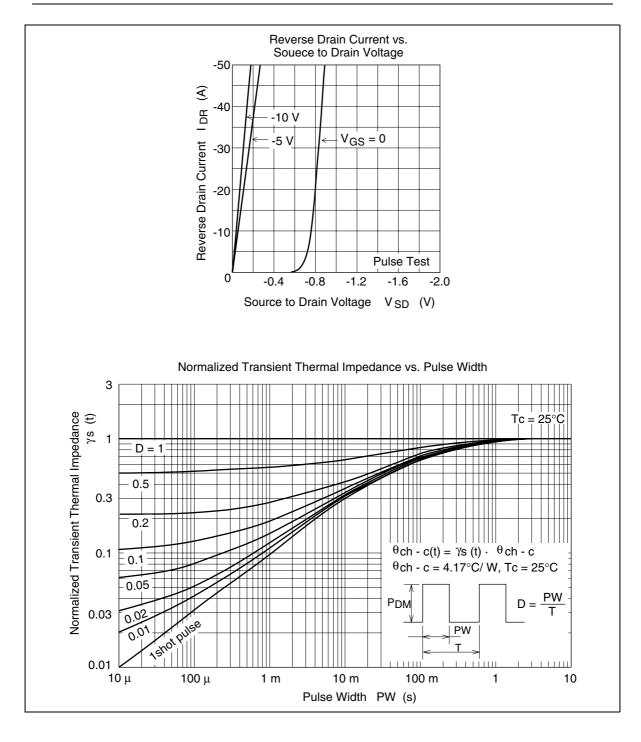
Notes: 3. Pulse test

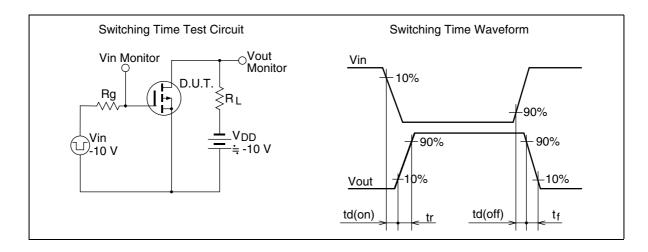
#### **Main Characteristics**



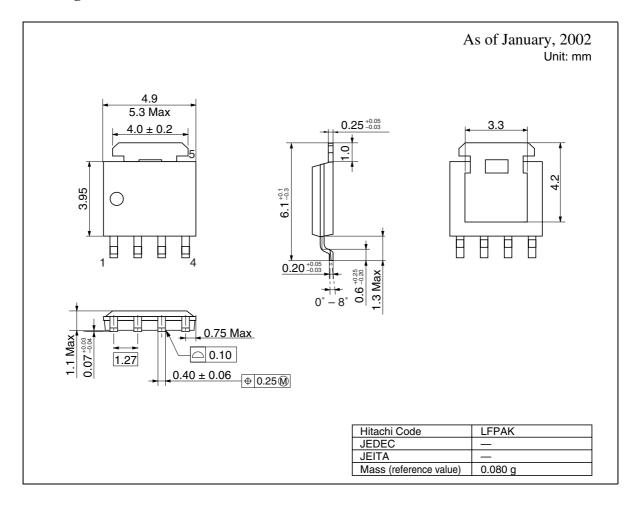








## **Package Dimensions**



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